

# SUMMER TRAINING PROGRAM ON VHDL PROGRAMMING

<b>COURSE CONTENT</b>	
<b>Duration:</b>	1Month
<b>Timing:</b>	Theory 1Hours (Daily)
	Practical 2Hours (Daily)
<b>DAYS</b>	<b>TOPICS</b>
Day 1	Introduction
Day 2	Code Structure (Fundamentals) Part #1
Day 3	Code Structure (Examples) Part #2
Day 4	Data Types (Fundamentals) Part #1
Day 5	Data Types Part(Examples) #2
Day 6	Operators and Attributes (Fundamentals) Part #1
Day 7	Operators and Attributes (Examples) Part #2
Day 8	Concurrent Code (Fundamentals) Part #1
Day 9	Concurrent Code (Examples) Part #1
Day 10	Sequential Code (Fundamentals) Part #1
Day 11	Sequential Code Part (Examples) #1
Day 12	Signals and Variables
Day 13	State Machines
Day 14	Package and Components (Fundamentals) Part #1
Day 15	Package and Components (Examples) Part #2
Day 16	Functions and Procedure (Fundamentals) Part #1
Day 17	Functions and Procedure (Examples) Part #2
Day 18	Additional Circuit and System Designs Examples
Day 19	Project 8-Bit ALU Design
Day 20	Project Continue . . .

---