FIRST SELECTION LIST OF STUDENTS

PG Program in VLSI SoC Design & Verification (Online) VS500

The following list of students, who are provisionally selected based on the application and proof of registration received until **4th April 2025** and should pay the fees for admission on or before 10th April 2025.

All candidates who are selected can remit 1st Installment fees as mentioned in *Fee Details* section below, and the details regarding remittance should be mailed to sree@calicut.nielit.in on or before **10-04-2025, 5:00PM** to confirm their seat.

Suggested to keep the mail subject as *"Fee Payment-PG Program in VLSI SoC Design & Verification- April 2025".*

Those who have applied for the course and not received any information regarding selection may send a mail to sree@calicut.nielit.in or call **9447769756** before 14-04-2025.

Provisionally Selected Candidates list (₹.1000/- paid candidates):

Sl. No	Name	Qualification
1.	MOGULURI ABHINAV SIDDARTHA	B.Tech
2.	ANITHA K B	B.Tech.
3.	DODDIPATLA NAGENDRA	B.Tech.
4.	DNYANESHWAR	M.Tech.
5.	PONNAGANTI VIJISRI	B.Tech.
6.	ARPITA CHANDRA	M.Tech.
7.	VIPIN KHANNA	M.Tech.
8.	PALEPU MANGARAJU	B.Tech.

Waitlisted candidates List who has not paid Registration Fee of ₹.1000/-

Sl No	Name
1.	PUSHPESH
2.	NIVED REDDY
3.	ABHISHEK KRISHNA
4.	G.SHARMILA
5.	MUDIMUKKALA SAIPRASAD
6.	DEEPJYOTI BORAH
7.	RITU SAHU
8.	KURUBA HANUMANTHARAYUDU GARI PAVITHRA
9.	ANITHA KB
10.	RANJNA SINGH
11.	ALLAKONDA SRIVARSHINI
12.	NEETHIMANI R
13.	ARGHA SINGHA
14.	AHMDULLAH

15.	BHAVANA KOUJALAGI
16.	HUMAIRA WASIM
17.	KALAPALA ANITHA
18.	ADITYA SHARMA
19.	ARUN SANJAY S
20.	ADITYA SHARMA
21.	THANGABAL M
22.	NEHA MOHANLAL JAIN
23.	NEMALA UDDEEP
24.	S SAJID AHAMED
25.	GUJJU KUSUMANJALI

1) Fee Details:

Fees	*Amount for General/OBC Candidates	Due Date (on or before)
Registration Fee	₹.1,000/-	*During Online Registration*
1st Installment #	₹.20,000/-	*On or before 14.04.2025*
2nd Installment	₹.19,000/-	*On or before 20.06.2025*
Total Fee*	₹.40,000/-	*On or before 20.06.2025*

   *Above fees is inclusive *GST* @actuals and revision if any will be applicable at the time of payment.

After publication of first selection list, the students in the first selection list have to pay the 1st Installment within the due date to take the provisional admission.

 # Those who has not paid the registration amount of ₹.1000/- needs to pay ₹ 21,000/-as First Installment.

Mode of payment:

   Payments can be done through the below link https://nielit.gov.in/calicut/content/mode-payments-0. The Institute will not be responsible for any mistakes done by either the bank concerned or by the depositor while remitting the amount into our account

PG Program in VLSI SoC Design and Verification –VS500 April 2025

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान, कालीकट
NIELIT | National Institute of Electronics & Information Technology, Calicut

Ministry of Electronics & Information Technology
Government of India

2) Admission:

Your admission will be confirmed after verification of certificates (online) and remitting the first installment of course fee on the counseling date **(14.04.2025). Please share the soft copy of the** *qualifying degree certificate* **and** *Aadhaar* **to the below mentioned email IDs of coordinator for verification.**

Coordinator

Mr. Sreejeesh SG
Senior Technical Officer
Mobile: 9447769756
Email: sree@calicut.nielit.in copy to **nanditha@calicut.nielit.in**