राष्ट्रीयइलेक्ट्रॉनिकीएवंसूचनाप्रौद्योगिकीसंस्थान(रा.इ.सू.प्रौ.सं.), कालिकट

NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY (NIELIT), CALICUT

(An Autonomous Scientific Society of Ministry of Electronics & Information Technology, Government of India)

NIT Campus PO, CALICUT - 673 601

①0495 2287266; Web: https://nielit.gov.in/calicut/index.php Advt. Ref.No.3(133)/97/Contract/RECTT/2024

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान (नाइलिट) कालीकट इलेक्ट्रॉनिकी, सूचना प्रौद्योगिकी तथा सूचना प्रौद्योगिकी के क्षेत्रों में शिक्षा, प्रशिक्षण, अनुसंधान एवं विकास तथा परामर्श के कार्य में संलग्न एक प्रमुख संगठन है। हम औपचारिक (एम.टेक) और गैर-औपचारिक (पीजी कार्यक्रम, अनुकूलित पाठ्यक्रम आदि) दोनों क्षेत्रों में प्रशिक्षण कार्यक्रम आयोजित करते हैं। हमें भारत सरकार के पाठ्यक्रमों/वित्त पोषित परियोजनाओं के लिए तदर्थ संकाय के रूप में काम करने के लिए अनुबंध के आधार पर निम्नलिखित कर्मचारियों की आवश्यकता है। वॉक-इन-इंटरव्यू 28.06.2024 को आयोजित किया जाएगा।

National Institute of Electronics and Information Technology (NIELIT) Calicut is a premier organization engaged in Education, Training, R&D and Consultancy in the areas of Electronics, IT and ITES. We conduct training programs both in the formal (M. Tech) and Non-formal (PG programs, customized courses etc.) sectors. We require the following staff on **contract basis** for working for the courses/funded projects of Govt. of India. The Walk-in-Interview will be held on **28.06.2024**.

NAME OF THE PROJECT: DRONE

Name of the post	Project Engineer				
No. of Post	02				
Qualification	Bachelor's Degree in Engineering or Technology or Master's Degree in Electronics from a recognized University or equivalent.				
Experience	Desirable: Relevant work experience of minimum 1 year or Master's Degree in ECE/EEE/AEI/CSE/IT/Robotics/Automation preferred.				
Preferred	Skills/Certification in allied technologies for UAS like in Data Analytics, AI/ML,				
skills/Certification	Cloud computing, Cyber security, Drone Design Automation, IoT will be an added advantage.				
Job Profile	 Development of high quality professional training content on UAS/Drone allied technologies like Data Analytics, AI/ML, Cloud computing, Cyber security, Drone Design Automation, IoT. Delivery of hands on training programs. Conduct of boot camps. Conduct of PG Diploma/Certificate programs. Identification & Procurement support of Lab Items Advanced Lab Setup for Drone/UAS allied technologies. Support for R& D for Proof of Concepts in Drone related areas. Technical Documentation. Experimentations for paper publications and filing of IPRs. Conduct of awareness programs at multiple institutions Support for Project management. Support for preparation of Utilization Certificate and reports. 				

	Liaison with Resource Centres like IITs/IISc/IIITs etc and with the Project			
	Management Units			
	 Any other activity being assigned from time to time. 			
Age	Upper Age limit: 40 years.			
Selection Process	Candidates will be selected based on their performance in Test/ Interview			
Duration of	6 months. Extendable up to 1 year			
engagement				
Remuneration CTC	₹45,000/- consolidated			
(per month)				

NAME OF THE PROJECT : SMART LAB

Name of the post	Lab Engineer				
No. of Posts	Two (VLSI – 01, Embedded Systems – 01)				
Qualification	B.E/ B.Tech in Electronics/ECE/EEE/CSE and allied branches				
	Desirable: M.E/M.Tech. in ECE/ EEE/CSE Or PG Diploma in VLSI/Embedded System				
Experience	Desirable: VLSI / Embedded System Domains.				
Preferred skills	 C Programming, Knowledge of Microcontrollers & its programming. Knowledge of Computer Networking, IoT, Working experience with Linux OS, RTOS etc. Good knowledge in Hardware Design & Verification using Verilog HDL/System Verilog, FPGA based SoC/Embedded System Design Strong fundamentals in Digital Electronics, Computer Architecture etc. Aptitude to learn and contribute towards development of top notch professional quality content for Instruction Enhancement Programs. 				
Job Profile	 To support activities related to SMART Lab Lab support in the area of VLSI, Embedded Systems, IoT etc. Development of case studies, mini project etc. in the area of VLSI, Embedded Systems, IoT. General Scope: Setting up of Industry Standard EDA tool chains, high end				
	FPGA Development setup, Setting up of Embedded tool chains, Low and high end Microcontroller development boards, Remote Hardware Lab provisioning, monitoring and management, Development & delivery of curated e-learning contents for Professional Training, dissemination of the information at the C2S website, outreach for mobilization of target beneficiaries, Contribution to R&D and Start-up Handholding activities, preparation of drafts for training reports, financial and technical documentations etc.				
Age	Below 40 Years				
Selection Process	Candidates will be selected based on their performance in Test/ Interview				
Duration of engagement	One year				
Remuneration CTC (per month)	₹35,000/- consolidated				

NAME OF THE PROJECT : Electronics & ICT Academy Scheme- Phase II

Name of the post	Lab Engineer				
No. of Posts	Three (Electrical/Electronics – 03)				
Qualification	B.E/ B.Tech in Electronics/ECE/EEE/CSE/AEI and allied branches				
Experience	Desirable: Electrical / Power Electronics Domains.				
Preferred skills	 C Programming, Knowledge of Microcontrollers & its programming. Knowledge of Computer Networking etc Good knowledge in Electronics / Electrical fundamentals / PCB Design etc. Aptitude to learn and contribute towards development of top notch professional quality content for Instruction Enhancement Programs. 				
Job Profile	To support activities related to FDPs • Lab support in the area of Electronics /EV / Solar / Embedded etc. • Development of case studies, mini project etc. in the above areas. General Scope: Development & delivery of curated e-learning contents for Professional Training. Outreach for mobilization of target beneficiaries, Preparation of drafts for training reports, financial and technical documentations etc.				
Age	Below 40 Years				
Selection Process	Candidates will be selected based on their performance in Test/ Interview				
Duration of engagement	One year				
Remuneration CTC (per month)	₹30,000/- consolidated				

Interested candidates may appear for a Walk-in-Interview at our Institute on **28.06.2024** at **09.00 AM sharp.**

Candidates should bring with them all original documents in support of their age, qualifications, experience, Identity and address proof etc. along with a copy of signed Bio-data. Candidates who have the relevant qualification required for the above post **only** need to appear for the selection.

General Instructions are as follows.

राष्ट्रीयइलेक्ट्रॉनिकीएवंसूचनाप्रौद्योगिकीसंस्थान(रा.इ.सू.प्रौ.सं.), कालिकट

NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY (NIELIT), CALICUT

(An Autonomous Scientific Society of Ministry of Electronics & Information Technology, Government of India)

NIT Campus PO, CALICUT - 673 601

10495 2287266; Web: https://nielit.gov.in/calicut/index.php

Advt. Ref.No.3(133)/97/Contract/RECTT/2024

GENERAL INSTRUCTIONS

- 1. Only Indian nationals are eligible.
- 2. Canvassing in any form will be a disqualification.
- 3. No interim enquiry / correspondence, whatsoever, will be entertained regarding the recruitment.
- 4. Candidates should bring with them their bio data, the original certificates, and one copy of qualification proof and date of birth.
- 5. NIELIT Centre Calicut reserves the right to fill or not to fill the post advertised.
- 6. There will be no application fee required to be paid.
- 7. Candidates with relevant experience will be preferred. However, the same may be relaxed in deserving cases.
- 8. The vacancies notified are tentative and are subject to change
- 9. Experience is counted only for the period after obtaining the eligibility qualification.
- 10. Salary will be based on qualification, experience, and performance in the interview.
- 11. The Institute will have the right to fix criteria for screening the applications so as to reduce the number of candidates to be called for interview.
- 12. Mere fulfilling of requirements as laid down in the advertisement does not qualify a candidate for interview.
- 13. No TA/DA will be provided to attend the interview.
- 14. This selection is only for contract engagement. The selected candidate(s) cannot claim regularisation in any of the positions at the centre.

&&&&&&&&