

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान (रा.इ.सू.प्रौ.सं.), कलिकट

**NATIONAL INSTITUTE OF ELECTRONICS AND
INFORMATION TECHNOLOGY (NIELIT), CALICUT**

(An Autonomous Scientific Society of Ministry of Electronics & Information Technology,
Government of India)

NIT Campus PO, CALICUT - 673 601

☎ **0495 2287266; Web: <http://nielit.gov.in/calicut>**

National Institute of Electronics and Information Technology (NIELIT) Calicut is a premier organization engaged in Education, Training, R&D and Consultancy in the areas of Electronics, IT and ITES. We conduct courses both in the formal (M. Tech) and Non-formal (PG Diplomas and customized courses etc.) sectors. Established as Centre for Electronics Design and Technology (CEDT) during 1989, the Centre has undertaken number of R&D projects and product development. Recognized research centre of BEL, Bangalore. We require the following staff on contract basis for the Special Manpower Development Programme Chip to Start up (SMDP-C2S).

NAME OF THE POST : SENIOR PROJECT ENGINEER

No. of Post	One
Qualification	Essential : M.E/M.Tech (ECE) preferably with a specialization in VLSI/Embedded Systems
Preferred skills	VLSI/Embedded Systems Lab Infrastructure Management. EDA Tool Set Up and Configurations. Support for Instructional Design Including Experiments & Hands on Training Modules Delivery of Hands on Training Programs Organizing Workshops and remote and onsite training Programs. Liaisoning with Project Coordination Institute Ability for Technical Documentation Knowledge about procurement process in Government sector and familiarity with Government e Marketplace (GeM) Ability to support purchase procedures Good communication and teaching skills
Age	Up to 35 years
Selection Process	Candidates will be selected based on their Performance in the Interview
Duration of engagement	6 months/project period whichever is earlier
Remuneration	Up to Rs.30,000/- per month

WALK-IN-INTERVIEW

Date : 29.06.2022
Time : 9.00 AM
Venue : NIELIT, Calicut

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NAME OF THE POST: PROJECT ASSISTANT

No. of Post	One
Qualification	Essential : B.Tech. in ECE/EEE/CSE and allied branches or B.Sc/M.Sc in Electronics/CSE/IT
Preferred skills	Good communication and teaching skills Ability to Support Procurements under the Project. Knowledge of Purchase activities related to Government e Marketplace (GeM) Ability to Support Lab Management. Ability to Support Instructional Design Ability to Support Delivery of Training Programs Ability for technical documentation
Age	Up to 35 years
Selection Process	Candidates will be selected based on their Performance in the Interview
Duration of engagement	6 months/project period whichever is earlier
Remuneration	Up to Rs.20,000/- per month

No TA/DA for attending the Walk-in-Interview. Interested candidates may appear for a Walk-in-Interview at our Institute on **29.06.2022 at 09.00 AM sharp.**

Candidates should bring with them all original documents in support of their age, qualifications, experience, Identity and address proof etc. along with a copy of signed Bio-data. Candidates who have the relevant qualification required for the above post only need to attend the interview.

WALK-IN-INTERVIEW

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