

# SCHEDULE

Advt. No. NHQ-12/3/2025-NC (3151933)

Date	Position	Schedule	
25 <sup>th</sup> -Feb-2025 (Tuesday)	1) Consultant 2) Project Coordinator 3) Assistant Project Coordinator 4) Senior Consultant (VLSI Design) 5) Consultant (Business Development)	<b>Document Verification</b>	9:30 AM - 12:00 PM
	6) Consultant (International Cooperation) 7) Consultant (Projects) 8) Senior Resource Person (Business Development) 9) Consultant (Academics and Skilling) 10) Senior Resource Person (Academics) 11) Resource Person	<b>Interview</b>	11:00 AM onwards

## Venue for Document Verification and Interview:

National Institute of Electronics & Information Technology  
NIELIT Bhawan, Plot No. 3, PSP Pocket,  
Sector-8, Dwarka, New Delhi-110077

## Important Instructions

1. Applications without Application Fee shall be rejected.
2. Applicants are requested to ensure their presence on 25-Feb-2025 as per above-mentioned schedule.
3. All Applicants are requested to bring their Original Documents for Document Verification.
4. Applicants who clear Document Verification will be called for Interview.
5. Applicants who clear Document Verification and Interview may be deployed as per the requirement of NIELIT.
6. Applicants are requested to visit the website <https://nielit.gov.in/recruitments> for updates.