

# NATIONAL INSTITUTE OF ELECTRONICS & INFORMATION TECHNOLOGY (NIELIT, Delhi Centre)

An Autonomous Scientific Society of Department of Electronics &  
Information Technology (DeitY),  
Ministry of Communications and Information Technology,  
Government of India

## TENDER DOCUMENT

FOR

Supply of Embedded System Kits



NIELIT, DELHI CENTRE  
IInd Floor, Parsvnath Metro Mall  
Inderlok Metro Station,  
Inderlok, Delhi - 110052  
Phone – 011-23644149, 23652370 Fax : 011-23655083

[www.delhi.nielit.gov.in](http://www.delhi.nielit.gov.in)

# NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY

## DELHI CENTRE

### 1. Invitation of bids for purchase of Embedded Systems Kits (For Academic Use):

NIELIT, Delhi Centre intends to purchase 4 Nos. of Embedded Systems Kits for imparting technical training to students pursuing Engg. and other Electronics based courses. The proposed Embedded Systems Kits will be installed in the Centre premises (Address mentioned on Page-1 of the document).

### 2. Scope for supply of Embedded Systems Kits

The scope of work of the Bidder with respect to the Embedded Systems Kits and its installation is given below:-

- i) Supply and Installation of Embedded Systems Kits at NIELIT Delhi Centre as per the technical specifications included in this tender document, which includes transporting and delivery of the item safely to the location.
- ii) The Embedded Systems Kits must be delivered within 2 weeks from the date of issue of the purchase order. Penalty for late delivery would be levied at the rate of 0.5% per week subject to a maximum of 10 percent of the value of such portion of material as has not been supplied within the stipulated period. In case delivery is not completed within 2 weeks after specified delivery period, NIELIT may cancel the order and arrange to procure the said Embedded Systems Kits from any other source at the risk and cost of the Bidder and Earnest Money Deposit (EMD) will be forfeited.
- iii) Provide after sales support services for a period of 6 months.

### 3. **GENERAL INSTRUCTIONS**

- i. The Bidder will be required to send bids in two separate envelopes. The First envelop will contain Earnest money deposit, Technical Bid Performa (Annexure A), Technical Literature and signed copy of the tender document as a proof of reading and accepting the terms and conditions of the tender document.
- ii. The Second envelop will contain only the financial bid (Annexure B) showing the rates. Both the bids should be sealed in separate covers, super scribed with the statements "Technical Bid" and "Financial Bid" and both these covers should then be sealed in a bigger envelope super scribed in bold letters as **"Bids for Supply of Embedded Systems Kits"** & **"NOT TO BE OPENED BEFORE 2.00 PM, 3rd August, 2015"**
- iii. The last date of submission of the bid is **3rd August, 2015, 2.00 PM.**
- iv. Qualification and Technical Bids will be opened on 3rd August, 2015 at 2.00 p.m. in the presence of the Bidders who wish to be present at the time of opening the bids. The Financial Bids will also be opened the same day after completion of technical bid process. While opening the quotations, the envelopes containing Technical Bid and Terms & Conditions shall be opened first for ascertaining the acceptance of the terms

of tender. The Evaluation Committee will evaluate the Technical Bids. Financial Bid of only those bidders will be opened who qualify the eligibility criteria on the basis of Pre-Qualification and Technical Bid. In case of any Technical clarification please contact at Email\_Id: [soodksanjeev@nielit.gov.in](mailto:soodksanjeev@nielit.gov.in).

- v. The bidder will supply material strictly as per specification mentioned in the tender document. Any deviation from prescribed specs shall be clearly mentioned in the proposal.
- vi. The quotations should be sent by post or by hand only and should be addressed to : The Director, NIELIT Delhi Centre, 2<sup>nd</sup> floor, Parsvnath Metro Mall, Inderlok , New Delhi-110052.
- vii. Every envelope should be properly sealed and stamped by the bidder. Any deviation from it shall lead to cancellation of bid.
- viii. Sealed quotations, complete in all respects, should reach NIELIT Delhi Centre, “Parsvnath Metro Mall, 2<sup>nd</sup> Floor, Inderlok, Delhi-110052” latest by **3rd August, 2015 at 2.00 p.m.** Incomplete quotations or those without earnest money or bids received after closing date and time will be rejected. Technical bids will be opened on the same day at 2.00 p.m. at **NIELIT Delhi Centre** in the presence of the Bidders or their representative who may wish to be present at the time of opening of the Technical Bids. Financial bids will be opened the same day (**3rd August, 2015 at 4.00 p.m.**).

#### **B. PRE-QUALIFICATION/ELIGIBILITY CRITERIA (Annexure-A)**

The Bidders fulfilling the following criteria will be eligible to qualify for opening of the Financial Bid (by furnishing the information showing proof for all the criteria listed below):

- i) Bidder should be in business of supply of Electronic kits for at least 3 years.
- ii) The Bidder should have valid authorization from OEM (KEIL) to supply the Embedded Systems Kits and would submit a copy of authorization to participate in this tender on behalf of OEM.
- iii) Average Annual turnover should be Rs. 1.00 cr. in the last 3 years.
- iv) Should have valid PAN and Sales Tax Registration number.
- v) Should not have been black-listed by any of the State/Central Govt. Organization. An affidavit is to be attached in the prescribed format enclosed at Annexure-D.
- vi) Should have enclosed **Earnest Money equivalent to 10% of Bid Value** in the form of Demand Draft in favour of Director, NIELIT Delhi Centre. Bids without Earnest money deposit will not be accepted.
- vii) Should have adequate number of trained engineers (minimum Diploma in Computer Science/IT/Electronics) to provide maintenance support services. Details to be provided.
- viii) The Bidder shall enclose the documentary evidence as a fulfilment of above criteria and all the documents attached along with Annexure-A will form part of Pre-qualification cum Technical Bid.
- ix) A Performa is attached at Annexure-A. Duly filled Performa along with copy of the tender document, duly signed and stamped on each page must be attached with the pre-qualification bid as acceptance of the above terms and conditions.

**C. TECHNICAL BID: (Annexure-A)**

- a) Bidder will attach copies of literature along with Annexure-B.
- b) Bidder will clearly mention all the deviations from technical specifications mentioned in the tender document.
- c) NIELIT Delhi Centre will accept the supply of complete material in all respect in one lot at its office address.
- d) NIELIT Delhi Centre will conduct the acceptance test(s) on the Embedded Systems Kits ordered. If the Embedded Systems Kits supplied fails in the acceptance test(s), the same will be rejected. Replacement of cards or devices found faulty during such test may not be allowed and the Bidder will have to supply new kits in replacement within a week's time.
- e) The acceptance test will consist of running the Systems for 3 hours daily for full two working days.
- f) Bidder will be responsible for comprehensive maintenance of Embedded Systems Kits for six months, free of charge, during warranty period after the acceptance of supplied material. In case of default, this Centre will have the right to arrange maintenance at Bidder's risk and cost from any other source.
- g) Comprehensive maintenance will cover replacement of all defective parts/equipment, excluding nothing.
- h) The Bidder must specifically mention the place, contact address, mobile number from where support for maintenance would be offered.
- i) Bidder will be required to provide paper license of certification of originality in the name of NIELIT Delhi Centre for all kits. Bidder should enclose printed brochure of the Embedded Systems Kits quoted along with the technical bid.

**D. FINANCIAL BID: (Annexure-C)**

- a) A Performa is enclosed as Annexure-C for filling details. Bidder will fill this performa and enclose it in a separate sealed envelope.
- b) Prices quoted should be F.O.R. NIELIT Delhi Centre **inclusive of all charges** like sales Tax, VAT, excise duty, insurance, technical service/installation charges. Octroi as per actual shall be payable extra. No other charges/taxes/levies shall be payable. The rates quoted shall be for academic license as the kits will be used for academic purpose only.
- c) **PAYMENT TERMS**  
The terms of payment will be as follows:
  - i) No advance payment against purchase order will be made.
  - ii) 90% of the bill amount will be paid within 15 days of submission of the bill along with reports showing successful delivery, Installation & satisfactory functioning of Software.
  - iii) Balance 10% of the value of the bill amount will be paid against Pay Order/FDR/Bank Guaranty in favour of NIELIT Delhi Centre towards performance security within 30 days after submission of Performance Security through any of the aforesaid instruments the satisfactory functioning report signed by the designated officer of NIELIT, Delhi Centre. The Pay Order/FDR/BG should remain valid for a period of sixty days beyond the date of completion of all contractual obligations of the supplier including warranty obligation.
  - iv) Form 'C' / 'D' will not be provided by NIELIT, Delhi Centre.

#### **E. OTHER TERMS AND CONDITIONS:**

- i) Director, NIELIT Delhi Centre, reserves the right to reject any or all tenders/ quotations without assigning any reason.
- ii) Director, NIELIT Delhi Centre reserves the right to revise or alter the requirements and/or specifications of the material before acceptance of any quotation and call for offers.
- iii) If a dispute arises out of or in connection with the contract, or in respect of any defined legal relationship associated therewith or derived there from, the parties agree to submit that dispute to arbitration under the ICADR Arbitration Rules, 1996. The Authority to appoint the arbitrator(s) shall be the International Centre for Alternative Dispute Resolution. The International Centre for Alternative Dispute Resolution will provide administrative services in accordance with the ICADR Arbitration Rules, 1996. The place of arbitration proceedings shall be New Delhi
- iv) In all matters and disputes arising hereunder, the appropriate Courts at Delhi alone shall have jurisdiction to entertain and try them.
- v) Non-compliance of the above terms and conditions will render the bid liable for rejection.

#### **F. EVALUATION PROCEDURE**

- i) The following evaluation procedure shall be followed for selecting the Bidder:  
Pre-Eligibility & Technical bid of all bids received till due time and on due date will be opened for verification of documents and evaluated as per the Technical specifications required.
- ii) Financial bids of only those Bidders will be opened and evaluated who are technically qualified.
- iii) Bidder will be selected for supply of material on the basis of lowest quote for the Embedded Systems Kits

**TECHNICAL BID FOR SUPPLY AND INSTALLATION OF EMBEDDED SYSTEMS  
KITS**

**PLEASE COMPLETE THE FORM AND ENCLOSE NECESSARY DOCUMENTS  
WHERE REQUIRED OTHERWISE THE QUOTATION WILL BE REJECTED**

**PERFORMA FOR PRE-QUALIFICATION**

BID PARTICULARS & QUOTATION NO. : \_\_\_\_\_

1. Name of the Bidder : \_\_\_\_\_

2. Address of the Bidder : \_\_\_\_\_  
\_\_\_\_\_

Telephone :

Fax :

Mobile :

E-mail :

3. Bidder's proposal number and date :

4. Name & Address of the contact :

Person to whom all references shall  
Be made regarding this tender

Telephone :

Fax :

Telex :

E-mail :

5. EMD Details :

S.No.	DD No.	Dated	Bank Name	Amount
1.				

6. No. of years in business (Electronics Kits) :

7. Turnover (for last 3 years) – 2012-15 : 2012-13:

2013-14:

2014-15:

8. Supplies in India (Reference of Organization):

9. Copy of Income Tax returns ( Last 3 years ) : Yes/No

(Attach attested copies)

- 12. Sales Tax Registration Certificate : Yes/No  
(Attach attested copy)
- 13. PAN No. : Yes/No
- 14. Documentary evidence of OEM : Yes/No  
Authorization Certification attached
- 15. Onsite Warranty for Six Months : Yes/No
- 16. I/We have read all the terms and conditions : Yes/No  
of the letter inviting quotations and accept  
them in full
- 17. Self declaration of not blacklisted attached : Yes/No

(Signature)

Seal

**Witness 1:**

Signature.....  
Name.....  
Designation.....  
Company.....  
Date.....

**Witness 2:**

Signature.....  
Name.....  
Designation.....  
Company.....  
Date.....

## TECHNICAL BID

Embedded Systems Kits	Description	Yes/No	Deviation
<p>1) <b>ARM Cortex™-M3 board with specifications:</b></p> <p>2) <b><u>Debugger</u> :</b></p>	<ul style="list-style-type: none"> <li>• 100MHz ARM Cortex™-M3 processor-based MCU</li> <li>• On-Chip Memory: at least 512KB Flash &amp; 64KB RAM</li> <li>• 10/100 Ethernet Port</li> <li>• USB Port</li> <li>• 2 Serial Ports</li> <li>• Card Interfaces</li> <li>• Analog Voltage Control for ADC Input</li> <li>• Amplifier and Speaker</li> <li>• Debug Interface Connectors</li> <li>• Cortex debugger</li> <li>• Schematic diagrams, necessary cables, power supplies etc.</li> <li>• Expandability with additional hardware like Logic controller Interface, LCD Interface, Stepper Motor Interface with Stepper Motor , Traffic Lights Interface, Seven segment Display, Elevator Interface, etc.</li> <li>• Compatibility with industry leading IDE/Software.</li> <li>• Should support all popular IDE's.</li> <li>• Speed matching with Modern controllers</li> <li>• Feature of direct download into flash memory for most of popular microcontrollers.</li> <li>• Supported CPUs: Any ARM7/9/11, Cortex Series</li> <li>• Full-speed USB 2.0 interface</li> <li>• Free software updates</li> <li>• Cross platform support (runs on Windows, Linux, Mac OS X)</li> </ul>		



<p>3) <b><u>EXPANDABLE INTERFACE MODULES</u></b></p>	<ul style="list-style-type: none"> <li>• ARM board and debugger should support various kinds of interface modules including Logic controller Interface, LCD (16X2) Interface, Stepper Motor Interface with Stepper Motor, Traffic Lights Interface, Seven segment Display, Elevator Interface</li> <li>• FRC cables/power Cables/ connectors should be supplied</li> <li>• All interface modules should be covered with protective boxes for durability.</li> <li>• Complete documentation/ programs including schematic diagrams and interface details should be provided</li> </ul>		
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**Certificate of Authenticity**

This is to certify that the quoted item conforms as per the technical specification mentioned in the bid document.

Dated this \_\_\_\_\_ Day of \_\_\_\_\_ 20\_\_

(Signature)

(In the capacity of)

Duly authorized to sign the Tender Response for and on behalf of:

(Name and Address of Company) Seal/Stamp of bidder

**COMMERICAL BID (PRICE QUOTE)**

Items	Make	Quantity Required	Unit Rate (Inclusive of all taxes except Octroi) ( In Rs.)	Total Amount (in Rs.)
Embedded Systems Kits of specifications mentioned in Technical Bid with Debugger		4		
TOTAL				

(Total Amount in words: Rupees \_\_\_\_\_)

Dated this..... Day of .....

Signature (in the capacity of duly Authorized to sign the Tender for and on behalf of):

(Name and address of Company)  
Seal/Stamp of bidder

**Note: The Unit price mentioned will be considered for bid evaluation and no other mentioned price will be taken in to account**

**SELF-DECLARATION (NOTARIZED AFFIDAVIT)**

Date: -

To  
**The Director,  
NIELIT Delhi Centre,  
II Floor, Parsvnath Metro Mall,  
Inderlok Metro Station, Inderlok,  
New Delhi - 110052.**

In response to the tender No.....Dated.....  
as a owner/partner / Director of .....

I / We hereby declare that our Agency ..... is having unblemished past record and was not declared ineligible for corrupt and fraudulent practices either indefinitely or for a particular period of time.

I/ We M/s\_\_\_\_\_ (Name of the Company) are not blacklisted in any Department of Government of India or any State Government.

I/We further undertake that our partner M/s \_\_\_\_\_ (Name of Bidder) having office are also not blacklisted in any Department of Government of India or any State Government.

I/We hereby declare that there are no pending cases against M/s\_\_\_\_\_ (Name & Address of Bidder) with Department of Government of India or any State Government or any other court of law.

I/We hereby declare that Bidder's company or Director/Owner of the company have not been declared by any Court or Competent Authorities insolvent or involved in any fraudulent mean (Economical & Criminal) as on 31<sup>st</sup> March, 2015.

Name of the Bidder: -

Signature: -

Seal of the Organization: -