

Certificate Course in VLSI Design

Classes will be in Mixed Mode(Online+Offline)

Objective of the Course:

This course aims to providing detailed knowledge in VLSI design process starting from digital design, hardware descriptive languages, RTL, synthesis & simulation, verification, FPGA programming & implementation. In this process the student will understand the entire logic design process .

Duration of the Course: 80 Hrs (2 Hr per day)

Course Fee : 12,000/+ GST (18%)

COURSE OUTLINE

- Digital Design Review
- Hardware Description Language (Verilog HDL)
- FPGA Prototyping

ADMISSION PROCESS

Online Registration form at : <https://onlineapply.nielit.in/>

TRAINING STRATEGY

- Mode of Training is Instructor-led live Online and Offline.
- Interactive Query session.
- Soft copy of study material, Training PPT's & Projects Code
- Daily assignment for practice after class
- Digitally Signed Certificate on Completion of training

PREREQUISITE

Attendees must have a computer system/ Laptop with latest configuration with internet connection