

About NIELIT

NIELIT Aurangabad is a unit of NIELIT, New Delhi, which is an Autonomous Scientific Society of Department of Electronics & Information Technology (DeitY), Ministry of Communications & Information Technology, Government of India.

The Centre was established in the year 1986, primarily to train manpower in Electronics Design & Technology, Electronic Product Design & Development, Manufacturing Technology, Applied Research & Consultancy in the field of Information, Electronics & Communications Technology (IECT).

The Vision of Government of India is to create a globally competitive electronic design & manufacturing industry environment to meet country's domestic needs & to serve the international market. With this vision, government has announced several policy initiatives to create an eco-system which is expected to pay-off in terms of rejuvenated technology, wealth creation & generation of high quality employment. NIELIT as part of DeitY is taking initiatives to carry out awareness in Electronics System Design & Manufacturing (ESDM) area, leading to development of new technologies products & solutions.

Field Programmable Gate Array

Field Programmable Gate Arrays (FPGAs) are semiconductor devices that can be programmed to desired application or functionality. Present day FPGAs have high logic density and other features such as embedded processors, DSP blocks, clocking, etc.

FPGAs have the potential for higher performance and lower power consumption than microprocessors and compared with application specific integrated circuits (ASICs), offer lower non-recurrent engineering (NRE) costs, reduced development time, easier debugging and reduced risk.

Xilinx is the world's leading provider of All Programmable FPGAs and SoCs. Xilinx cofounders invented the first commercially viable field-programmable gate array in 1985- the XC2064. It had programmable gates and programmable interconnects between gates, the beginnings of new technology and market.

In the early 1990's FPGAs were primarily used in telecommunications and networking. This was an explosive period of time for FPGA's, both in sophistication and the volume of production. By the end of the decade FPGAs found their way into consumer, automotive, and industrial applications.

Advantages of FPGAs include the ability to reprogram in the field to fix bugs and may include a shorter time to market and lower non-recurring engineering costs.

Objective of workshop is to throw insight on

- Verilog Hardware Description Language
- Xilinx FPGA Design Tools
- FPGA Prototyping.

Faculty

- VLSI / Embedded Lab, NIELIT Aurangabad
- CoreEL Technologies (I) Pvt. Ltd., Bangalore

Five (5) days Workshop on

FPGA Prototyping with Xilinx Tools

At

NIELIT Aurangabad

on

14th June 2016 at 10:00 AM

Venue

Seminar Hall, NIELIT Aurangabad

Registration Form

Name :

Designation : Student /Faculty /Others

(tick appropriately and pay fee accordingly)

Department :

Area of Interest :

Name of Institute / College / University :

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Address :

.....

Mobile No. :

Email :

Date :

Signature :



To,

Thokchom Sunilkumar Singh
VLSI Lab, NIELIT Aurangabad
Dr. B.A.M. University Campus
Aurangabad - 431004
Phone 0240-2982021/22

For Address See Overleaf

Photo Copies of this form can be used

General Information

1. Students and Faculty members of Engineering Colleges are welcome to the workshop.
2. Registration can be done by using the enclosed form sending by post, e-mail or Fax with contact details.
3. Course Fee: Rs 1250/- + Service Tax @actual, for students and Rs 2000/- + Service Tax @actual, for Faculty & Other (Mandatory to carry College / Office ID cards). Fee exemption for SC/ST applicants (on production of relevant certificates)
4. Mode of payment for course fee should be either Cash or DD in favour of NIELIT, payable at Aurangabad (Maharashtra)

Important Dates

Last Date for Registration by post / Email
14th June 2016 (Spot Registration Also Available)

How to Register

1. Submit the attached form duly filled by post/courier to, Thokchom Sunilkumar Singh, VLSI Lab, NIELIT Aurangabad, Dr. B.A.M. University Campus, Aurangabad - 431004, (Maharashtra) Phone 0240-2982021/22, Fax - 0240-2982050 Email id: thsunil@nielit.gov.in
Please mention your institutions details.

On Receipt of Form / e-mail, a confirmation mail will be sent. It is Mandatory to bring a printed copy of this confirmation mail. Website: www.nielit.gov.in/aurangabad

Five (5) Days Workshop on



FPGA Prototyping with Xilinx Tools
Starting on 14th June, 2016 at 10:00 AM



Organized at

NIELIT Aurangabad

Dr. Babasaheb Ambedkar University Campus
Aurangabad (Maharashtra)

An initiative under project
Capacity Building in the Area of
Electronic Product Design and Production
Technology

Organized By

NIELIT Aurangabad, An Autonomous Body of
Department of Electronics & IT (DeitY),
Ministry of Communications & IT,
Govt of India

