

Centre of Excellence in Chip Design at NIELIT NOIDA in association with SoCTeamup Semiconductors Pvt. Ltd as industry partner

One-week Short Term Course & Training Program on VLSI Design



Duration: 5 Days (40 Hours)

NOTE: Only in physical mode. **Limited seats (Only 20)**

Eligibility: Students who have completed or pursuing B.E/B.Tech/M.E/M.Tech.

Note: Research Scholars, Faculty members and Industry professionals can also enrol

Prerequisites: -

1. Basic knowledge of digital circuits and logic gates.
2. Familiarity with a hardware description language (HDL) such as Verilog or VHDL
3. Familiarity with a Unix/Linux environment and command-line interface

Course Fees: Rs. 3,540/- (incl.GST)

Registration Process: Candidates have to apply in prescribed application form through online registration portal <https://regn.nielitvte.edu.in/> or through Android App "NIELIT Kaushal Setu". The duly filled form along with the course fees Rs. 3,540/- (incl. GST) has to be submitted in online mode through the above link. The Fees deposited is Non-Refundable.

Course Date

26th Dec. 2023
to
30th Dec. 2023

Mode of Payment: Fees can be paid either by debit/credit card or in any online mode.

Course Contents

Day 1	Inauguration & Invited talk FPGAs: Types, Architecture, Applications FPGAs: Hands on Session
Day 2	Synthesizing RTL Design on FPGA: Lecture, Demonstration, Lab session Hands on Session
Day 3	ASIC Design Flow: Lecture, Demonstration, Lab session RTL Design & Verification: Lecture, Demonstration, Lab session
Day 4	Synthesis: Lecture, Demonstration, Lab Session DFT (Design for Testability): Lecture, Demonstration, Lab Session
Day 5	DFT Lab Session Contd.... Physical Design: Lecture, Demonstration, Lab Session Q & A

For any queries and more details please
contact on
8218724641/9711177638

Course Venue

NIELIT NOIDA, IETE NOIDA Centre Building, PS-1D,
Behind Brahampurtra Shopping Complex Sector 29,
Noida, Uttar Pradesh 201301

Registration Link:

<https://regn.nielitvte.edu.in/>

or

Through Android App "NIELIT Kaushal Setu"