

Advt. No.: NIELIT/GKP/259/15/Admn

Dated:25/10/2024

**Advertisement for the Post of Research Assistant (RA) under CST-UP Project
(On Contract Basis)**

Walk-in-Interview

Applications are invited for the post of “**Research Assistant (RA) (on contract basis)**” for the project entitled “**Development of IoT Integrated Monitoring System for Distributed Solar PV Power Plants**” (Project ID: 4086) sanctioned by the Council of Science & Technology, UP (CSTUP) vide letter CST/D-1426 dated 16.10.2024 under the Department of Science & Technology, Government of Uttar Pradesh. Interested candidates are requested to present themselves before the selection committee as per the following schedule:

Date: November 04, 2024

Day: Monday

Time: 10:00 AM

Venue: Center of Excellence in Chip Design, NIELIT, Noida

IETE Bhawan, PS-1D, Behind Brahmputra Shopping Complex, Sector 29,
Noida, Uttar Pradesh 201303

Candidates must be present in person with duly completed application on the prescribed format (Annexure-1) along with copies of the supporting documents. A confirmation email of attending the Walk-in interview may also be sent at the email sc-academics@nielit.gov.in of the Principal Investigator.

The details of the project are as under:

Title of the Project: Development of IoT Integrated Monitoring System for Distributed Solar PV Power Plants

Name of the Position: Research Assistant (on contract basis)

Number of Position: 01 (One)

Project Duration (remaining): Three (03) Years

Age Limit: Maximum 28 Years as on 01/04/2024

Essential Qualifications: B.E./B.Tech. or equivalent degree in CSE/ Electronics and Communication Engineering or equivalent. Candidates with higher qualifications may also apply.

Desirable Qualifications: Candidate should have practical experience of working in the area of IoT and should have hands-on experience in handling solar panels.

Stipend: 28,000/- p.m. (Fixed), No HRA will be admissible

Important Notes

1. The applicant should be resident of Uttar Pradesh.
2. The selected candidate should be ready to visit different research centres in India as and when required.
3. The applicant will be responsible for the authenticity of information, documents and photographs submitted.
4. Mere possessing the prescribed qualification does not ensure that the candidate would be called for the Interview. The Candidates will be shortlisted based on merit and need of the project.
5. Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
6. No TA/DA will be paid for appearing in the interview.
7. The first and foremost responsibility of the candidate selected as Research Scholar is to fulfill the job requirements and complete the above-mentioned project in timely manner.
8. The association between PI and the candidate can be discontinued at any time by giving one month’s notice in writing either by the PI to the candidate and vice versa.
9. This appointment will be purely contractual. Initially, the appointment will be for the One year which will be renewed for another year or till the end of project whichever is earlier based on the performance of the candidate and recommendation of the selection/review committee.

Dr. Smita Sharma

Principal Investigator (PI)

National Institute of Electronics & Information

Technology (NIELIT), Center of Excellence-Chip

Design

Noida, Uttar Pradesh, India

Email: sc-academics@nielit.gov.in