SPOT ADMISSIONS NOTIFICATION FOR
M.TECH. (SESSION 2024-26)

1. Electronics Design & Technology	2. VLSI Design
Eligibility: B.E /B.Tech. (Min. 50%* or equivalent in CGPA) in ECE / Electronics Engineering / EEE / Electronics and Instrumentation Engineering / Instrumentation and Control Engg.	**Eligibility:** B.E /B.Tech. (Min.50%* or equivalent in CGPA) in ECE / Electronics Engineering / EEE / Electronics & Instrumentation Engineering.

*5% relaxation in percentage is applicable in the case of SC/ST candidates
100% Fee Exemption for SC/ST Candidates as per Govt. norms (Limited Seats)
Stipend for Non-GATE Candidates (up-to Rs. 9000/- per month, limited seats)

FEATURES

- Approved by AICTE, & Affiliated to AKTU, Lucknow (U.P.)
- Experienced and Well Qualified Faculty.
- Placement and Entrepreneurial Assistance.
- Campus having NKN with Wi-Fi.
- Well Equipped Library & Virtual Labs.

- Scholarships for GATE qualified candidates.
- For Non-GATE candidates stipend/Assistantship by the Institute. (Limited Seats)
- R&D Labs with modern tools.
- A big lush green campus and Hostel
- Sports and Gymnasium facility.

IMPORTANT DATE
05th September 2024

HOW TO APPLY FOR SPOT ADMISSION

➢ Candidates shall register for spot admission by visiting the campus or Scanning the QR / visiting the website
https://www.nielit.gov.in/gorakhpur/content/mtech-programme-0

➢ For spot admission candidates shall report at NIELIT Gorakhpur campus on 5th September 2024 (09:00 AM onwards)

➢ Eligibility: GATE 2022 / 2023 / 2024 qualified candidates **OR**
AKTU PG Entrance Exam (CUET PG 2024 Exam) qualified candidates.

➢ For other candidates: Institute Level Test on 5th September 2024

➢ To Apply please visit the website or **Contact:** Sh. Deepam Dubey, Scientist 'C'.
Mobile No.: 8317093874   **Email ID**: deepamdubey@nielit.gov.in

Advt. No: NIELIT/GKP/259/11/2024	Additional Director