



**National Institute of Electronics and Information Technology**  
(Under Ministry of Electronics & Information Technology, Govt. of India)  
MMMUT Campus, Deoria Road, Gorakhpur-273010  
Website <https://www.nielit.gov.in/gorakhpur/index.php>

## SPOT ADMISSIONS FOR M.TECH. (SESSION 2022-24)

Applications are invited for spot admission against vacant seats in  
**M. Tech. (Electronics Design & Technology) and M. Tech. (VLSI Design)**

### 1. Electronics Design & Technology

**Eligibility:** B.E /B.Tech. (Min. 50%\* or equivalent in CGPA) in Electronics & Comm. Engineering / Electronics Engineering / Electrical and Electronics Engineering / Electronics and Instrumentation Engineering / Instrumentation and Control Engg.

### 2. VLSI Design

**Eligibility:** B.E /B.Tech. (Min.50%\* or equivalent in CGPA) in Electronics & Comm. Engineering / Electronics Engineering / Electrical & Electronics Engineering / Electronics & Instrumentation Engineering.

**\*5% relaxation in marks for SC/ST candidates**

**100% Fee Exemption for SC/ST Candidates as per Govt. norms (Limited Seats)**

### FEATURES

- Approved by AICTE, & Affiliated to Dr. A.P.J. Abdul Kalam Technical University, Lucknow (U.P.).
- Scholarships for GATE qualified candidates.
- Experienced and Well Qualified Faculty.
- Full Placement and Entrepreneurial Assistance.
- R&D Labs with modern tools.
- Well Equipped Library & Virtual Labs.
- Campus having NKN and Library facility.
- A big lush green campus and Hostel & Gymnasium facility for boys.

### IMPORTANT

- **GATE qualified Candidates: Stipend as per PG Scholarship norms of AICTE.**
- **Non-GATE Candidates: Stipend as per Internship Scheme of NIELIT.**

### HOW TO APPLY

- For Direct Admission: GATE 2020 / 2021 / 2022 qualified candidates OR AKTU PG Entrance Exam (CUET PG 2022 Exam) qualified candidates.
- For other candidates: Institute Level Test
- To Apply please visit the website or scan the QR Code

<https://www.nielit.gov.in/gorakhpur/content/mtech-programme-0>

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Additional Director