

National Institute of Electronics and Information Technology

(Under Ministry of Electronics & Information Technology, Govt. of India) M.M.M. University of Technology Campus, Gorakhpur-273010 Website https://nielit.gov.in/gorakhpur

INSTITUTE LEVEL SPOT ADMISSION FOR VACANT SEATS M.Tech. Session 2020-21

Applications for admission are invited for vacant seats in M. Tech. (ED&T) and M. Tech. (VLSI Design) Courses.

1. Electronics Design & Technology

Eligibility: B.E / B.Tech. (50% or equivalent CGPA) in Electronics & Comm. Engg. / Electronics Engg. / Instrumentation and Control Engineering/ Electrical and Electronics Engg. / Electronics and Instrument/Instrumentation and Control Engineering. *5% relaxation is applicable in the case of SC/ST candidates

2. VLSI Design

Eligibility: B.E / B.Tech. (50% or equivalent CGPA) in Electronics & Comm. Engg/ Electronics Engg / Electronics & Engg / Electronics & Instrumentation Engineering

*5% relaxation is applicable in the case of SC/ST candidates

100% Fee Exemption for SC/ST Candidates (Limited Seats)

FEATURES

- Approved by AICTE, & Affiliated to Dr. A.P.J. Abdul Kalam Technical University, Lucknow (UP)
- Scholarships: GATE qualified and SC/ST students as per rules.
- Experienced and Well Qualified Faculty.
- R&D Labs with modern tools.
- Well Equipped Library.
- Campus having NKN facility.
- A big lush Green campus and Hostel facility for boys.
- Virtual Labs

Last date to apply for Spot Admission: 09th November 2020

- Interested candidates may fill the online application form for Spot Admission.
- Direct Admission for GATE / UPSEE 2020 (PG) qualified candidates.
- Institute level test for Non-GATE and Non-UPSEE candidates.
- To Apply: https://www.nielit.gov.in/gorakhpur/content/mtech-programme-0

Contact: Deepam Dubey, Scientist 'B': (Mob.) 8317093874 Email: deepamdubey@nielit.gov.in.

For more details, please visit our website https://nielit.gov.in/gorakhpur

Advt. No: NIELIT/GKP/259/25/2020

Joint Director

