

National Institute of Electronics & Information Technology Gorakhpur – Extension Centre Lucknow

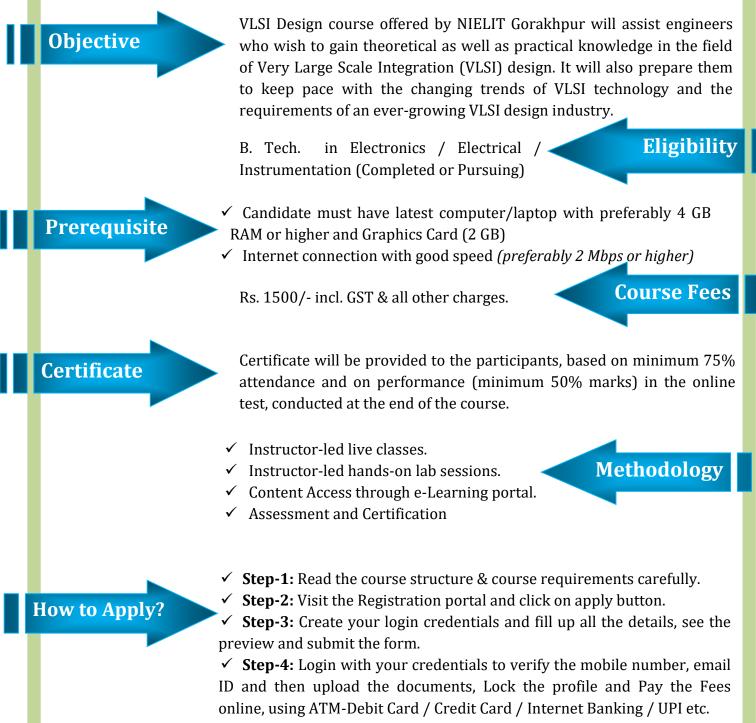
(Under Ministry of Electronics and Information Technology, Govt. of India) MMMUT Campus, Deoria Road, Gorakhpur-273010 http://www.nielit.gov.in/gorakhpur/



VLSI Design

VLSI Design using Cadence Tool 2 Weeks Online Course

Duration: 2 Weeks (2 Hrs. per day) Medium of Instructions: English + Hindi





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Course Conten	
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Day	Торіс	Day	Торіс	Day	Торіс
Day #01	Introduction to VLSI Design, Historical Perspective, VLSI technology trends, System approach to VLSI Design.	Day #02	VLSI Design Cycle, ASIC Design Flow, Fabrication, Packaging, Testing and Debugging.	Day #03	Basics of Analog Circuits- 1, Design and Analysis of RC circuits, Timing issues in RC Circuits.
Day #04	Operation Amplifiers Fundamentals, Design and Analysis of Op- Amps-1	Day #05	Design and Analysis of Op- Amps-2	Day #06	CMOS Inverter Basics
Day #07	CMOS Inverter Basics, Sizing	Day #08	Designing Combinational Logic Gates, Logical Efforts	Day #09	Combinational Circuits, Timing Analysis
Day #10	VLSI Physical Design Auto	mation			

Course Coordinator

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