

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान, तिरुपति

National Institute of Electronics and Information Technology, Tirupati (Ministry of Electronics & Information Technology, Govt. of India)

VACANCY DETAILS AND ELIGIBILITY CRITERIA Advt. No. NT/Rectt/2025/01

Name of the Post	No. of Vacancies	Max. Age limit	Consolidated Emolument (per month)
Senior Faculty	2	40 years	Rs. 35,000 to Rs. 40,000/-
Faculty	3	35 years	Rs. 28,000 to Rs. 33,000/-
Junior Accountant	1	40 years	Rs. 20,000 to Rs. 25, 000/-

Place of Posting: NIELIT Tirupati

The National Institute of Electronics & Information Technology (NIELIT) is operating under Ministry of Electronics & Information Technology (MeitY), Government of India with the objective of fostering Human Resource Development and associated activities in the domain of Information, Electronics, and Communications Technology (IECT), NIELIT has played a pivotal role in enhancing digital literacy and technical education across the country. NIELIT actively engages in formal and non-formal education within the IECT sector. It continuously develops industry-relevant, high-quality educational and training programs in cutting-edge fields such as Artificial Intelligence (AI), Cyber Security and Forensics, Blockchain, Internet of Things (IoT), Cloud Computing, Electronics System Design & Manufacturing (ESDM), and various other emerging technology domains. Offering a broad range of courses, from diploma and degree programs to specialized skilling initiatives, NIELIT also functions as a National Examination and Accreditation Body, ensuring the credibility and quality of IT and electronics-related courses conducted by affiliated institutes and organizations in the non-formal education sector.

With a strong national presence, NIELIT operates across 55 locations, including major cities such as Agartala, Aizawl, Ajmer, Aurangabad, Balasore, Bhubaneswar, Bikaner, Buxar, Calicut, Chandigarh, Chennai, Chuchuyimlang, Churachandpur, Daman, Delhi, East Delhi, South West Delhi, Dibrugarh, Dimapur, Gangtok, Gorakhpur, Guwahati, Haridwar, Hyderabad, Imphal, Itanagar, Jammu, Jorhat, Kargil, Kohima, Kolkata, Kokrajhar, Kurukshetra, Leh, Lucknow, Lunglei, Majuli, Mandi, Muzaffarpur, Noida, Pasighat, Patna, Pali, Pilibhit, Ranchi, Ropar, Senapati, Shillong, Shimla, Silchar, Srinagar, Tezpur, Tezu, Tirupati, and Tura. The organization's headquarters is located in New Delhi.

NIELIT Tirupati is under establishment in the SV University campus, Tirupati to be emerged as one of the premier institutions providing affordable quality education as per the job market requirements for candidates from all over India in state-of- the-art fields like Information Security, Cloud Computing, Data Science, AI, VLSI, Embedded Systems, Industrial IoT. NIELIT Tirupati is being set-up to imparts training in online, blended and class room modes delivered with the help of modern ICT tools.

NIELIT Tirupati is looking for bright & result-oriented candidates. The positions available are purely on a temporary contractual basis on consolidated emolument initially for a period of **Six (06) Months,** which is further extendable based on the performance of the candidate and requirement under the project. The details of the posts are as below:

Sl. No.	Post Name and No of Vacancies	Essential Qualification	Post Qualification Experience	Role/ Responsibility
1	Senior Faculty 01 No.	B.E / B. Tech. (CSE /IT/ ECE/ Electronics and Instrumentation), or equivalent or allied areas with First class from recognized university/ institution or higher	At least 03 years' experience in teaching/research/working in the areas of Embedded System/IoT/VLSI Design/SoC Design/FPGA/RTL/PCB Design	 Taking Regular Classes. Lab-in charge of the concerned lab. Assist in day to day administrative activities in Training Section. Assist Students in Projects. Assist in various
2	Senior Faculty 01 No	B.E / B. Tech. (CSE /IT/ ECE/ Electronics and Instrumentation), or equivalent with First class from recognized university/ institution or higher	At least 03 years' experience in teaching/research/working in the areas of Data Engineering/Cloud Computing/ Machine Learning/ Artificial Intelligence/Information Security	projects being implemented by NIELIT Tirupati Interaction with various stakeholders.
3	Faculty 02 Nos	B.E / B. Tech. (CSE /IT/ ECE/ Electronics and Instrumentation), or equivalent or allied areas with First class from recognized university/institution or higher	Experience in teaching/research/working in the areas of Embedded System/IoT/VLSI Design/SoC Design/FPGA/RTL/PCB Design is desirable	 Taking Regular Classes. Lab support of the concerned lab. Assist in day-to-day administrative activities in Training Section. Assist Students in Projects. Assist in various
4	Faculty 01 No	B.E / B. Tech. (CSE /IT/ ECE/ Electronics and Instrumentation), or equivalent with First class from recognized university/ institution or higher	Experience in teaching/research/working in the areas of Data Engineering/Cloud Computing/ Machine Learning/ Artificial Intelligence/	projects being implemented by NIELIT Tirupati Interaction with various stakeholders

		Information Security is desirable	
5 Junior Accountant 01 No	Master Degree in Commerce from recognized university/Institution with knowledge of Tally and computer proficiency certificate preferably NIELIT 'ÇCC' or higher. Must be conversant with GoI rules & regulations including GFR, CVC etc.	At least One (01) years post qualification experience in the area of commercial finance and accounts, preferably in Govt. Organization/ Autonomous Body/ PSU/ Industrial Establishment of repute and must also have working knowledge of Tally	 Recording day-to-day transactions in Tally. Maintaining PFMS account TDS, GST-TDS and any other statutory payment & filing thereof. Preparation of documents as per Audit requirements and Generation of e-invoice Preparation of salary bill for all categories of employees, EPF, Budget, and finalization of accounts. Preparation of Balance Sheet Assist in day-to-day Accounts activities. Interaction with various stakeholders