## RECRUITMENT NOTICE

**Recruitment for the Post of Junior Research Fellow - 1 No.**

**Place of Posting: Chennai**

**Date of Interview/Test : 10-July-2024**

National Institute of Electronics and Information Technology (NIELIT) is an Autonomous Scientific Society of Ministry of Electronics and Information Technology (MeitY), Govt. of India, engaged in human resource development and related activities like skill development, capacity building, project execution in the areas of Information Technology, Electronics, ESDM, e-Governance, Cyber Security, etc. It is engaged in formal and non-formal education in the above areas besides project execution. It is also one of the National Examination bodies, which accredits institutions / organizations for conducting courses in IT and Electronics in non-formal sector.

NIELIT is growing at a fast pace and at present has Centres/ Extension Centers at 52 locations in Agartala, Ahlawalpur (Saksharta Kendra), Aizawl, Ajmer, Aurangabad, Bhubaneswar, Calicut, Chandigarh, Chennai, Chuchuyimlang, Churachandpur, Daman, Delhi, Dibrugarh, Dimapur, Gangtok, Gorakhpur, Guwahati, Haridwar, Hyderabad, Imphal, Itanagar, Jammu, Jorhat, Kargil, Kohima, Kokrajhar, Kolkata, Kurukshetra, Lakhanpur (Saksharta Kendra), Leh, Lucknow, Lunglei, Majuli, Mandi, Pali, Pasighat, Patna, Ranchi, Ropar, Senapati, Shillong, Shimla, Silchar, Srinagar, Tezpur, Tezu, Tirupati and Tura etc., with the Headquarters at New Delhi.

NIELIT Chennai one of the premier institutions providing affordable quality education as per the job market requirements for candidates from all over India in state-of-the-art fields like Information Security, Cloud Computing, Data Sciences, AI, VLSI, Embedded Systems, Industrial IoT, Electronic Product Design and Automotive Electronics. NIELIT Chennai imparts training in online, blended and class room modes delivered with the help of modern ICT tools including Virtual Labs. NIELIT Chennai has a profound experience of implementing various capacity building projects of the Centre and the State Governments for empowering the weaker sections of the society and to bridge the skill gaps of the Industry.

NIELIT Chennai is looking for bright & result-oriented candidates for **working in a research project jointly implemented with IIT Madras, funded by Department of Biotechnology, Govt. of India.** The following position is available purely on a temporary contractual basis on consolidated emolument initially for a period of **One (01) Year and extendable maximum up to period of the project.**

| 1) | **Name of the post** | : | **Junior Research Fellow** |
|---|---|---|---|
|  | Number of post(s) | : | 01 (One) |
|  | Consolidated Emolument | : | 37000/- * |
|  | Method of Recruitment | : | **Online Interview/Test** |
|  | Age Limit | : | Not exceeding 45 Years |

\* As per the DST OM DST/PCPM/Z-06/2022 dated 26.06.2023

## Essential Qualification:

B.E/ B. Tech./ M.E/ M.tech in Electronics and Communication/ Electrical and Electronics/ VLSI/ Microelectronics/ SoC-Design/ Semiconductor Technology/ Computer Science and allied branches/ M.Sc. in Electronics/ Computer Science selected through a process described through any one of the following:

> i. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.
>
> ii. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc

## Desired Candidate Profile
- Sound Knowledge of Verilog RTL Coding,
- Strong Fundamentals in C/C++, Matlab
- DSP/AI algorithm implementation in C/Python/Matlab
- Hands on Experience in FPGA Architecture and programming.

## Job Responsibilities
- Work as a JRF in a DBT-funded project titled "Indigenous state-of-the-art FPGA- based ultrasound beam former modules for Affordable Ultrasound scanners: A Proof-of-concept scanner development"
- Involved in the implementation of Image Processing algorithms in FPGA using Verilog/C/C++
- Work in a Multi-Institutional R&D team
- Reporting the outcome of the work from time to time
- Any other relevant work assigned

# <mark>Online Interview from 10-07-2024 onwards</mark>

**General Terms & Conditions:**

1.  Candidates fulfilling the above-mentioned qualifications, experience, age, etc., may **APPLY ONLINE** through the website https://rect.nielitchennai.edu.in which will be available from **19.06.2024 to 07.07.2024** Till 05:00 PM.

2.  If the candidate does not have a valid email id, he/she should create a new valid email id before applying online. Details regarding interviews through Walk-in/Video Conferencing will be sent through email only.

3.  The candidates must upload self-attested copies of certificates of qualification, age, experience, detailed resume, etc., and passport-size photographs **on the portal**.

4.  The qualification of candidates must be from a recognized University/Institution. Mere fulfilling the essential qualification/experience does not guarantee selection.

5.  **Applicants with last semester's result awaited or incomplete degrees are not eligible to apply.**

6.  **Incomplete applications (i.e without photograph and all applicable certificates etc.,) will not be entertained and will be summarily rejected**.

7.  Shortlisted candidates will be informed of the interview after verification of essential qualifications and experience.

8.  Poor network connection/connection failure from any side during the **online interview through VC** will not be the responsibility of NIELIT Chennai.

9.  Registration Fee (Non-Refundable): Rs. 200/- for all categories per post.

    **Mode of Payment**:**Through Online/Net Banking/UPI through Account Details**:

    **Name of Account Holder**: "NIELIT Chennai",

    **SB Account No.** :31185720641,

    **IFSCcode**: SBIN0001669.

    **Name of the Bank:** State Bank of India, Kottur (Chennai),

    **Kindly note the Reference/UTR Number for the further online application submission process.**

10. The selected candidate shall be paid consolidated emoluments only and will not be paid any other financial benefits like Medical, HRA, Transport, etc.

11. Number of vacancies may increase/decrease based on the final assessment or the recruitment process may stand cancelled and such changes will be made by NIELIT Chennai without any notice.

12. Unless suitable candidates are found, posts shall not be filled.

13. Selection of candidates for appointment to the above-mentioned position will be based on the performance of the candidates in the walk-in / online interview / written test and as found eligible as per prescribed criteria.

14. NIELIT reserves the right to conduct online/offline written tests or skill tests for screening of the

candidates before the interview if felt necessary.

15. The selected candidates will be offered contractual engagement on a purely temporary contract basis for a period of one year initially, which may be extended depending upon the performance of the candidate and the requirement of NIELIT. These posts do not carry any entitlement for regularization in the future.

16. The contract appointment shall not confer any right for regularization/absorption or continuation of service in NIELIT, Chennai.

17. Contract may be terminated at any time by giving one-month notice on either side. The applicants will have no claim implicit or explicit for consideration against any NIELIT post.

18. The offer of appointment for the selected candidates will be subject to verification of certificates/testimonials at the time of joining and completeness of other formalities.

19. NIELIT reserves the right to assign duties to the selected candidate in any location where NIELIT Chennai has a work in progress.

20. Applications not providing adequate information required for assessment will be liable for rejection.

21. Canvassing/trying to influence NIELIT employees to secure the job in any manner shall disqualify the candidate.