

COURSE SYLLABUS

Name of the Division: *Smart Technology & Education Division (STED)*

Name of the Course: *Online PG Diploma in VLSI System Design*

Course Code: *VL 500-Online*

Duration: *24 Weeks [120 days/ 840 hours]*

Course Coordinator: *Nandakumar R, +91 9995427802*

Course Structure: The VL 500-Online contains eight modules including project. The students are required to do a project work in any one of the modular areas, for a period of 8 weeks to be eligible for issue of Online PG Diploma in VLSI System Design.

The modules are as follows:

Module Code	Module Title	Duration		
		Weeks	Days	Hours
VE 501	Analog and Digital System Design	3	15	105
VE 502	Embedded C and ARM Cortex Microcontrollers	4	20	140
VE 503	Linux OS & Internals	2	10	70
VL 504	Verilog HDL : Language and Coding for Synthesis	2	10	70
VL 505	FPGA based Embedded System Design	2	10	70
VE 506	OS Porting on FPGA with ARM Core	1	5	35
VE 507	Industrial Product Design	2	10	70
VE 508	Project Work	8	40	280
	Total Duration	24	120	840

VE 501: Analog and Digital System Design

Module Duration: 105 Hours

Objective

The objective of the course is to provide a thorough understanding about the elements and techniques for analog and digital system design.

Course Description

Analog Concepts

- Introduction to Analog Circuits
- BJT Small Signal Model
- MOS Circuit Model, Biasing of Circuits
- Amplifiers, MOS Amplifiers
- Frequency Response of Amplifier
- Differential Amplifier
- Feedback Theory
- OPAMP Circuits
- ADC/DAC

Digital Concepts

- Combinational Circuit Design
- Sequential Circuit Design
- Design of controller and Data path units
- State Machines
- Design Examples & Case Studies

Learning Outcomes

On successful completion of the module, the candidate shall be able to:

- Design and analyse Analog and Digital Electronic systems

Reading List

1. Design of Analog CMOS ICs - Razavi. The best book available on CMOS analog.
2. Microelectronic circuits : Adel Sedra and Kenneth C. Smith
3. Franco S, Design with Operational Amplifiers and Analog Integrated Circuitis
4. CMOS Analog circuit design - Allan and Holberg
5. Analog Integrated Circuit Design - Ken Martin and David Johns
6. Digital Design by Morris Mano & Michael D Ciletti
7. Digital Design: Principles and Practices by John F. Wakerly
8. Digital Design by Frank Vahid

VE 502: Embedded C and ARM Cortex Microcontrollers

Module Duration: 140 Hours

Objective

This module is framed to set the required background in embedded system concepts and 'C' language for the rest of the modules. It aims at familiarizing the students in embedded concepts and programming in 'C'. This module covers the advanced topics in 'C' such as Memory management, Pointers, Data structures which are of high

relevance in embedded software is considered in depth. This module also covers application development with ARM Cortex Microcontrollers.

Course Description

Embedded Concepts

Introduction to embedded systems, Application areas, Categories of embedded systems, Overview of embedded system architecture, Specialties and trends of embedded systems, Architecture of embedded systems, Development and debugging Tools.

'C' and Embedded C

Introduction to 'C' programming, Storage Classes, Data Types, Controlling program flow, Arrays, Functions, Memory Management, Pointers, Arrays and Pointers, Pointer to Functions and advanced topics on Pointers, Structures and Unions, Data Structures, Linked List, Stacks, Queues, Conditional Compilation, Preprocessor directives, File operations, Variable arguments in Functions, Command line arguments, bitwise operations.

Introduction to ARM Cortex Architecture

Introduction to ARM Architecture, Overview of ARM, Overview of Cortex Architecture

Cortex M3 Microcontrollers & Peripherals

Cortex M3 based controller architecture, Memory mapping, Cortex M3 Peripherals – GPIOs, Timers, UARTs, Cortex M3 interrupt handling – NVIC. Application development with Cortex M3 controllers with standard peripheral libraries.

Learning Outcomes

After successful completion of the module, the students shall be able to:

- Develop Embedded application using Embedded C Programming
- Use ARM Cortex Microcontrollers with Embedded C Programming for Application Development

Text Books:

1. Embedded/Real Time Systems Concepts, Design and Programming Black Book, Prasad, KVK.
2. Let us C by Yashwant Kanetkar.
3. The Definitive Guide to the ARM Cortex M3, Joseph Yiu, Newnes.

Reference Books:

1. Embedded Systems Architecture Programming and Design: Raj Kamal, Tata McGraw Hill.
2. Embedded C, Pont, Michael J

3. Embedded Systems an Integrated Approach: Lyla B Das, Pearson
4. C Programming language, Kernighan, Brian W, Ritchie, Dennis M
5. Art of C Programming, JONES, ROBIN, STEWART, IAN
6. ARM System Developer's Guide - Designing and Optimizing System Software
by: Andrew N Sloss, Dominic Symes, Chris Wright; 2004, Elseiver.
7. Cortex M3 Reference manual.
8. STM32Ldiscovery datasheets, reference manuals & Application notes.

VE 503: Linux OS & Internals

Module Duration: 70 Hours

Objective

The objective of the course is to provide understanding of the Operating System concepts and internals with focus on Linux OS.

Course Description

- **Introduction**
Basic Operating System Concepts, Linux as OS for System , Comparison of OSes, Tools and Development, Discussion on Embedded OS Applications and Products.
- **System architecture of a Basic OS**
Internals of Linux OS, System Calls, Linux Compiler options, Process, Multithreading and Synchronization, Serial port and Network programming, Kernel module programming and Device drivers
- **Inter Process Communication**
Pipe and FIFOs, Shared memory, Sockets
- **Practical Sessions**
Programming with Linux Processes, Threads and IPC mechanisms

Learning Outcomes

After successful completion of the module, the students shall be able to:

- Understand Internals of Linux OS
- Develop applications with Linux OS

Reading List

1. GNU/LINUX Application Programming, Jones, M Tims
2. Embedded Linux: Hardware, Software, and Interfacing, Hollabaugh, Craig,
3. Building Embedded Linux Systems: Yaghmour, Karim
4. Embedded Software Primer: Simon, David E.
5. Linux Kernel Internals: Beck, Michael At Al
6. UNIX Network Programming : Steven, Richard
7. Linux: The Complete Reference: Petersen, Richard
8. Linux Device Drivers: Rubini, Alessandro, Corbet, Jonathan

9. Linux Kernel Programming: Algorithms and Structures of version 2.4: Beck, Michael At Al
10. Linux Kernel Development: Love, Robert
11. Operating System Concepts, Peter B. Galvin, Abraham Silberschatz, Gerg Gagne, Wiley Publishers

VL 504: Verilog HDL: Language and Coding for Synthesis

Module Duration: 70 Hours

Objective

The objective of the course is to provide a thorough understanding about and hands-on with digital design & Verification using Verilog HDL

Course Description

- Introduction to Verilog HDL & Hierarchical Modeling Concepts
- Lexical Conventions & Data Types
- System Tasks & Compiler Directives
- Modules, Ports and Module Instantiation Methods
- Gate Level Modeling
- Dataflow Modeling
- Behavioural Modeling
- RTL Design and Logic Synthesis and Synthesis issues
- Design Verification using Test benches
- Mini-project and Case Studies

Learning Outcomes

After successful completion of the module, the students shall be able to:

- Author Design IPs for VLSI using Verilog HDL
- Develop Test benches using Verilog HDL

Reading List

1. Verilog HDL - A guide to Digital Design and Synthesis by Samir Palnitkar.
2. A Verilog HDL Primer by J.Bhasker.
3. Verilog HDL Synthesis, A Practical Primer by J. Bhasker
4. Verilog **Digital System** Design by Zainalabedin Navabi
5. Fundamentals Of Digital Logic With Verilog Design by Stephen Brown
6. Modeling, Synthesis, and Rapid Prototyping with the VERILOG HDL by Michael D. Ciletti

7. The Verilog Hardware Description Language by Donald E. Thomas & Philip R. Moorby
8. IEEE Std 1364-2005 : IEEE Standard Hardware Description Language based on the Verilog Hardware Description Language by the IEEE

VL 505: FPGA based Embedded System Design

Module Duration: 70 Hours

Objective

The objective of the course is to provide a thorough understanding about and hands-on practice with FPGA based digital system design and emulation

Course Description

- Introduction to Programmable Logic and FPGAs
- Architecture of popular Xilinx FPGAs
- FPGA Design Flow Xilinx Vivado®
- Implementation Details
- Advanced FPGA Design tips
- Logic Synthesis for FPGA
- Static Timing Analysis
- Design problems using Xilinx® Platforms
- Case Studies on FPGA Based implementations

Learning Outcomes

After successful completion of this module, students should be able to:

- Prototype digital Systems using FPGA
- Emulate, debug & Characterize reusable IPs

Reading List

1. FPGA-Based System Design by Wayne Wolf
2. Advanced FPGA Design Architecture, Implementation and optimization by Kilts
3. Xilinx® User guides & documentation available at <https://www.xilinx.com/support/documentation-navigation/overview.html>
4. Intel® FPGA User guides & documentation available at <https://www.intel.com/content/www/us/en/programmable/documentation/lit-index.html>

5. Swadeshi Microprocessors user guides & documentation available at available at <https://shakti.org.in/>
6. Embedded Core Design with FPGAs. by Zainalabedin Navabi
7. FPGA Prototyping by Verilog Examples by Pong P. Chu
8. Digital Design Using Digilent FPGA Boards Verilog / Vivado Edition Richard E Haskell, Darrin M Hanna
9. FPGA Design: Best Practices for Team-Based Reuse by Philip Andrew Simpson

VE 506: OS Porting on FPGA with ARM Core

Module Duration: 35 Hours

Objective

The objective of the course is to provide understanding of the techniques essential to port Operating System on FPGA with ARM Core.

Course Description

- ARM IP core Porting on FPGA
- Linux Porting
 - Linux booting procedures
 - Linux Kernel compilation
 - Boot loader for Embedded Systems
 - Debuggers
- Techniques of Linux OS Porting on FPGA with ARM Core

- **Practical Sessions**
Porting Linux OS on FPGA with ARM Core

Learning Outcomes

After successful completion of the module, the students shall be able to:

- ARM IP core Porting on FPGA
- Port the Linux OS on FPGA with ARM Core

Reading List

1. GNU/LINUX Application Programming, Jones, M Tims
2. Embedded Linux: Hardware, Software, and Interfacing, Hollabaugh, Craig,
3. Building Embedded Linux Systems: Yaghmour, Karim
4. Embedded/Real-Time Systems: Concepts, Design and Programming: The Ultimate Reference, Dr. K.V.K.K. Prasad, Published by Wiley DreamTech, 2003
5. ARM System Developer's Guide - Designing and Optimizing System Software by: Andrew N Sloss, Dominic Symes, Chris Wright; 2004, Elseiver.
6. FPGA-Based System Design by Wayne Wolf

7. Advanced FPGA Design Architecture, Implementation and optimization by Kilts
8. Xilinx® User guides & documentation available at <https://www.xilinx.com/support/documentation-navigation/overview.html>

VE 507: Industrial Product Design

Module Duration: 70 Hours

Objective

The objective of this module is to help fresh graduates and practicing engineers to enhance their knowledge and skills of industrial product design covering the various aspects of product development process and design of an Industrial Electronics Product.

Course Description

Product Development Process

- Electronic Hardware Design Flow
- Hardware and software integration issues and testing
- Hardware and software co-verification
- Component cost and costing in product design

Embedded Hardware design

- Understanding component datasheet
- Selection of passive components
- Interfacing of sensors with microcontrollers
- Power supply requirements for Electronic circuits.

Printed Circuit Board Design

- Evolution and Classification of Printed Circuit Boards
- PCB fabrication methodologies design considerations
- Design rules for analog, digital and power applications,
- EMI/EMC, crosstalk, reflection
- Basics if IPC standards
- Thermal management of electronic devices and systems.

Semiconductor Packages

- SMD packages, Single chip packages or modules (SCM)
- Commonly used packages and advanced packages
- Materials in packages, Current trends in Packaging

Industrial Design

- Enclosure designing aspects
- IP ratings overview
- Product standards.

Learning Outcomes

After successful completion of the module, the students shall be able

- Understand the design and development process of an Industrial Electronics Product
- Apply product development process for realization of the product

Reading List

1. Product Design & Development - Karl T Ulrich & Steven D. Eppinger; Mc Graw Hill
2. Relevant Data sheets and application notes

VE 508: Project Work

Module Duration: 280 Hours

Description

The students can select hardware, software or system level projects. The project can be implemented using FPGAs, Microcontrollers and Open Source Hardware Platforms which students have studied and used during the course.