

COURSE PROSPECTUS

Name of the Division: *Smart Technology & Education Division (STED)*

Name of the Course: *Online PG Diploma in VLSI System Design*

Course Code: *VL 500–Online*

Starting Date: *28.12.2020*

Duration: *24 Weeks [120 days/ 840 hours]*

Course Fee: *Rs. 39,228/- + All Taxes*

Course Coordinator: *Nandakumar R, +91 9995427802*

Preamble: *VLSI (Very Large Scale Integration) has emerged as a very significant technology to provide tremendous quantum of process technologies for MEMS, NEMS and RF components, many of the formerly external components can now be integrated into a single System-on-Chip which has resulted in a dramatic improvements in performance while achieving reduction in the size, cost and power consumption. Complexity in such systems arises not only from the diversity of the technologies, from sensors and actuators and RF front-ends to base-band DSP software, etc., that must be integrated on-chip comprising of tens of millions of transistors, but also from the fact that such systems must be increasingly built from parts that have been designed separately and using different tools and flows.*

The curriculum has been designed in consultation with industry and academic experts and our strategic partners, to map the skill sets and design methodologies, which is high in demand in VLSI & Embedded Systems industries. Our students have been successfully placed in reputed product companies and we enjoy the trust of many reputed companies, who have entered into strategic alliances with us. Emphasis of the teaching curriculum is on design methodology and practical applications. The course contents have been designed keeping in view the emerging trends in needs for skilled manpower.

Objective of the Course: *Online PG Diploma in VLSI System Design is intended to impart skills needed for designing VLSI System design using reusable Intellectual Property (IP) Cores as building blocks.*

Outcome of the Course:

- Provide the trainees in-depth knowledge and skills required by VLSI & SoC markets around the globe by imparting comprehensive understanding about the fundamental principles, methodologies and industry practices.
- Makes the successful participants readily employable in multiple roles available in VLSI industry
- Enhances the skillsets and confidence for fabless startups,
- Serves as a concrete platform for people involved in application research, consultancy and high end product development in both industry and academia.

Expected Job Roles:

- RTL Design Engineer
- RTL Verification Engineer
- FPGA design Engineer
- Board Design Engineer

Course Structure: The VL 500 – Online contains eight modules including project. The students are required to do a project work in any one of the modular areas, for a period of 8 weeks to be eligible for issue of Online PG Diploma in VLSI System Design.

The modules are as follows:

Module Code	Module Title	Duration		
		Weeks	Days	Hours
VE 501	Analog and Digital System Design	3	15	105
VE 502	Embedded C and ARM Cortex Microcontrollers	4	20	140
VE 503	Linux OS & Internals	2	10	70
VL 504	Verilog HDL : Language and Coding for Synthesis	2	10	70
VL 505	FPGA based Embedded System Design	2	10	70
VE 506	OS Porting on FPGA with ARM Core	1	5	35
VE 507	Industrial Product Design	2	10	70
VE 508	Project Work	8	40	280
	Total Duration	24	120	840

Other Contents

- I. **Course Fees:** *Course fee is Rs 39,228/- + All taxes as applicable*

Modular wise Course Fee: Not Applicable for this course

- II. **Registration Fee:** An amount of Rs.1000/- (including all taxes as applicable) (nonrefundable) should be paid at the time of registering for the course.

This fee shall be considered as part of course fee, if the student joins the course. If a student register and pay for more than one course and join for any one course, all such amount will be adjusted against the course fee payable. If the candidate does not join or fails to complete the course the amount will be forfeited.

However above the registration fee shall be refunded on few special cases as given below.

- Course postponed and new date is not convenient for the student
- Course cancelled in advance, well before the admission date

III. Course Fee Installment Structure:

Students can pay the full fees of (Rs 39,228/- + all taxes as applicable) in advance or as installments as given below.

Fees	*Amount for General Candidates	*Amount for SC/ST Candidates	# Due Date (on or before)
Registration Fee	Rs 1000/-	Rs 1000/-	During Registration
1 st Installment	Rs 20,000/-	Rs 20,000/-	27.12.2020
2 nd Installment	Rs 18,228/-	Rs 18,228/-	24.02.2021
Total Fee	Rs 39,228/-	Rs 39,228/-	24.02.2021

*Taxes Extra (Currently GST @18% + KFC @1%), and revisions, if any by Government shall be applicable at the time of payment.

Fine will be applicable to late fee payment as given below

Sl. No.	Description	Fine
1	Late fee payment within two weeks after due date	18% (annually) of the outstanding dues
2	After second week of due date the candidate has to pay readmission fees along with the fine	Readmission fee Rs. 250/- plus fine of 18% (annually) of the outstanding dues
3	The candidate has to discontinue the course after third week from the due date	

IV. **Eligibility:** BE/B.Tech (ECE/EEE/AEI/CSE/IT/Biomedical/Medical Electronics, Mechatronics and allied branches) / M.Sc (Electronics/CS). Students undergoing BTech /MSc are also eligible, however they will be issued course certificate only on production of their degree certificate.

For more details about the policy refer:

<http://nielit.gov.in/sites/default/files/course/NIELITCalicutPoliciesShortTermCourses.pdf>

- V. **Number of Seats :40**
- VI. **Selection of candidates:** Selection is based on the marks in the qualifying Degree
- VII. **Test/Interview: Not Applicable**
- VIII. **Counseling/Admission : 28.12.2020**

IX. **Important Dates (if applicable) :**

<i>Last date for receiving online application for the course with payment of Rs 1000/- for registration. Candidates applying after this date will be considered in spot admission against vacancy.</i>	20.12.2020.
<i>Publication of selection list in our Website.</i>	21.12.2020
<i>Last date for Payment of the first installment fee of Rs 20,000/-</i>	27.12.2020
<i>Date of Counseling & Commencement of Classes</i>	28.12.2020

- X. **Course Timings:** *The course is planned to be conducted with flexible timing by including recorded sessions of lectures and demonstrations through LMS. The candidates will be given prior notice about live lectures scheduled between 9.00 am to 5.30 pm. The hands-on session using Remote Hardware Lab shall be provided with pre-intimated schedule.*

Participants should have the following setup to attend this course

- *Computer system (Desktop/Laptop with Camera) and stable broadband internet connection.*
- *Computer System Configuration - Linux OS/ Dual boot, i3/i5, 64bit processor, Minimum 4GB RAM*

- XI. **Placement :** visit <http://nielit.gov.in/content/placement-3>
- XII. **Lab Facilities :** <http://nielit.gov.in/calicut/>
- XIII. **Course Contents :** Course [Syllabus](#)

[Click here for General Terms and Conditions – Applicable to all courses](#)