



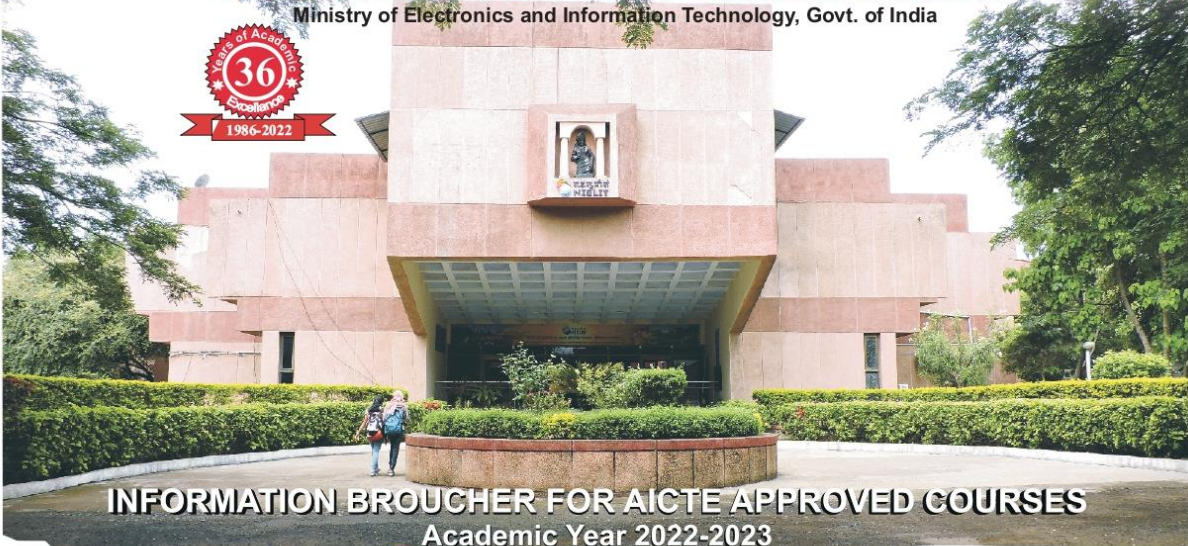
राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान, औरंगाबाद (महाराष्ट्र)



इलेक्ट्रॉनिकी सूचना और प्रौद्योगिकी मंत्रालय,

NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY, AURANGABAD

Ministry of Electronics and Information Technology, Govt. of India



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कार्यकारी संचालकांचा संदेश



नाईलीट औरंगाबाद केंद्र (पूर्वीचे CEDTI) हे नाईलीट च्या प्रमुख केंद्रांपैकी एक आहे ज्याची स्थापना आयटी आणि इलेक्ट्रॉनिक्समध्ये नेतृत्व विकसित करण्यासाठी अध्यापन, शिक्षण आणि संशोधनातील उत्कृष्टतेसह नाविन्यपूर्ण, उद्योजकता आणण्यासाठी 1987 मध्ये करण्यात आली होती. हे केंद्र अत्याधुनिक 14 सुसज्ज प्रयोगशाळा आणि यांत्रिक कार्यशाळा याशिवाय समृद्ध ग्रंथालय, एनकेएन, विद्यार्थ्यांसाठी व्यायामशाळा, सभागृह, वसतिगृह, कॅन्टीन, क्रीडा सुविधासह डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ (BAMU) परिसरामध्ये 18 एकरांपेक्षा जास्त क्षेत्रात सह-स्थित आहे.

केंद्र AICTE मान्यताप्राप्त बी.टेक (इलेक्ट्रॉनिक्स सिस्टीम इंजिनीअरिंग), एम.टेक (इलेक्ट्रॉनिक्स डिझाईन आणि टेक्नॉलॉजी), इलेक्ट्रॉनिक्स उत्पादन आणि देखभाल डिप्लोमा आणि पीएच.डी. अभियांत्रिकी आणि तंत्रज्ञान पदवीसाठी डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबादचे संशोधन केंद्र आहे.

प्रकल्प-आधारित अध्यापन पद्धतीवर आधारित, हे अभ्यासक्रम इलेक्ट्रॉनिक्स डिझाईन आणि तंत्रज्ञानाच्या क्षेत्रात व्यावहारिक कौशल्ये प्रदान करतात आणि अभियांत्रिकी आवश्यकता, औद्योगिक डिझाईन, उत्पादन अभियांत्रिकी, एर्गोनॉमिक्स, सौंदर्यशास्त्र, सिस्टम-स्तरीय पॅकेजिंग, थर्मल डिझाईन, विश्वासार्हता, EMI आणि EMC, चाचणी आणि मूल्यमापन, देखभालक्षमता, यशस्वी प्रणाली विकासासाठी आवश्यक सेवाक्षमता, डिझाईन, अंमलबजावणी आणि सेवाकालपश्चात अंतिम विल्हेवाट यासारख्या आंतरविद्याशाखीय क्षेत्र समस्यांचा समावेश करतात. हे अभ्यासक्रम नवीनतम औद्योगिक कल आणि आवश्यकता लक्षात घेतात आणि विद्यार्थ्यांना उद्योजक, तज्ञ आणि डिझायनर बनण्यासाठी, R&D करण्यासाठी आणि IECT मध्ये औद्योगिक प्रशिक्षण देतात.

एकंदर दर्जा उंचावण्यासाठी नाईलीट औरंगाबाद केंद्र उद्योगाभिमुख प्रकल्प, R&D आणि सल्लामसलतीसाठी प्रोत्साहन देत आहे. केंद्राची गणना आधुनिक युगातील विद्यार्थ्यांसाठी आधुनिक अभियांत्रिकी आणि तंत्रज्ञान शिक्षणाच्या गरजा ओळखून दूरदृष्टी आणि ध्येयास पूरक अशी एक मोठी शिक्षण प्रणाली निर्माण होईल जी परिणामाभिमुख, पारदर्शक, उत्तरदायित्वपूर्ण आणि प्रवेशयोग्य आहे आणि स्वतःला अद्ययावत ठेवण्यासाठी प्रभावी आहे, या विचारसरणीवर केली जाते आणि आम्हाला आमच्या प्रतिस्पर्धापेक्षा खूप पुढे ठेवते.

केंद्रात काम करणारे सर्व प्राध्यापक सदस्य आणि शास्त्रज्ञ व्यावसायिक शिक्षण देण्यासाठी, नाविन्यपूर्ण विचारांना चालना देण्यासाठी, ज्ञानाचा वापर, व्यावसायिक नैतिकता आणि सामाजिक जबाबदाऱ्यांबद्दल जागरूकता निर्माण करण्यासाठी कठोर परिश्रम घेत आहेत. उत्कृष्टता, सचोटी, पारदर्शकता, गुणवत्ता, सांघिक कार्य, उत्कटतेने कार्यान्वित करणे, विश्वास, सतत आणि विद्यार्थी केंद्रित शिक्षण ही आमची मूलभूत मूल्ये आमच्या शैक्षणिक कार्यक्रमांमध्ये जवळून समाकलित केलेली आहेत.

मी तुम्हाला नाईलीट औरंगाबाद केंद्राने देऊ करत असलेल्या सर्व गोष्टींचा शोध घेण्यास प्रोत्साहित करतो आणि मला विश्वास आहे की केंद्रातून पदवीधर झालेल्या तुमच्यापैकी प्रत्येकजण जीवनाच्या कोणत्याही क्षेत्रात तुम्ही निवडलेल्या यशाची अमित छाप सोडेल.

(संजीव कुमार गुप्ता)

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SAY NO TO RAGGING

According to UGC guidelines, the definition of ragging states that any conduct whether by words spoken or written or by an act which has the effect of teasing, treating or handling with rudeness any other student, indulging in rowdy or undisciplined activities which causes or is likely to cause annoyance, hardship or psychological harm or to raise fear or apprehension thereof in a fresher or a junior student or asking the student to do any act or perform something which such student will not do in the ordinary course and which has the effect of causing or generating a sense of shame or embarrassment so as to adversely affect the physique or psyche of a fresher or a junior student.

IMPORTANT INSTRUCTIONS FOR THE STUDENTS

1. As per the directions of the Hon'ble Supreme Court in SLP No. 24295 of 2006 dated 16-05-2007 and in Civil Appeal number 887 of 2009, dated 08-05-2009, ragging is strictly prohibited and banned.
2. All students of the institute have to study and fill affidavit online.
http://antiragging.in/site/affidavits_registration_form.aspx.
3. Ragging is a Cognizable Offence. Students are advised not to indulge in Ragging.
4. Ragging entails heavy fines and/or suspension/expulsion.
5. In case the applicant for admission in the institute is found to have indulged in ragging in the past or if it is noticed later that he has indulged in ragging, admission may be refused or he/she shall be expelled from the institution.
6. It is mandatory for the parents to report immediately to the Authorities of the Institute in case their wards inform them about ragging.

PUNISHABLE INGREDIENTS OF RAGGING

1. Abetment to ragging or Criminal conspiracy to rag
2. Unlawful assembly and rioting while ragging
3. Public nuisance created during ragging
4. Violation of decency and morals through ragging
5. Injury to body, causing hurt or grievous hurt
6. Wrongful restraint or Wrongful confinement
7. Use of criminal force
8. Assault as well as sexual offences or unnatural offences
9. Extortion or Criminal intimidation
10. Criminal trespass or Offences against property
11. Attempts to commit any or all of the above mentioned offences against the victim(s)
12. Physical or psychological humiliation
13. All other offences following from the definition of "Ragging"

PUNISHMENT

Depending upon the nature and gravity of the offence as established by the Anti-Ragging Committee of the institution, the possible punishment for those found guilty of Ragging at the Institution level shaped any one or any combination of the following:

1. Suspension from attending classes and academic privileges
2. Withholding/ Withdrawing scholarship/ fellowship and other benefits
3. Debarring from appearing in any test/ examination or other evaluation process
4. Withholding results
5. Debarring from representing the institution in any regional, national or international meet, tournament, youth festival etc.
6. Suspension/expulsion from the hostel
7. Rustication from the institution for period ranging from 1 to 4 semesters
8. Expulsion from the institution and consequent debarring from admission to any other institution for a specified period
9. Fine ranging between Rupees 25,000/- and Rupees 1 Lakh
10. Collective punishment: When the persons committing or abetting the crime of ragging are not identified, the institution shall resort to collective punishment
11. Fresher who do not report the incidents of ragging either as victims or as witnesses shall also be punished suitably.

As per the directions of the Hon'ble Supreme Court of India, if any incident of ragging comes to the notice of authority, the concerned student shall be given liberty to explain and if his/her explanation is not found satisfactory, the authority would expel him/her from the Institute"

१.0 NIELIT औरंगाबाद - एक परिचय

१.१ उत्पत्ती

NIELIT चा इतिहास 1974 चा आहे जेव्हा इलेक्ट्रॉनिक्स विभाग (DoE) आता इलेक्ट्रॉनिक्स आणि माहिती तंत्रज्ञान मंत्रालय (MeitY), भारत सरकार आणि विद्यापीठ अनुदान आयोगाने (UGC) स्विस डेव्हलपमेंट कॉर्पोरेशनच्या सहाय्याने भारतीय विज्ञान संस्था (IISc.), बंगळुरूच्या आवारात प्रथम CEDT ची स्थापना केली.

CEDT, बंगळुरूच्या यशस्वी संचालनानंतर एका दशकानंतर, DoE (आता MeitY) ने 1987 मध्ये औरंगाबाद, इम्फाल, श्रीनगर, 1989 मध्ये कालिकत, मोहाली आणि गोरखपूर येथे अशीच केंद्रे स्थापन केली, ज्याचा उद्देश विविध स्तरांवर आणि विविध विशिष्ट इलेक्ट्रॉनिक्स डिझाइन क्षेत्रात मानवी संसाधने विकसित करण्याचा आहे। शैक्षणिक संस्था आणि उद्योग यांच्यातील दरी कमी करणे हा यामागील उद्देश होता.

औरंगाबाद, कालिकत, गोरखपूर, इम्फाल आणि श्रीनगर येथील CEDT केंद्रे 2001 मध्ये DOEACC (MeitY ची एक वैज्ञानिक सोसायटी) मध्ये विलीन करण्यात आली. राष्ट्रीय महत्त्वाच्या संस्थेत रूपांतर करण्यासाठी सोसायटीचे नाव बदलून '**National Institute of Electronics and Information Technology (NIELIT)**' 10 ऑक्टोबर 2011 रोजी असे करण्यात आले.

NIELIT औरंगाबाद Dr. Babasaheb Ambedkar Marathwada विद्यापीठच्या हिरव्यागार परिसरामध्ये सह-स्थित आहे आणि त्याचे कॅम्पस 18 एकरांपेक्षा जास्त पसरलेले आहे. यात सुमारे 14 सुसज्ज प्रयोगशाळा आणि यांत्रिक कार्यशाळा याशिवाय समृद्ध ग्रंथालय, विद्यार्थ्यांसाठी व्यायामशाळा, सभागृह, कॅन्टीन, बास्केट बॉल मैदान, व्हॉली बॉल मैदान, खो खो मैदान इ. आहे।

केंद्राने 1987 पासून **Diploma in Electronics Production and Maintenance**, 1990 पासून **M.Tech (Electronics Design and Technology)**, 2013 पासून **B.Tech (Electronics Engineering (specialization in system engineering))** असे अद्वितीय एआयसीटीई मान्यताप्राप्त कोर्सेस ऑफर करण्यास सुरुवात केली आणि हे Dr. Babasaheb Ambedkar Marathwada विद्यापीठ मान्यताप्राप्त संशोधन केंद्र देखील आहे।



केंद्र बजाज ऑटो लिमिटेड, व्हिडिओकॉन, स्टरलाइट, सीमेन्स, मेलटॉन, महाराष्ट्र पोलिस वायरलेस, इत्यादी क्षेत्रातील आघाडीच्या उद्योगांना सल्लामसलत आणि इतर सेवा देखील प्रदान करते. इलेक्ट्रॉनिक्स डिझाइन आणि प्रॉडक्शन टेक्नॉलॉजीमध्ये पुरेशा क्षमतेच्या पातळीसह मानवी संसाधने विकसित करण्यासाठी इलेक्ट्रॉनिक्स आणि माहिती तंत्रज्ञान मंत्रालय (MeitY) द्वारे प्रायोजित ESDM योजना देखील राबवत आहे.

केंद्राच्या औद्योगिक दर्जाच्या प्रयोगशाळा Printed Circuit board, VLSI Design, Embedded Systems, Product Design, Digital Systems, Process Control & Instrumentation and CAD/ CAM क्षेत्रातील नवीनतम प्रणाली आणि विकास साधनांनी पूर्णपणे सुसज्ज आहेत.

याशिवाय असंख्य संदर्भ पुस्तके, जर्नल्स, मासिके; केंद्रातील विद्यार्थ्यांना MeitY Library Consortium (IEEE आणि पुस्तकांसह नवीनतम ई-जर्नल्सचा समृद्ध संग्रह) आणि National Knowledge Network (NKN): सर्व विद्यापीठे, संशोधन संस्था, प्रयोगशाळा, आरोग्यसेवा आणि कृषी संस्था, ग्रंथालयांशी जोडलेले मल्टी-गीगाबिट क्षमतेचे मजबूत नेटवर्कमध्ये प्रवेश आहे.

सर्व प्रयोगशाळा, लायब्ररी आणि कार्यालय केंद्रीय नेटवर्कद्वारे जोडलेले आहेत आणि विद्यार्थी त्यांच्या टर्मिनल्समधून आणि चांगल्या प्रकारे कनेक्ट केलेल्या वाय-फाय प्रणालीद्वारे माहिती पुनर्प्राप्त करू शकतात. केंद्र कृषी- इलेक्ट्रॉनिक्स, इलेक्ट्रॉनिक्स उत्पादन डिझाइन, बौद्धिक संपदा अधिकार (IPR), न्यूरल नेटवर्क्स, ई-लर्निंग यांसारख्या क्षेत्रात राष्ट्रीय स्तरावरील सेमिनार/कार्यशाळा नियमितपणे आयोजित करते.

केंद्रातील R&D प्रशिक्षित अभियंता विद्यार्थी Texas, L&T, HCL, Wipro Technologies, BITS, IIT, BEL, HAL, ISRO, DRDO, BARC, ECIL, Messung, Thermax, Honeywell Cyrus logic L&T सारख्या आघाडीच्या आणि नामांकित संस्थांमध्ये काम करत आहेत.



केंद्र एक समाधान-केंद्रित मॉडेल संस्था आणि ज्ञान-आधारित उपक्रम बनले आहे आणि R&D अभियंते आणि उद्योजकांचा पूल तयार करण्यासाठी अथक प्रयत्न करत आहे.

1.2 Objectives of Centre

1. To bring an **innovative, entrepreneurial spirit** along with excellence in teaching, learning and research to develop leaders in IT and Electronics.
2. To generate and keep update **Industry-ready quality professionals with knowledge-based skill set** in IECT and allied fields through formal and informal education system.
3. To establish a **Quality system of examination and certification** that is globally recognized and provides a fair assessment of the competency of students.
4. To maintain **close links with Industries, R&D and Academic Institutions** to promote electronics, IT and industrial design culture.
5. To develop **entrepreneurs, experts and designers**, carry out R&D and provide
6. **Industrial Consultancy** in IECT.
7. To offer **e-Training** in Electronics, Information Technology and Industrial Design methodology and production technique.

Mission: Identifying the needs of modern engineering & technology education and providing Quality Technical Education leading to Academic Excellence, creativity and innovation in the areas of Electronics and Information Technology.

Vision: To impart professional education that is outcome oriented, combined with fostering innovative thinking, application of knowledge, inculcating professional ethics and consciousness to social responsibilities.

१.३ उत्पादन डिझाइन

केंद्र तरुणांना जागतिक दर्जाच्या शैक्षणिक आणि कौशल्य विकासाच्या संधी उपलब्ध करून देत आहे आणि केंद्रातील अभ्यासक्रमाची रचना विद्यार्थ्यांमध्ये प्रणाली पातळीवरील समज विकसित करण्यासाठी तयार करण्यात आली आहे. बहुतेक M.Tech प्रकल्प कंपन्यांनी प्रायोजित केले आहेत आणि त्याचा परिणाम हार्डवेअर इलेक्ट्रॉनिक उत्पादनांमध्ये होतो. काही विद्यार्थी नंतर त्यांच्या ज्ञानाचे व्यावसायिक उपक्रमांमध्ये रूपांतर करतात.



उद्योग संवाद:

संस्था उद्योगांना product design & development, product engineering, proto-type development, process automation, consultancy इत्यादी सेवा देखील प्रदान करते. अत्याधुनिक उपकरणे/ मशीनची सर्व्हिसिंग आणि देखभाल, तंत्रज्ञान आत्मसात करण्यात सहाय्य आणि अत्याधुनिक उपकरणे/ मशीनची खरेदी यामध्ये उद्योगांना दर्जेदार सेवा देण्यासाठी सर्वोत्कृष्ट पायाभूत सुविधा निर्माण करण्यासाठी संस्था सर्वतोपरी प्रयत्न करत आहे.

1.4 R & D, प्रकल्प आणि सल्लागार

पदव्युत्तर स्तरावरील शैक्षणिक प्रकल्प एक (01) वर्ष कालावधीचे आहेत, तर डिप्लोमा आणि B.Tech स्तरावरील प्रकल्प एक (01) सेमिस्टर (सहा महिने) कालावधीचे आहेत. विद्यार्थ्यांना उद्योगाच्या वातावरणाशी परिचित होण्यासाठी उद्योगांशी संवाद साधण्यासाठी प्रोत्साहित केले जाते आणि प्राध्यापकांच्या मार्गदर्शनानुसार उद्योगातील वास्तविक समस्या त्यांच्या नाविन्यपूर्ण प्रकल्प कार्य म्हणून हाती घेण्यास प्रवृत्त केले जाते. वरील व्यतिरिक्त, संस्था सरकार तसेच उद्योग प्रायोजित प्रकल्प देखील करते. त्यापैकी काही “ई-लर्निंगमधील शिक्षकांचे प्रशिक्षण”, “माहिती सुरक्षा शिक्षण आणि जागरूकता” आणि IECT मध्ये मूल्यवर्धित कौशल्य विकासाद्वारे महिला सक्षमीकरण”. वरील व्यतिरिक्त, उद्योगांना सल्लामसलत देखील दिली जाते.

1.5 State of the Art Laboratories

- i. CAD/CAM
- ii. Consumer Electronics
- iii. Industrial Automation
- iv. Internet of Things
- v. Network & Server Facilities
- vi. Opto-Electronics
- vii. Power Electronics
- viii. Printed Circuit Board
- ix. VLSI Design
- x. Embedded System Design
- xi. Open-Source Computing



- xii. AR/VR Lab
- xiii. Library Infrastructure
- xiv. Multimedia Lab
- xv. Additive Manufacturing/3D Printing Lab
- xvi. Robotics Process Automation (RPA) Lab
- xvii. Central Workshop

1.6 Other Amenities /Facilities:

Table no. 01

Lecture Halls	Uninterrupted Power (63 KVA DG Set)
Seminar Hall	Cafeteria
Conference Hall	Boy's Hostel
Auditorium	PG Boy's Hostel
Local Area Network with 225 (100 Mbps) Nodes.	Warden Quarters
Leased line internet connectivity	Guest House
Library with online access to IEEE Journals and National Digital Library of India along with a rich print collection of books, journals and magazines (<i>refer Annexure XXVIII</i>)	Vehicle Parking
Virtual Smart Class-Room facility	Open Theatre
Placement Cell and Model Career Centre	Record Room (143 Sqm)
Gymnasiums(Separate for Boys & Girls)	Sports Facilities
Dramatics, dance and Extra-Curricular	Jogging Track

1.7 Student Life

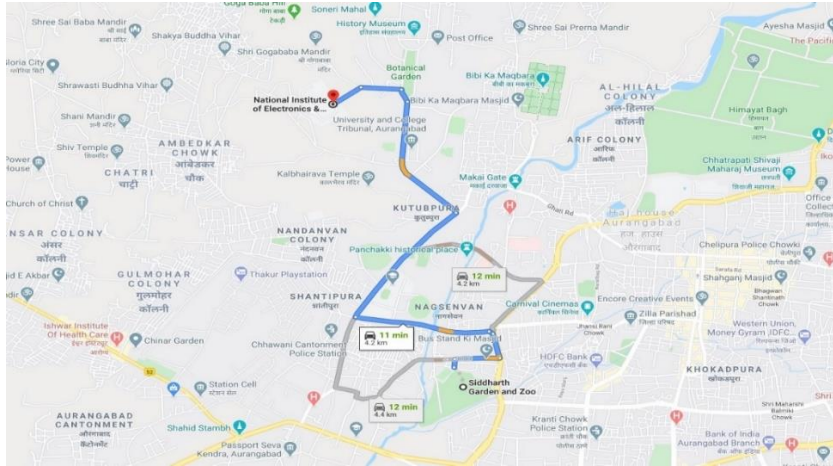
The course work is project based and students get ample time to work on innovation. There are various sports and cultural clubs that are being managed by the student community on campus which serve for various extra-curricular activities:

1. Cricket
2. Badminton
3. Lawn Tennis
4. Basket-Ball
5. Body Building
6. Drama Club
7. Music Club
8. Athletics
9. Literary and Fine Arts
10. Photography





1.8 Location



नॅशनल इन्स्टिट्यूट ऑफ इलेक्ट्रॉनिक्स अँड इन्फॉर्मेशन टेक्नॉलॉजी औरंगाबाद
डॉ.बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ परिसर, औरंगाबाद, महाराष्ट्र-431004
वेबसाइट: <http://nielit.gov.in/aurangabad/>
लँडलाइन: (+91-240) 2982021, 2982022
फॅक्स: (+91-240) 2982050

2.0 औपचारिक अभ्यासक्रम

संस्था खालील AICTE मान्यताप्राप्त अभ्यासक्रम ऑफर करत आहे:

- A) Diploma in Electronics Production and Maintenance (DEPM)
- B) B. Tech in Electronics Engineering (Specialization in System Engineering).
- C) M. Tech in Electronics Design and Technology (full time)
- D) Part-time M. Tech in Electronics Design and Technology

हे अभ्यासक्रम प्रॅक्टिकल ओरिएंटेड आहेत आणि डिझाइन आणि प्रोजेक्ट वर्कवर भर देऊन डिझाइन केलेले आहेत. उद्योगातील नवीनतम ट्रेड आणि गरजा लक्षात घेऊन अभ्यासक्रमाचे नियतकालिक पुनरावलोकन आणि अद्ययावत करून, सेमिस्टर पद्धतीद्वारे विद्यार्थ्यांचा सखोल अभ्यास, पारदर्शक मूल्यमापन प्रणाली आणि Dr. Babasaheb Ambedkar Marathwada University यांनी केंद्राला दिलेली स्वायत्तता यामुळे शिक्षणाचा दर्जा राखला जातो. विद्यार्थ्यांचा अभ्यासक्रम पूर्ण केल्यावर किंवा यशस्वी उद्योजक म्हणून स्थायिक झाल्यामुळे नामांकित उद्योग आणि संस्थांमध्ये नोकरी मिळविल्याचा आनंद मिळतो.

महत्वाच्या तारखा

क्र.	उद्देश	डीईपीएम प्रथम वर्ष	डीईपीएम (लॅटरल एंट्री)	बी.टेक (लॅटरल ट्री)	एम.टेक (ईडीटी) पूर्णवेळ	एम.टेक (ईडीटी) अर्धवेळ
1	वेबसाइटवरून अर्ज डाउनलोड करणे.	कृपया वेबसाइटला भेट द्या www.dtemaharashtra.gov.in		14 जून 2022	कृपया https://cmt.admission.nic.in या वेबसाइटला भेट द्या	14 जून 2022
2.	फी सह अर्जाची पावती		10 जुलै 2022			10 जुलै 2022
3.	लेखी चाचणी (तात्पुरती)		13 जुलै 2022			13 जुलै 2022
4.	निवडलेल्या आणि प्रतीक्षा यादीतील उमेदवारांच्या यादीची घोषणा		15 जुलै 2022			15 जुलै 2022
5.	दस्तऐवज पडताळणी आणि अभ्यासक्रमासाठी प्रवेश		15 जुलै 2022			15 जुलै 2022
6.	प्रतीक्षा यादीतील विद्यार्थी प्रवेश		17 जुलै 2022			17 जुलै 2022
7.	शैक्षणिक दिनदर्शिका सुरू					1 ऑगस्ट 2022

सर्व तारखा तात्पुरत्या आहेत आणि तारखेतील कोणताही बदल केवळ वेबसाइटवर प्रदर्शित केला जाईल

** निवडलेल्या आणि प्रतीक्षा यादीतील उमेदवारांची यादी केवळ वेबसाइटवर प्रदर्शित केली जाईल

** डिप्लोमा अभ्यासक्रमाच्या प्रवेशाच्या तारखा CAP 2022 च्या वेळापत्रकानुसार असतील.

** बी.टेक प्रथम वर्ष अभ्यासक्रमाच्या प्रवेशाच्या तारखा JoSSA/CSAB 2022 च्या वेळापत्रकानुसार असतील.

** एम.टेक पूर्णवेळ अभ्यासक्रमाच्या प्रवेशाच्या तारखा CCMT 2022 च्या वेळापत्रकानुसार असतील.

** सक्षम अधिकारी त्याच्या अंतिम तारीख वाढवू शकतात

फी भरण्याची प्रक्रिया

संस्थेचे नाव/ लाभार्थी	नॅशनल इन्स्टिट्यूट ऑफ इलेक्ट्रॉनिक्स अँड इन्फॉर्मेशन टेक्नॉलॉजी (NIELIT)
बँकेचे नाव	स्टेट बँक ऑफ इंडिया
शाखा	समर्थ नगर औरंगाबाद महाराष्ट्र
बचत बँक खाते क्रमांक	32078399585
IFSC/RTGS NO	SBI 0007919
इलेक्ट्रॉनिक हस्तांतरणाची पद्धत	NEFT, SBICollect, वेबसाइट: www.onlinesbi.com

लेखी परीक्षेचा अभ्यासक्रम

क्र.	उद्देश	अभ्यासक्रम	विषय
1.	बी.टेक (लॅटरल एंट्री)	राज्य/केंद्रीय तंत्रशिक्षण मंडळाद्वारे मान्यताप्राप्त बी.टेक (लॅटरल एंट्री) डिप्लोमा (इलेक्ट्रॉनिक्स)	
2.	2. M.Tech (EDT) पार्ट टाईम	गेट 2022 साठी अभ्यासक्रम (इलेक्ट्रॉनिक्स आणि कम्युनिकेशन/इंस्ट्रुमेंटेशन इंजी./इलेक्ट्रिकल इंजिनीअरचा सामान्य विषय)	

प्रवेशासाठी लेखी परीक्षा

1. लेखी प्रवेश परीक्षा दीड तास कालावधीची असेल
2. प्रश्न वस्तुनिष्ठ प्रकारचा असेल, ज्यामध्ये उमेदवाराला बहुपर्यायी उत्तरे दिली आहेत
3. उमेदवाराने त्याला/तिला प्रदान केलेल्या त्याच पत्रकात योग्य उत्तर चिन्हांकित करणे आवश्यक आहे.
4. उमेदवारांनी पेन, एचबी पेन्सिल, शार्पनर आणि इरेजर आणणे आवश्यक आहे.
5. उमेदवारांना परीक्षा हॉलच्या बाहेर प्रश्न/उत्तर पुस्तिका घेण्याची परवानगी नाही.
6. परीक्षा फक्त इंग्रजी माध्यमात असेल.
7. परीक्षा हॉलमध्ये कॅल्क्युलेटर, मोबाईल, डिजिटल डायरी, लॉगबुक आणि पॉकेट पीसीला परवानगी नाही.
8. लेखी परीक्षेचे आयोजन अभ्यासक्रमासाठी अर्ज केलेल्या उमेदवारांच्या संख्येवर अवलंबून असते.

3. डिप्लोमा इन इलेक्ट्रॉनिक्स प्रोडक्शन अँड मेंटेनेन्स (DEPM)

हा तीन वर्षांचा (सहा सेमिस्टरचा) अभ्यासक्रम आहे, जो विद्यार्थ्यांना इलेक्ट्रॉनिक्स किंवा संबंधित उद्योग किंवा उद्योगातील उत्पादन/देखभाल पर्यवेक्षक किंवा डिझाइन असिस्टंट म्हणून करिअरसाठी तयार करतो. अभ्यासक्रमाला AICTE, नवी दिल्ली आणि महाराष्ट्र राज्य तंत्रशिक्षण मंडळ (MSBTE), महाराष्ट्र (भारत) यांनी मान्यता दिली आहे। डीईपीएम प्रथम वर्ष आणि डीईपीएम लॅटरल प्रवेश हे तंत्रशिक्षण संचालनालय (डीटीई) महाराष्ट्र द्वारा आयोजित Centralized Admission Process (CAP) आहेत.

3.1. पहिल्या वर्षी प्रवेश आणि दुसऱ्या वर्षी पार्श्विक प्रवेश/थेट प्रवेश

3.1.1. प्रथम वर्ष आणि पार्श्विक प्रवेश/थेट द्वितीय वर्ष या दोन्हीसाठी किमान पात्रता निकष तंत्रशिक्षण संचालनालय (DTE) महाराष्ट्र द्वारे आयोजित केंद्रीय प्रवेश प्रक्रियेनुसार (CAP) अनुसार आहेत. कृपया www.dtemaharashtra.gov.in पहा किमान पात्रतेसाठी

3.1.2. सीट मॅट्रिक्स

3.1.2.1. DEPM मध्ये एकूण 60* जागा उपलब्ध आहेत. सीट मॅट्रिक्ससाठी कृपया www.dtemaharashtra.gov.in तपासा.

*वर दर्शविलेल्या जागांची संख्या तात्पुरती आहे आणि वेळोवेळी दिलेल्या सरकारी आदेशांनुसार बदलल्या जातील.

महत्वाचे

- तंत्रशिक्षण संचालनालय (DTE) महाराष्ट्र द्वारे आयोजित केंद्रीकृत प्रवेश प्रक्रियेत (CAP) सूचित केल्यानुसार राखीव प्रवर्गातील उमेदवारांसाठी जागा राखीव आहेत.
- www.dtemaharashtra.gov.in मध्ये दर्शविल्याप्रमाणे open, OBC, SC, ST, PWD आणि परदेशी राष्ट्रीय जागा राखीव आहेत.
- तंत्रशिक्षण संचालनालय (DTE) महाराष्ट्राने वर्णन केल्यानुसार परदेशी नागरिकांसाठी जागा राखीव आहेत.
- निवडलेल्या उमेदवारांचा प्रवेश त्यांच्या कागदपत्रांची पडताळणी आणि लागू शुल्क भरण्याच्या अधीन असेल.
- DTE महाराष्ट्राच्या CAP फेरीद्वारे निवडलेल्या उमेदवारांनी विचारलेल्या कागदपत्रांच्या यादीसह अधिसूचित केलेल्या दिवशी नोंदणी करणे आवश्यक आहे आणि section 10.1 मध्ये नमूद केल्यानुसार प्रवेशासाठी शुल्क भरून नोंदणी करणे आवश्यक आहे, अन्यथा त्यांचा दावा जप्त केला जाऊ शकतो।
- डीईपीएम अभ्यासक्रमाची प्रवेश प्रक्रिया पूर्ण केली जाते जेव्हा सीट मॅट्रिक्सनुसार उमेदवारांना तात्पुरते प्रवेश दिला जातो आणि नोंदणी केली जाते किंवा सक्षम प्राधिकाऱ्याने ठरवलेली कालमर्यादा संपली जाते, जी कधीही आधी असेल.

3.2 शैक्षणिक दिनदर्शिका - संदर्भ -1 पहा

3.3 प्रथम वर्ष आणि थेट द्वितीय वर्षासाठी शिक्षण योजना:

- प्रत्येक विद्यार्थ्याने खाली नमूद केल्याप्रमाणे सेमिस्टरच्या सर्व विषयांसाठी नोंदणी करणे आवश्यक आहे. डीईपीएम विद्यार्थ्यांसाठी IV सेमिस्टरच्या शेवटी उन्हाळ्याच्या सुट्टीत सहा आठवड्यांचे अनिवार्य इन-प्लॉट औद्योगिक प्रशिक्षण अनिवार्य आहे.
- तपशीलवार अभ्यासक्रम <https://www.nielit.gov.in/aurangabad> येथून डाउनलोड करता येईल.

3.4 टर्म कोर्स लोड:

- प्रत्येक सेमिस्टरमध्ये, विषयाचा भार प्रति सेमिस्टर 33 ते 41 क्रेडिट्स पर्यंत बदलतो.

3.5 मूल्यांकन:

- i. एखाद्या विद्यार्थ्याला सेमिस्टर परीक्षेला बसण्याची परवानगी आहे जेव्हा त्याची थिअरी आणि प्रॅक्टिकल क्लासेसमध्ये किमान 75% उपस्थिती आहे, त्याच्या सर्व सत्रीय असाइनमेंट पूर्ण केल्या आहेत आणि त्याची/तिची सर्व देणी साफ करतो.
- ii. कोणत्याही परीक्षेत गैरहजर राहणे हे त्या विषयाच्या परीक्षेत शून्य गुण मिळालेल्या विद्यार्थ्याला मानले जाते.
- iii. मूल्यमापन सरासरी वेटेज प्रणालीवर आधारित आहे. प्रत्येक विषयाला आवश्यक अभ्यासाच्या तासांवर आधारित क्रेडिट पॉइंट्स असतात.
- iv. प्रत्येक विद्यार्थ्याचे सत्रीय काम आणि सेमिस्टर परीक्षेला समान महत्त्व असलेल्या विषयात मूल्यमापन केले जाते, ज्यामुळे विद्यार्थी नियमितपणे अभ्यास करतात.
- v. प्रत्येक विद्यार्थ्याला प्रत्येक विषयातील कमाल 10 गुणांपैकी ग्रेड गुण दिले जातात. (10 पॉइंट स्केलवर आधारित).
- vi. प्रत्येक विषयात मिळालेल्या ग्रेड गुणांच्या आधारे, सेमिस्टर ग्रेड पॉइंट सरासरी (SGPA) आणि नंतर संचयी ग्रेड पॉइंट सरासरी (CGPA) ची गणना परिशिष्ट-II नुसार केली जाते.

3.6 डिप्लोमा पुरस्कार:

- i. विद्यार्थ्यांनी जास्तीत जास्त सहा (6) वर्षांच्या कालावधीत क्रेडिट्सची किमान आवश्यकता पूर्ण करणे आवश्यक आहे आणि डिप्लोमा पुरस्कारासाठी पात्र होण्यासाठी अभ्यासक्रमात किमान CGPA 4.0 प्राप्त करणे आवश्यक आहे.
- ii. हा डिप्लोमा डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबादतर्फे दिला जातो.
- iii. त्याला महाराष्ट्र शासनाच्या तंत्रशिक्षण संचालनालयानेही मान्यता दिली आहे.
- iv. याला MSBTE कडून समतुल्यता प्राप्त झाली आहे ज्यामध्ये असे म्हटले आहे की इलेक्ट्रॉनिक्स उत्पादन आणि देखभाल डिप्लोमा MSBTE अभ्यासक्रमानुसार इलेक्ट्रॉनिक्स अभियांत्रिकी डिप्लोमाच्या समतुल्य आहे.

3.7 शिष्यवृत्ती

राज्य सरकारच्या नियमांनुसार शिक्षण शुल्क आणि देखभाल भत्त्याच्या प्रतिपूर्तीसाठी केंद्र / राज्य सरकारच्या शिष्यवृत्तीसाठी अर्ज करण्यासाठी संस्था सर्व विद्यार्थ्यांना, विशेषतः राखीव श्रेणीतील विद्यार्थ्यांना प्रोत्साहित करते आणि मदत करते.

3.8 प्लेसमेंट मध्ये सहाय्य / उच्च अभ्यासाचा पाठपुरावा:

केंद्राचा प्लेसमेंट सेल विद्यार्थ्यांना रोजगार/स्वयंरोजगारासाठी सर्व सहाय्य प्रदान करतो | केंद्रातून उत्तीर्ण होणाऱ्या जवळपास सर्वच विद्यार्थ्यांना करिअरच्या चांगल्या संधी मिळत आहेत.



4.0 बी.टेक. इलेक्ट्रॉनिक्स अभियांत्रिकीमध्ये (सिस्टम अभियांत्रिकीमधील विशेषीकरण)

हा अभ्यासक्रम उद्योगाशी तात्काळ संबंधित विषयांची श्रेणी देतो आणि विद्यार्थ्यांना इलेक्ट्रॉनिक्स उत्पादन विकासामध्ये गुंतलेल्या उद्योगांसाठी अगदी योग्य बनवतो. या वेगाने विकसनशील क्षेत्रात अनुप्रयोग संशोधनाचा पाठपुरावा करू शकतो. या अभ्यासक्रमाच्या अभ्यासक्रमात इलेक्ट्रॉनिक उत्पादनाच्या नवकल्पना, डिझाइन आणि विकासावर भर देण्यात आला आहे.

4.1 प्रथम वर्ष प्रवेशासाठी पात्रता

NIELIT Aurangabad द्वारे ऑफर केल्या जाणाऱ्या B.Tech (इलेक्ट्रॉनिक्स इंजिनीअरिंग (सिस्टम अभियांत्रिकीमध्ये स्पेशलायझेशन)) प्रवेश भारत सरकारच्या वतीने राष्ट्रीय चाचणी अकादमी (NTA) द्वारे आयोजित JEE (मुख्य) च्या आधारावर केला जाईल. संयुक्त जागा वाटप प्राधिकरण (JoSAA) / केंद्रीय जागा वाटप मंडळ (CSAB) या अभ्यासक्रमासाठी केंद्रीकृत प्रवेश आयोजित करेल. अधिक तपशिलांसाठी उमेदवारांनी कृपया <http://josaa.nic.in/> आणि <http://csab.nic.in/> ला भेट द्यावी.

संयुक्त जागा वाटप प्राधिकरण (JoSAA) बद्दल

IITs, ISM, NITs, IIITs आणि इतर-सरकारी अनुदानित तांत्रिक संस्था (इतर GFTIs) मधील प्रवेशांसाठी संयुक्त जागा वाटपाचे व्यवस्थापन आणि नियमन करण्यासाठी मनुष्यबळ विकास मंत्रालयाने (MHRD) संयुक्त जागा वाटप प्राधिकरण (JoSAA) ची स्थापना केली आहे. या संस्थांद्वारे दिल्या जाणाऱ्या सर्व शैक्षणिक कार्यक्रमांचे प्रवेश एकाच व्यासपीठाद्वारे केले जातात.

a. पात्र उमेदवाराला नोंदणी करावी लागेल आणि JoSAA अंतर्गत शैक्षणिक कार्यक्रमासाठी निवडी भराव्या लागतील.

b. JoSAA द्वारे निवडलेल्या उमेदवारांना स्वीकारण्यासाठी आणि संस्थेत प्रवेश देण्यासाठी कागदपत्रांची पडताळणी केली जाईल.

c. केंद्रीय जागा वाटप मंडळाद्वारे विशेष रिक्त जागा भरण्याची फेरी (विशेष फेरी) घेतली जाईल. अधिक माहितीसाठी कृपया भेट द्या: <http://csab.nic.in>

d. NIELIT औरंगाबादला वाटप केलेल्या उमेदवाराने या संस्थेला सर्व-मूळ प्रमाणपत्रांसह आणि प्रत्येक कागदपत्रांच्या झेरॉक्स प्रतीसह अहवाल द्यावा ज्याची उमेदवाराने विहित कालावधी आणि वेळेत अहवाल केंद्रावर पडताळणी केली होती.

ई. उमेदवाराला फरकाची रक्कम (if any) NEFT/ RTGS द्वारे जमा करावी लागेल

४.१.१ Seat मॅट्रिक्स

4.1.1.1. B.Tech मध्ये एकूण 60* जागा उपलब्ध आहेत. वर्गवार सीट मॅट्रिक्ससाठी कृपया <http://josaa.nic.in/> आणि <http://csab.nic.in/> ला भेट द्या.

*वर दर्शविलेल्या जागांची संख्या तात्पुरती आहे आणि वेळोवेळी दिलेल्या सरकारी आदेशांनुसार बदलल्या जातील.

परदेशात शिकणाऱ्या विद्यार्थ्यांसाठी 9 जागांचा वेगळा कोटा आहे. इच्छुक उमेदवारांना DASA (<https://www.dasanit.org/>) द्वारे प्रवेश घ्यावा लागेल.

4.2 B.Tech इलेक्ट्रॉनिक्स अभियांत्रिकी (प्रणाली अभियांत्रिकीमधील स्पेशलायझेशन) मध्ये पार्श्व प्रवेश (थेट द्वितीय वर्ष)

4.2.1 किमान पात्रता निकष

इलेक्ट्रॉनिक्स आणि संबंधित प्रवाहातील तीन वर्षांचा डिप्लोमा किमान 45% गुणांसह (SC/ST उमेदवारांसाठी 40 टक्के) B.Tech मध्ये पार्श्व प्रवेशासाठी (थेट द्वितीय वर्ष) पात्र आहेत.

4.2.2 Seat मॅट्रिक्स

रिक्त जागा (असल्यास)

टीप: भारत सरकारच्या नियम, AICTE आणि/ किंवा विद्यापीठाच्या मान्यतेनुसार open, OBC, SC, ST आणि PWD जागा राखीव आहेत.

4.2.3 Lateral Entry प्रवेशासाठी निवड प्रक्रिया

- किमान पात्रता निकष पूर्ण करणारे उमेदवारच प्रवेशासाठी पात्र असतील.
- बी.टेकमध्ये लॅटरल एंट्रीसाठी (थेट द्वितीय वर्ष) प्रवेश असेल NIELIT औरंगाबाद द्वारा आयोजित लेखी परीक्षेद्वारे.
- पात्र उमेदवारांना अर्ज ऑनलाइन डाउनलोड करावा लागेल आणि तो ईमेल आयडी: btech-abad@nielit.gov.in वर सबमिट करावा लागेल.
- अर्ज शुल्क रु. 500/- नॉन-रिफंडेबल असेल. तथापि, SC/ST/PWD च्या उमेदवारांना अर्ज शुल्कातून सूट देण्यात आली आहे.
- द्वितीय वर्ष बी. टेक (लॅटरल एंट्री) प्रवेशासाठी गुणवत्ता यादी लेखी परीक्षेतील गुणांवर आणि/ किंवा इलेक्ट्रिकल/ इलेक्ट्रॉनिक्स आणि संबंधित प्रवाहातील डिप्लोमामधील गुणांवर आधारित असेल.
- निवडलेल्या उमेदवारांचा प्रवेश त्यांच्या कागदपत्रांची पडताळणी आणि लागू शुल्क भरण्याच्या अधीन असेल.
- द्वितीय वर्ष बी. टेक (लॅटरल एंट्री) च्या प्रवेशासाठी (वर्ष 2022-23) उमेदवारांची श्रेणीनिहाय मुख्य यादी (निवडलेली) आणि प्रतीक्षा यादी केवळ संस्थेच्या वेबसाइट आणि सूचना फलकावर प्रदर्शित केली जाईल.
- निवडलेल्या उमेदवारांच्या यादीसह प्रतीक्षा यादीच्या समुपदेशनाची तारीख आणि वेळ देखील घोषित केली जाईल. प्रतीक्षा यादीतील सर्व उमेदवारांनी प्रतीक्षा यादी समुपदेशनाच्या वेळी स्वतःला उपलब्ध करून द्यावे, अन्यथा त्यांचा दावा जप्त केला जाईल.
- प्रतीक्षा यादी समुपदेशनाच्या वेळी उपलब्ध असलेल्या प्रतीक्षा यादीतील उमेदवारांना श्रेणीनिहाय प्रतीक्षा यादीच्या गुणवत्तेनुसार तात्पुरते प्रवेश दिला जाईल.
- मुख्य यादी आणि प्रतीक्षा यादीतील निवडलेल्या उमेदवारांनी कागदपत्रांसह अधिसूचित केल्यानुसार आणि section 10.1 मध्ये नमूद केल्यानुसार प्रवेशासाठी शुल्क भरून नोंदणी करणे आवश्यक आहे, अन्यथा त्यांचा दावा जप्त केला जाऊ शकतो.

4.3 सुरक्षित प्रवेश रद्द झाल्यास प्रवेश प्रक्रिया पूर्ण झाल्यानंतर कोणतीही जागा रिक्त असल्यास, खाली नमूद केलेल्या प्रक्रियेनुसार रिक्त जागा भरल्या जाऊ शकतात:

- सामाईक गुणवत्ता यादीतील क्रमवारीनुसार प्राधान्य दिले जाईल.
- गुणवत्ता यादीतील रॅकिंगनुसार निवडलेले विद्यार्थी, जे कायदेशीर आणि/किंवा खऱ्या कारणामुळे प्रवेशासाठी केंद्रापर्यंत पोहोचू शकले नाहीत आणि संस्थेशी संपर्क साधू शकले नाहीत, त्यांना प्रथम रिक्त जागा भरण्यासाठी विचारात घेतले जाईल.
- (i) आणि (ii) नंतर, ज्या उमेदवारांना प्रवेशाची ऑफर दिली गेली नाही आणि जवळ येत आहेत, त्यांचा विचार केला जाऊ शकतो.

4.4 शैक्षणिक दिनदर्शिका - संदर्भ -I पहा

4.5 पहिल्या वर्षासाठी (2022-2023) शिक्षण योजना:

- प्रत्येक विद्यार्थ्याने खाली नमूद केल्याप्रमाणे सेमिस्टरच्या सर्व विषयांसाठी नोंदणी करणे आवश्यक आहे.
- तपशीलवार अभ्यासक्रम <https://www.nielit.gov.in/aurangabad> येथून डाउनलोड करता येईल.

4.6 टर्म कोर्स लोड:

प्रत्येक सेमिस्टरमध्ये 30 क्रेडिट्स असतात. अभ्यासक्रमाच्या कालावधीत, विद्यार्थ्याला पुढील सेमिस्टरमध्ये प्रवेश घेण्यासाठी काही विषयांमध्ये उत्तीर्ण होणे आवश्यक आहे.

4.7 मूल्यांकन:

- एखाद्या विद्यार्थ्याला सेमिस्टर परीक्षेला बसण्याची परवानगी आहे की त्याची थिअरी आणि प्रॅक्टिकल क्लासेसमध्ये किमान 75% उपस्थिती असेल, त्याने त्याच्या/तिच्या सर्व सत्रीय असाइनमेंट पूर्ण केल्या असतील आणि त्याची सर्व देय रक्कम भरली असेल.
- कोणत्याही परीक्षेत गैरहजर राहणे हे त्या विषयाच्या परीक्षेत शून्य गुण मिळालेल्या विद्यार्थ्याला मानले जाते.
- मूल्यमापन सरासरी वेटेज प्रणालीवर आधारित आहे. प्रत्येक विषयाला आवश्यक अभ्यासाच्या तासांवर आधारित क्रेडिट पॉइंट्स असतात.
- प्रत्येक विद्यार्थ्याचे मूल्यमापन विषय, सत्रातील काम आणि सेमिस्टर परीक्षेत केले जाते, ज्यामुळे विद्यार्थी नियमितपणे अभ्यास करतात.
- ई. प्रत्येक विद्यार्थ्याला प्रत्येक विषयातील कमाल 10 गुणांपैकी ग्रेड गुण दिले जातात. (10 पॉइंट स्केलवर).
- फ. प्रत्येक विषयात मिळालेल्या ग्रेड पॉइंट्सच्या आधारे, सेमिस्टर ग्रेड पॉइंट अॅव्हरेज (SGPA) आणि नंतर Cumulative Grade Point Average (CGPA) ची गणना केली जाते.

टीप: SGPA CGPA च्या गणनेसाठी, परिशिष्ट- II पहा.

4.8 पदवी पुरस्कार:

विद्यार्थ्याने जास्तीत जास्त आठ (8) वर्षांच्या कालावधीत क्रेडिटची किमान आवश्यकता पूर्ण करणे आवश्यक आहे आणि पदवी पुरस्कारासाठी पात्र होण्यासाठी अभ्यासक्रमात किमान CGPA 4.0 प्राप्त करणे आवश्यक आहे. ही पदवी डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबाद यांनी दिली आहे.

4.9 शिष्यवृत्ती

संस्था सर्व विद्यार्थ्यांना, विशेषतः राखीव श्रेणीतील, केंद्र/राज्य सरकारसाठी अर्ज करण्यासाठी प्रोत्साहित करते आणि मदत करते. राज्य सरकारच्या नियमांनुसार शिक्षण शुल्क आणि देखभाल भत्त्याच्या प्रतिपूर्तीसाठी शिष्यवृत्ती.

4.10 प्लेसमेंट मध्ये सहाय्य / उच्च अभ्यासाचा पाठपुरावा:

संस्थेचा प्लेसमेंट सेल विद्यार्थ्यांना रोजगार/स्वयंरोजगारासाठी सर्व मदत पुरवतो. संस्थेतून उत्तीर्ण झालेल्या जवळपास सर्वच विद्यार्थ्यांना करिअरच्या चांगल्या संधी उपलब्ध होत आहेत

5.0 मास्टर ऑफ टेक्नॉलॉजी (इलेक्ट्रॉनिक्स डिझाईन आणि टेक्नॉलॉजी)

[एम. टेक (EDT)] पूर्णवेळ अभ्यासक्रम

डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबाद (MS) शी संलग्न AICTE मान्यताप्राप्त पदव्युत्तर अभ्यासक्रम हा चार सेमिस्टरचा (2 वर्षे) आहे.

5.1 पात्रता/निवड प्रक्रिया

M. Tech ला प्रवेश. पदवी अभ्यासक्रम केंद्रीकृत समुपदेशन किंवा M.Tech (CCMT) 2022 द्वारे केले जातील. तपशीलांसाठी, कृपया <https://ccmt.admissions.nic.in/> ला भेट द्या

५.२ सेवन (२८ जागा)

दोन जागा उद्योग प्रायोजित उमेदवारांसाठी राखीव आहेत आणि तीन जागा अनिवासी भारतीय (NRI) / भारतीय वंशाच्या व्यक्ती (PIO) / आखाती देशांमध्ये (CIWG) कोट्यातील भारतीय कामगारांच्या मुलांसाठी राखीव आहेत.

जागांचे वितरण खालीलप्रमाणे आहे.

Table no. 06

Seat Matrix	
Open	9
Open- PWD	1
EWS	2
OBC	6
SC	3
ST	2
Industry Sponsored	2
Foreign Nationals	3
Total	28

महत्वाचे

- भारत सरकारच्या नियमानुसार, AICTE आणि/किंवा विद्यापीठाच्या मान्यतेनुसार जागा आरक्षित आहेत.
- SC उमेदवारांसाठी 15%, ST उमेदवारांसाठी 7.5% आणि इतर मागासवर्गीयांसाठी 27% कोटा राखीव आहे
- PWD कायदा 1995 नुसार अपंग व्यक्तींच्या कोट्यावर (5%) निवडलेल्या उमेदवारांना योग्य श्रेणीमध्ये ठेवले जाते उदा. SC/ST/OBC/ open ते आरक्षणासाठी असलेल्या रोस्टरमध्ये कोणत्या श्रेणीशी संबंधित आहेत यावर अवलंबून.

5.3 प्रथम वर्ष एम. टेक (EDT) पूर्णवेळ प्रवेशासाठी निवड प्रक्रिया

अ) राष्ट्रीय अर्जदार

- प्रवेशासाठी, कृपया <https://ccmt.admissions.nic.in/> ला भेट द्या
- CCMT 2022 च्या सर्व फे-यांनंतरच्या रिक्त जागा अर्जाच्या पावतीच्या (प्रथम येणाऱ्यास प्रथम सेवा तत्वावर) भरल्या जातील (अनुबंध VI). तथापि, रिक्त जागांच्या संदर्भात काही विसंगती आढळल्यास, कार्यकारी संचालक, NIELIT औरंगाबाद यांचा निर्णय अंतिम आणि सर्वाना बंधनकारक असेल.

ब) आंतरराष्ट्रीय अर्जदार

- परदेशी नागरिकांचा प्रवेश भारत सरकारने वेळोवेळी दिलेल्या मार्गदर्शक तत्वांच्या अधीन आहे.
- भारतीय वंशाच्या व्यक्ती (पीआयओ) ही पाकिस्तान आणि बांगलादेश वगळता परदेशी नागरिकत्व असलेली, "एनआरआय" दर्जा नसलेली, प्रवेशासाठी अर्ज करताना तसेच अभ्यासाच्या कालावधीत परदेशी पासपोर्ट धारण केलेली व्यक्ती आहे आणि ती स्वतः किंवा कोणीही आहे, त्याचे/तिचे आई-वडील किंवा कोणीही, त्याचे/तिचे आजी-आजोबा दोघेही भारतीय नागरिक आहेत/होते.

- iii. गल्फ कंट्रीज (CIWG) मधील भारतीय कामगारांची मुले ही संबंधित वर्किंग व्हिसाखाली आखाती देशांमध्ये काम करणाऱ्या भारतीयाची मुले आहेत.
- iv अनिवासी भारतीय (NRI) उमेदवार हा आयकर कायद्याच्या कलम 6 अंतर्गत परिभाषित केल्यानुसार 'NRI दर्जा' असलेल्या व्यक्तीचे मूल/वॉर्ड आहे.
- v. परदेशी नागरिक योग्य चॅनेलद्वारे किमान पात्रता आवश्यकता पूर्ण करण्याच्या अधीन M. Tech (EDT) पूर्णवेळ अभ्यासक्रमाच्या प्रवेशासाठी अर्ज करू शकतात.
- vi त्यांच्या अर्जाचा, तथापि, अर्ज मिळाल्यावर स्वतंत्रपणे विचार केला जाईल, ज्याचा उल्लेख प्रथम सह प्रथम सेवा तत्वावर परिशिष्ट-IV (A) मध्ये केला आहे.
- vii परदेशी नागरिकांनी पात्रता सह प्रवेश (ANNEXURE-IV(A)) आणि घोषणा आणि उपक्रमाचे स्वरूप (ANNEXURE-IV(B)) साठी अर्ज फॉर्म डाउनलोड करून सबमिट करणे आवश्यक आहे आणि त्यासोबत रु.5000/- किंवा समतुल्य USD (न परतावा नाही).

5.4 शैक्षणिक दिनदर्शिका - संदर्भ -I पहा

5.5 पहिल्या वर्षासाठी (2022-23) शिक्षण योजना:

- i. प्रत्येक विद्यार्थ्याने सेमिस्टरच्या सर्व विषयांसाठी नोंदणी करणे आवश्यक आहे.
- ii तपशीलवार अभ्यासक्रम <https://www.nielit.gov.in/aurangabad> येथून डाउनलोड करता येईल.

5.6 टर्म कोर्स लोड:

- i. प्रत्येक सेमिस्टरमध्ये, विषयाचा भार प्रति सेमिस्टर 11 ते 21 क्रेडिट्स पर्यंत बदलतो.

5.7 मूल्यांकन:

- i. एखाद्या विद्यार्थ्याला सेमिस्टर परीक्षेला बसण्याची परवानगी: त्याची थिअरी आणि प्रात्यक्षिक वर्गांमध्ये किमान उपस्थिती 75% आहे, त्याने त्याच्या/तिच्या सर्व सत्रीय असाइनमेंट पूर्ण केल्या आहेत आणि त्याची सर्व देय रक्कम भरली आहे.
- ii. कोणत्याही परीक्षेत गैरहजर राहणे हे विद्यार्थ्याने त्या विषयाच्या शेवटच्या सत्राच्या परीक्षेत शून्य गुण प्राप्त केले आहे असे मानले जाते.
- iii. मूल्यमापन सरासरी वेटेज प्रणालीवर आधारित आहे. प्रत्येक विषयाला आवश्यक अभ्यासाच्या तासांवर आधारित क्रेडिट पॉइंट्स असतात.
- iv. प्रत्येक विद्यार्थ्याला प्रत्येक विषयातील जास्तीत जास्त 10 गुणांपैकी ग्रेड गुण दिले जातात. (10 पॉइंट स्केलवर आधारित).
- v. प्रत्येक विषयात मिळालेल्या ग्रेड गुणांवर आधारित, सेमिस्टर ग्रेड पॉइंट सरासरी (SGPA) आणि नंतर संचयी ग्रेड पॉइंट सरासरी (CGPA) ची गणना परिशिष्ट-II नुसार केली जाते.

5.8 PG पदवी पुरस्कार:

पदवीसाठी पात्र ठरण्यासाठी विद्यार्थ्याने M.Tech अध्यादेश आणि नियमांनुसार कमाल चार (04) वर्षांच्या कालावधीत क्रेडिटची किमान आवश्यकता पूर्ण करणे आवश्यक आहे. डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबाद (एमएस) द्वारे पीजी पदवी प्रदान केली जाते.

5.9 शिष्यवृत्ती

- i. गैरगैर-प्रायोजित विद्यार्थी, M Tech (EDT) कोर्समध्ये प्रवेश घेतलेले, वैध GATE SCORE असलेले, AICTE नवी दिल्ली कडून रक्कम मंजूर करण्याच्या अधीन राहून, शिष्यवृत्तीसाठी पात्र आहेत | पीजी-शिष्यवृत्तीचे वितरण हे एआयसीटीई पोर्टलद्वारे थेट रोख हस्तांतरण योजनेद्वारे केले जाते ज्यासाठी त्याने अर्ज केला असेल आणि अटी पूर्ण केल्या असतील. शिष्यवृत्तीचा पुरस्कार आणि त्याचे सातत्य नियमित उपस्थितीच्या अधीन आहे, समाधानकारक प्रगती, चांगले आचरण आणि संस्थेच्या नियमांचे पालन.

- ii. प्रायोजित विद्यार्थी किंवा वैध GATE SCORE नसलेले विद्यार्थी या शिष्यवृत्तीसाठी पात्र नाहीत.
- iii. ट्यूशन फी फक्त SC/ST उमेदवारांसाठी सूट दिली जाते, त्यांनी त्यासाठी अर्ज केला असेल आणि अटी पूर्ण केल्या असतील.
- iv. प्रत्येक विद्यार्थ्याने, M. Tech (EDT) मध्ये प्रवेश मंजूर केला आहे आणि शिष्यवृत्ती दिली आहे, त्याला नियुक्त केलेल्या अध्यापन आणि संशोधन क्रियाकलापांशी संबंधित काम करणे बंधनकारक आहे.

5.10 प्लेसमेंट मध्ये सहाय्य / उच्च अभ्यासाचा पाठपुरावा:

केंद्राचा प्लेसमेंट सेल विद्यार्थ्यांना रोजगार/स्वयंरोजगारासाठी सर्व सहाय्य प्रदान करतो. केंद्रातून उत्तीर्ण होणाऱ्या जवळपास सर्वच विद्यार्थ्यांना करिअरच्या चांगल्या संधी मिळत आहेत.

6.0 मास्टर ऑफ टेक्नॉलॉजी (इलेक्ट्रॉनिक्स डिझाईन आणि टेक्नॉलॉजी)

[एम. टेक (EDT)] पार्टटाइम अभ्यासक्रम

डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबाद (एमएस) द्वारे प्रदान केलेल्या पदवीसह कार्यरत व्यावसायिकांसाठी हा सहा सेमिस्टरचा (3 वर्षे) AICTE मान्यताप्राप्त अभ्यासक्रम आहे.

6.1 पात्रता

- किमान 55% गुणांसह मान्यताप्राप्त विद्यापीठातून इलेक्ट्रॉनिक्स/ इलेक्ट्रिकल/ टेलिकम्युनिकेशन/ इंस्ट्रुमेंटेशन अभियांत्रिकी इ. मध्ये B.E./ B. टेक पदवी किंवा समकक्ष.
- उमेदवाराने B.E./ B. Tech ची पदवी पूर्ण केल्यानंतर किमान दोन वर्षे इलेक्ट्रॉनिक उत्पादन किंवा सिस्टम डेव्हलपमेंटमध्ये गुंतलेल्या शैक्षणिक संस्था/ उद्योग/ R&D संस्थेमध्ये सेवा करत असावी
- उमेदवाराला नियुक्त्याने प्रायोजित केले पाहिजे.
- त्याचे/तिचे कामाचे ठिकाण संस्थेपासून ६० किमी अंतरावर असावे.
- त्याने परिशिष्ट XII मध्ये दिलेल्या विहित नमुन्यातील अर्जासोबत आवश्यक प्रायोजकत्व प्रमाणपत्र सादर करावे.

6.2 सेवन: 24 जागा

6.3 निवड प्रक्रिया

- पात्रता निकष पूर्ण करणारे उमेदवारच प्रवेशासाठी पात्र असतील.
- पात्र उमेदवारांना अर्जाचा फॉर्म (ANNEXURE VI) भरावा लागेल आणि तो ईमेल आयडीवर सबमिट करावा लागेल: mtech-abad@nielit.gov.in
- M.Tech (EDT) पार्टटाइम कोर्ससाठी प्रवेशासाठी निवड, लेखी परीक्षेतील गुणांवर आधारित असेल. शॉर्टलिस्ट केलेल्या उमेदवारांची यादी संस्थेच्या वेबसाइटवर प्रदर्शित केली जाईल.
- निवडलेल्या उमेदवारांचा प्रवेश त्यांच्या कागदपत्रांची पडताळणी आणि लागू शुल्क भरण्याच्या अधीन असेल.
- निवडलेल्या उमेदवारांच्या यादीसह प्रतीक्षा यादी समुपदेशनाची तारीख आणि वेळ देखील घोषित केली जाईल. प्रतीक्षा यादीतील सर्व उमेदवारांनी प्रतीक्षा यादी समुपदेशनाच्या वेळी स्वतःला उपलब्ध करून द्यावे, अन्यथा त्यांचा दावा जप्त केला जाईल.
- प्रतीक्षा यादीतील समुपदेशनाच्या वेळी उपलब्ध असलेल्या प्रतीक्षा यादीतील उमेदवारांना श्रेणीनिहाय प्रतीक्षा यादीच्या गुणवत्तेनुसार तात्पुरते प्रवेश दिला जाईल.
- निवडलेल्या गुणवत्ता आणि प्रतीक्षा यादीतील उमेदवारांनी दस्तऐवजासह अधिसूचित केल्यानुसार आणि section 10.1 मध्ये नमूद केल्यानुसार प्रवेशासाठी शुल्क भरून नोंदणी करणे आवश्यक आहे, अन्यथा त्यांचा दावा जप्त केला जाऊ शकतो.
- निवड आणि प्रवेश बंद करण्याबाबत कार्यकारी संचालक, NIELIT, औरंगाबाद यांचा निर्णय अंतिम असेल.

6.4 सुरक्षित प्रवेश रद्द झाल्यास प्रवेश

प्रवेश प्रक्रिया पूर्ण झाल्यानंतर नोंदणीकृत विद्यार्थ्यांनी अभ्यासक्रम सोडल्यामुळे कोणतीही रिक्त जागा उद्भवल्यास, खाली दिलेल्या प्रक्रियेनुसार कार्यकारी संचालकांच्या दिशानुसार रिक्त जागा भरल्या जाऊ शकतात:

- सामाईक गुणवत्ता यादीतील क्रमवारीनुसार प्राधान्य दिले जाईल.
- गुणवत्ता यादीतील रँकिंगनुसार निवडलेले विद्यार्थी, जे कायदेशीर आणि/किंवा खऱ्या कारणामुळे प्रवेशासाठी केंद्रापर्यंत पोहोचू शकले नाहीत आणि संस्थेशी संपर्क/संपर्क साधू शकले नाहीत, त्यांना प्रथम रिक्त जागा भरण्यासाठी विचारात घेतले जाते.

iii. (i) आणि (ii) नंतर, ज्या उमेदवारांना प्रवेशाची ऑफर दिली गेली नाही आणि जवळ येत आहेत, त्यांचा विचार केला जाऊ शकतो.

6.5 शैक्षणिक दिनदर्शिका - संदर्भ -I पहा

6.6 सूचना योजना:

- प्रत्येक विद्यार्थ्याने सेमिस्टरच्या सर्व विषयांसाठी नोंदणी करणे आवश्यक आहे.
- तपशीलवार अभ्यासक्रम <https://www.nielit.gov.in/aurangabad> येथून डाउनलोड करता येईल.

6.7 टर्म कोर्स लोड:

- प्रत्येक सेमिस्टरमध्ये, विषयाचा भार प्रति सेमिस्टर 10 ते 20 क्रेडिट्स पर्यंत बदलतो.

6.8 मूल्यांकन:

- एखाद्या विद्यार्थ्याला सेमिस्टर परीक्षेला बसण्याची परवानगी: त्याची थिअरी आणि प्रात्यक्षिक वर्गामध्ये किमान उपस्थिती 75% आहे, त्याने त्याच्या/तिच्या सर्व सत्रीय असाइनमेंट पूर्ण केल्या आहेत आणि त्याची सर्व देय रक्कम भरली आहे.
- कोणत्याही परीक्षेत गैरहजर राहणे हे विद्यार्थ्याने त्या विषयाच्या शेवटच्या सत्राच्या परीक्षेत शून्य गुण प्राप्त केले आहे असे मानले जाते.
- मूल्यमापन सरासरी वेटेज प्रणालीवर आधारित आहे. प्रत्येक विषयाला आवश्यक अभ्यासाच्या तासांवर आधारित क्रेडिट पॉइंट्स असतात.
- प्रत्येक विद्यार्थ्याला प्रत्येक विषयातील कमाल 10 गुणांपैकी ग्रेड गुण दिले जातात. (10 पॉइंट स्केलवर आधारित).
- प्रत्येक विषयात मिळालेल्या ग्रेड गुणांवर आधारित, सेमिस्टर ग्रेड पॉइंट सरासरी (SGPA) आणि नंतर संचयी ग्रेड पॉइंट सरासरी (CGPA) ची गणना परिशिष्ट-II नुसार केली जाते.

6.9 PG पदवी पुरस्कार:

पदवीसाठी पात्र ठरण्यासाठी विद्यार्थ्याने M.Tech अध्यादेश आणि नियमांनुसार कमाल सात (07) वर्षांच्या कालावधीत क्रेडिटची किमान आवश्यकता पूर्ण करणे आवश्यक आहे. डॉ. बाबासाहेब आंबेडकर मराठवाडा विद्यापीठ, औरंगाबाद (एमएस) द्वारे पीजी पदवी प्रदान केली जाते.

7.0 Partial List of Teaching Staff

Table no. 07

Sr No	Name of Faculty	Designation	Educational Qualification	Brief Profile
1.	Dr. Sanjeev Kumar Gupta	Executive Director	Ph.D. (Computer Engg.), MS (Software Systems) and B.Tech (Computer Engg.)	Alumnus of BITS Pilani and NIT Kurukshetra, he is a renowned engineer, technocrat and administrator. In his professional career he is also instrumental in automation of various organizations such as Punjab State Power Corporation Limited, HSEB, CHB, ICSI, Labor Bureau. His research work is spanned across various aspects of Wireless Sensor Network. His other areas of Interest include Web Application Development, Mobile Application Development, Software Engineering, IoT, Blockchain, Big Data & Cyber Security
2.	Dr. Y. Jayanta Singh	Director	PhD (Computer Science)	Overall 18 years of experience in research and teaching post doctorate. He is involved in Data Warehousing & Mining, Cloud Computing, Data Science & Artificial Intelligence [AI], Blockchain Technologies, Digital Image Processing, Agile Software Development and Testing.
3.	Dr. Manish Arora	Additional Director (Systems)	PhD (Computer Science)	Experienced Software Professional. Currently working on Big Data, e-governance etc.
4.	Dr. JayarajKidav	Scientist/Engineer 'E'	Ph.D.(Electronics & comm. Engg-VLSI Signal Processing)	Overall 18 years of experience in research and teaching. He is a principal investigator in Ultrasound System Development Project, SMDP-C2SD Project and CARS/NRB Project
5.	Sh. Sasi Kumar Gera	Dean (Academics)& Scientist E	M.Tech (Manuf. Engg) , B.Tech(Mech.Engg)	Alumnus of IIT Madras, in his professional career spanned over 24 years he is instrumental in initiating many out-of-box research works in areas of CAD/CAM, CNC Machines, Industrial Robots Machine vision, Industrial Design. He has implemented real-time robot path control by using image processing for seam-less welding applications at the University of Liverpool, England as research associate (UNDP/UNIDO

Sr No	Name of Faculty	Designation	Educational Qualification	Brief Profile
				fellowship). He has guided several projects at Masters level. His research interests are Industry 4.0, Autonomous Robots, CAD/CAM, Lean Manufacturing.
6.	Smt. Vimala Mathew	Scientist/Engineer 'E'	B.Tech(CSE) and DOEACC 'C' Level	Experienced Software Professional. Currently working on Machine Learning, Big data Analytics etc.
7.	D. Rama Rao	Scientist/Engineer-D	B.Tech (Electronics & Communications). M.Tech(Electronic Design Technology)	Overall 33 years of Experience which including 25 years of Teaching Experience to Diploma, Under Graduate, Post Graduate & Post Graduate Diploma Courses. * Subjects taught in the fields of Analog & Digital Electronics, Electronics, Measurements & Instrumentation, Service & Maintenance, Microprocessors & Microcontrollers and Linux Device Drivers. * Guided Academic Projects of the students of Diploma, Under Graduate, Post Graduate & Post Graduate Diploma Courses * Worked on Industry Sponsored & In-house R&D Projects.
8.	Sh. Lakshman	Dean (Skill Development), and Scientist D	M.Tech, B. Tech. (CSE)	In his professional career spanning over 12 years, he has executed many Government Projects of IT Mission (Kerala), Ministry of Social Justices, and ISEA project etc. His areas of areas of interest are Thermal Image Processing, Blockchain, Cyber Security, Mobile Application Development and Software Engineering. He is also working as a Placement Officer and Nodal Officer of Model Career Centre.
9.	Sh. Y. A. Khan	Sr. Principal Programmer	M.C.A.	Experienced Software Professional. Developed the projects for Office Automation. Well versed with Database techniques and Software Engineering Practices.
10.	Sh. D.S. Raje	Scientist C	B.E.(Electronics)	He has over 27 years of experience as Electronics Engineer and excels in the field of Test and Measurement.
11.	Sh. SaurabhKesari	Scientist C	B.Tech (Electronics and Tele-	More than 6 years of industrial experience in Embedded and IOT

Sr No	Name of Faculty	Designation	Educational Qualification	Brief Profile
			Communication Engineering)	Hardware design and development
12.	Chaitanya Narayan Kadadas	Scientist C	B.E.(Electronics and Tele-Communication Engineering)	Having 7 Years' experience in fiber optics communication and networking
13.	Sh. SaurabhBansod	Scientist C	M.Tech(Electronics & Instrumentation) B.E.(Electronics)	Alumnus of N.I.T. Rourkela, In the span of 6 years he has done tremendous work in the area of Industrial Automation. His area of interest includes data acquisition using NI DAQ cards, Machine Learning on Embedded Systems.
14.	Sh. Prashant Pal	Scientist B	M.Tech, B.Tech	With expertise in Electronics System design, he has experience of teaching advanced microcontroller and microprocessor. He has sound knowledge of Deep learning and Machine learning. He is also doing projects on Artificial Intelligence, Visual Information and Embedded Systems from IIT Kharagpur.
15.	Sh. Yogesh	Scientist 'B'	B.Tech (CSE)	A young scientist who possesses expertise in the area of Internet of Things, Scripting languages like Python. His other areas of interest are Cyber Security, Web Application Development.
16.	Sh. Shashank Kumar Singh	Scientist 'B'	B.Tech. (Electronics and Communication Engineering)	A young scientist who possesses expertise in the area of VLSI. More than 1 years of teaching experience.
17.	Sh. B. B. Sorte	Sr. Tech. Officer	DME, (DCS&M)	More than 20 Years' experience as Trainer / faculty of CAD/CAM/CAE including 08 Years of experience as Faculty for Mechanical Design and Developments and Workshop Technology. He is looking after Design & Development of Electro- Mechanical products under Academic activities of DEPM &B.Tech&M.Tech courses
18.	Sh. M.S.Kshirsagar	Sr. Tech. Officer	Diploma (Ind. Electronics)	He is highly motivated and experienced faculty who specializes in Electronics.
19.	Sh. MilindGarud	Sr. Tech. Officer	Diploma (Ind. Electronics)	About 29 Years' experience in Power Electronics and conducting of lab/practical's. He is also looking after the examination

Sr No	Name of Faculty	Designation	Educational Qualification	Brief Profile
				related activities and academic for about 15 years. Also working as NSS Officer for past 4 Years.
20.	Sh. KishorChaudhari	Sr. Tech. Officer	BCA	Specialization in VPN technologies, Routing & Switching, VOIP, Switched Network Design. Area of Interest is MPLS, SNMP, Network security, IPS, IDS, and Data Center.
21.	Sh. Th. Sunil Kumar Singh	Principal Technical Officer	BE(ECE)	He is interested learning new trends in Technologies in his area. He has more than 20 years of undergraduate teaching microprocessor and Microcontrollers.
22.	Sh. Suryacharan	Senior Technical Assistant	B.Tech (Electronics and communication.)	With expertise in Electronics System design, he has experience of teaching various subjects of electronics. He has also worked as program analyst in cognizant for 2 years on ETL testing, SQL, Informatics and MSTR. His area of interest includes Microelectronics, AI and data acquisition using NI DAQ cards.
23.	Sh. Pawan Kumar Patel	Senior Technical Assistant	B.Tech (Electronics and communication.)	Expertise in VLSI design (Ngspice, VHDL, Verilog, System Verilog, Blue spec), Scripting languages like Python. More than 1 years of teaching experience.

8.0 Placement Assistance and Support:

Students of the Centre are trained to become R&D engineers. In Course curriculum there is emphasize on Innovation, Design and Development of Electronic Product. The Centre has also signed MoU with Chamber of Marathwada Industries and Agriculture (CMIA) to platform to Startup Aspirants students. In association with Directorate of Employment, Ministry of Labour & Employment (MoLE), a Model Career Centre is also functioning to provide a variety of employment related services. Apart from this an independent Placement Cell is providing Placement support and assistance to all the students. Almost all the students of the Institute gets career opportunities of their choice.

1. MoU with CMIA



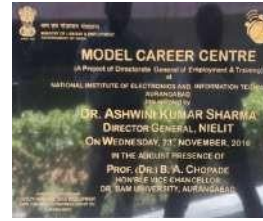
CMIA is a group organization representing around 650 small scale / medium scale / large scale industries including the Multinational Companies (MNC's) of the Maharashtra. An MoU was signed with CMIA in November, 2017 for referral of Students Projects which have potential of developing into scalable business models and also adoption of their business startup ideas. The support for internship and employment to our students in member companies of CMIA was another goal of the MoU.

2. NIELIT and CMET signed MoU



NIELIT and C-MET signed an MoU for technical collaboration and exchange of ideas for R&D and skilling in the emerging areas of IECT.

2. Model Career Centre



In association with Directorate of Employment, Ministry of Labour and Employment (MoLE), NIELIT is providing a variety of employment related services to students of the region. The students of the institute are by default members of National Career Services (NCS) of Government of India. The Model career Centre apart from organizing multiple Job Fairs every year is also conducting counseling sessions to improve Soft Skills and presentation skills of the students. Leading Experts and Industrialists are invited for the counseling sessions to share their views.

3. Industrial Tie-up

Multiple visits of the students are arranged in leading Industries of the region so that they can get well versed with current Industrial trends. The students also get a chance to take up real Industrial issues as their project work. The bright students are also provided with mentoring support for establishment of their own start-up by Industrialists.

4. Some of Companies who have come from Campus Placement








9.0 Some of the Alumni of the Centre

NIELIT Aurangabad believes in developing and maintaining a strong alumni association for its growth and progress

Table no. 08

<p>1. Dr. Suresh D. Shirbahadurkar</p>  <p>Professor Zeal College of Engineering, Narhe, Pune</p>	<p><i>“Progress Seminars Conducted by NIELIT Aurangabad research centre and attended by fellow researches, M.Tech Students & eminent guides provided me a forum to present and discuss my research. All the IEEE publications were available for reference. The state of art lab facilities were available for simulation & experimentation.”</i></p>
<p>2. Dr. Radhakrishna Naik</p>  <p>Vice Principal G S Mandals Maharashtra Institute of Technology Aurangabad</p>	<p><i>“The industrial Design & Product Design subjects helped me to compliment my class room learning with in-depth project work. Guest lectures arranged at NIELIT exposed us to latest trends in industry & real life problems. Industrial visits helped us to understand & relate our subject to industrial environment”</i></p>
<p>3. Dr Varsha Ratnaparkhe</p>  <p>Assistant Professor & Dean (Quality Assurance), Department of Electronics & Telecommunication Engineering, Government College of Engineering, Aurangabad</p>	<p><i>“Skills that I acquired and honed while in NIELIT Aurangabad, are benefiting me continuously in my professional career. NIELIT has helped me shape my character and strengthened attitude required to deliver strong results in academia.”</i></p>
<p>4. Mr. Sumit Wankhad</p>  <p>VLSI Engineer, Cerium Systems, Bangalore</p>	<p><i>“I found, NIELIT Aurangabad atmosphere conducive for learning. NIELIT Aurangabad helped in building strong fundamentals with deeper understanding in Electronics Product Design. A number of facilities including labs were accessible to students. Faculty were readily available to solve any study related difficulties and staffs were very kind in resolving any related issues.”</i></p>

<p>5. Dr. Alka Mahajan</p>  <p>Director Nirma University, Indore</p>	<p><i>“NIELIT Aurangabad taught me to think critically and confidently in experimental and theoretical situations. I developed professionally and made a wealth of friends and resources”.</i></p>
<p>6. Mr. Jaykumar H Prabhakar</p>  <p>Vice President (Global lead incident management) at Accenture, Thane. Also a Member of ISKCON working with NGO for Swachh Bharat</p>	<p><i>“What makes this course unique are the subjects in product design and PCB design which helped me understand the whole process of product development. Making of the product was a very creative experience with starting from design, to manufacturing of PCB in the PCB Lab & then making the enclosure in the workshop, not to forget the innumerable sketches we made of the various versions & forms of the product.”</i></p>
<p>7. Arvind B Nyayadhish</p>  <p>Director Enman Automation Pvt.Ltd, Aurangabad</p>	<p><i>“To shine in today's competitive world it is very essential to have the nurturing that helps you go the extra mile. NIELIT Aurangabad equipped me for the world outside with the best skill set. Those amazing years gave me much more than bookish knowledge; I met probably the best people in my life and some inspiring personalities. Proud to be an alumnus of NIELIT Aurangabad.”</i></p>
<p>8. Mr. Mahendra Padalkar</p>  <p>Principal Technical Architect (Cloud) at Tech Mahindra Ltd, Pune</p>	<p><i>“In NIELIT Aurangabad, I acquired and honed not only technical skills but also management and people skills that are assisting me immensely in my career. I'm thankful to NIELIT Aurangabad for providing such a strong foundation towards my career.”</i></p>
<p>9. Mr. Pradeep Kizhiseeri</p>  <p>Senior Consultant Presently into Hatstand, Singapore</p>	<p><i>“NIELIT Aurangabad is where the students are molded to Perfect Industry Professionals & Entrepreneurs. The reputation and brand equity associated with the institute makes one feel proud. Thanks to college management and faculty for engineering my career in right direction.”</i></p>

<p>10. Mr. Sandeep K Patni</p>  <p>Co-Founder and VP of Systems and Engineering at CumuloLogic Inc, New Jersey, USA</p>	<p><i>“NIELIT Aurangabad taught me that education can be the most challenging, extremely rewarding, exciting, and fun. I learned that passion for learning really is the driver of finding new knowledge, and that passion is honestly contagious.”</i></p>
<p>11. Mr. Rupesh Kollale</p>  <p>President & Director Endress+Hauser InforServe (India) Pvt. Ltd. Aurangabad</p>	<p><i>“NIELIT (CEDTI), provides the perfect platform for students to excel ‘Beyond the theory’ to work and experiment with latest technological things, this is a world class environment right here in Aurangabad where there is freedom to hone your practical skills which are very important along with the theory. We had very supportive and passionate teachers who made us what we are today. I am immensely thankful to NIELIT for shaping my life.”</i></p>
<p>13. Mr. Hrushikesh Gangur</p>  <p>Sr. Solutions Architect, Amazon Web Services, San Francisco, California</p>	<p><i>“It is close to 25 years, and I still have memories of those golden days that made me a complete man. The education, labs and end-to-end understanding of product lifecycle learnt for CEDTI (now known as NIELIT) still help me in day-to-day work and life. I am blessed that I had my education done from this institute. I wish other engineering colleges have a same bar raising faculties, facilities, and methodology as NIELIT has to build quality engineers for India.”</i></p>

10.0 Fee-Structure for Academic year (2022-23)

Table no. 9

Sr. No.	Course Name/ Hostel	Particulars	1 st Sem Fees (Rs.)	2 nd Sem Fees (Rs.)
1	DEPM 1 st year	Tuition Fee	23000/-	23000/-
		Caution Money Deposit*	1250/-	--
		Sub total	24250/-	23000/-
2	DEPM Lateral Entry	Tuition Fee	23000/-	23000/-
		Caution Money Deposit*	1250/-	--
		Sub total	24250	23000/-
3	B.Tech 1 st year	Tuition Fee	40000/-	40000/-
		Caution Money Deposit *	1250/-	-
		Sub Total	41250/-	40000/-
4	B.Tech Lateral Entry	Tuition Fee	40000/-	40000/-
		Caution Money Deposit *	1250/-	-
		Sub Total	41250/-	40000/-
5	M.Tech. 1 st year	Tuition Fee	48000/-	48000/-
		Caution Money Deposit *	1250/-	-
		Sub Total	49250/-	48000/-
6	M.Tech Part Time 1 st Year	Tuition Fee	48000/-	48000/-
		Caution Deposit *	1250/-	-
		Sub Total	49250/-	48000/-
7	Boy's Hostel Accommodation	Hostel Fee per Semester (5 months)	9500/-	9500/-
		Hostel Deposit*	2500/-	--
		Sub total	12000/-	9500/-

*Caution Money Deposits are onetime payment and transferred to student alumina fund on completion of the course.

*Mess Service are compulsory for Hostel Students. Outside food within the premises is not permissible. Mess charges are to be paid directly to mess manager on monthly basis.

Important:

- Presently, SC/ST students – only Tuition Fee is met from SCSP/TSP Scheme of MeitY (subject to the terms and conditions and availability of this scheme), only Caution Money deposit, hostel fee and deposit is required to be paid
- There shall be an increase of around 10% in tuition fee and hostel fee for every academic year.
- The student shall have to complete the registration formalities including full payment of hostel fees and tuition fees within one week of the commencement of registration process before every semester starts. Last date of registration will be notified in academic calendar with approval of executive director.

10.1 Refund of fees in the event of cancellation of admission:

- Fees (Tuition fees, Hostel fee, caution money deposit) once paid will not be refunded in any circumstances.
- In case of any discrepancy, the decision of Executive Director, NIELIT, Aurangabad in respect of refund of fees in the event of cancellation of admission will be final.

ANNEXURE I

Tentative Academic Calendar for M.Tech (EDT), B.Tech (ESE) and DEPM Programs Academic Year 2022-2023 (Semester I)

Sr. No.	Semester-I	
1	Start of Online application	14 th June 2022
2	Last date of Online application	11 th July, 2022
3	Classes Begin	1 st August 2022
4	Class Test-1	15 th Sep 2022*
5	Class Test-2	14 th Oct 2022*
6	Classes End	31 st Oct 2022*
7	Semester (Practical) Examination	1 st Nov. 2022*
8	Semester (Theory) Examination	10 th Nov. 2022*
9	Project Assessment	9 th Dec 2022*
	Project Feasibility Seminar	
10	Semester Break (DEPM and B.Tech)	19 th Dec 2022 to 5 th Jan 2023*
11	Declaration of Results	5 th Jan 2023*

* Tentative Dates.

ANNEXURE – II

Assessment Computation of SGPA & CGPA

1. **Semester Grade Point Average (SGPA)** is the weighted average of Grade Points obtained by a student in a semester. Details are given in M.Tech, B.Tech and Diploma Ordinances and Regulations
2. **The Cumulative Grade Point Average (CGPA)** is used to describe the overall performance of a student in a Post Graduate/ Graduate/ diploma programme. Details are given in M.Tech, B.Tech and Diploma Ordinances and Regulations.

Note: The Semester and Cumulative GPA are rounded off to the second place of decimal

ANNEXURE III

NIELIT Aurangabad, Maharashtra (India)			
Application form			
DEPM (Diploma in Electronics Production and Maintenance)			
First Year Admission[] /Lateral Entry Admission []			
To, The Dean Academics, NIELIT Aurangabad, Dr. BAM University Campus, Aurangabad, 4310004 (MS)		<div style="border: 1px solid black; padding: 10px; width: fit-content; margin: 0 auto;">Passport Size Recent Photograph Attested (Size: 4.5x5.5 cm)</div>	
Sir/Madam, I have passed SSC X th /XII th standard/ Higher Secondary Certificate (HSC) Examination passed from a recognized Board with Physics, Chemistry, Maths OR ITI (Electrical / Electronics) from recognized Institute. I am hereby applying for First Year/ Direct admission to 3 rd Semester Diploma in Electronics Production and Maintenance (DEPM) Course during the Academic year 2022-2023 and request you to kindly allow me to appear in the selection test for the said course at NIELIT Aurangabad. I submit my particulars as under:			
Name of Candidate:			
Mother's Name			
Father's Name:			
Date of Birth:			
Category [General/SC/ST/OBC /PWD(General)/PWD(OBC)]			
Address for Correspondence:			
		Pin:	
E-mail ID:		Landline No.:	Mobile No.:
Total % marks in ITI or 12th standards subjects			
ITI Year	% Marks	12th standards subject	% Marks
1 st Year		Physics	
2 nd Year		Chemistry	
		Maths	
		Vocational Training	
Average of above two years		Average of any above three subjects	

Candidate Signature with date

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. Mail scanned copy of filled form & fee receipt to depm-abad@nielit.gov.in
4. Please attach the scan copy of fee deposit counter along with application form

ANNEXURE- IV(A)**Application Form for Eligibility cum Admission to
DIPLOMA / B.Tech/M.Tech Full Time for Foreign Nationals**

To,

The Dean Academics,
NIELIT Centre Aurangabad,
University Campus,
Aurangabad 4310004 (MS)
India

Telephone: 91(0240) 2982021,2982022, 2982050(Fax)

Website: www.nielit.gov.in/aurangabad**Important Instructions**

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. No refund of Application form Fees.
4. Mail scanned copy of filled form & fee receipt to respective E-mail: depabad@nielit.gov.in, btechabad@nielit.gov.in, mtechabad@nielit.gov.in

Sir/Madam,

I hereby apply for grant of eligibility and admission as an International Student to Diploma in Electronics Production and Maintenance (DEPM) /B.Tech (EDT) / M.Tech course during the Academic year and request you to kindly grant me a certificate of eligibility and admission to the said course in NIELIT Centre, Aurangabad, Maharashtra (India). I submit my particulars as under:

Course Applied:**CANDIDATE'S DETAILS**

Passport Size Recent
Photograph Attested
(Size:4.5x5.5cm)

Last Name : First Name : Middle Name : Mother Name : Date of Birth : DD/MM/ YYYY Nationality:

ADDRESS FOR CORRESPONDENCE									
E-mail ID: Tel No. (with ISD/STD)								Pin:	

ACADEMIC QUALIFICATIONS (in ascending order)						
Sr. No.	Examination Passed	Name of School / College	Name of Examining body (Board / University)	Year of Passing	% Marks obtained	Class / Division

QUALIFYING EXAMINATION DETAILS						
10 th Std. Marks / Grade (For DEPM only)				GATE or Equivalent Score Details (if applicable) (For M.Tech (EDT) / Ph.D)		
Subject	Science	Mathematics	Total Marks	Year	Discipline	Percentile

ANNEXURE-IV (B)

(To be typed on Rs.100/- Stamp Paper) Declaration and Undertaking

1. I hereby declare that I have carefully read the information brochure for the academic year 2022-2023.
2. I hereby declare that I have carefully read this application form for eligibility and admission and have noted the instructions / requirements thereby.
3. I have also carefully noted the rules of eligibility & conduct and discipline, laid down by the NIELIT Centre, Aurangabad and I agree to abide by them.
4. I understand and declare that I shall be responsible for any discrepancies, error, wrong or incorrect information, supplied by me in this application form and for cancellation of admission thereby or otherwise found ineligible.
5. I undertake to furnish the necessary certificate(s)/ document(s)/ paper(s) in original along with a true copy of each of them as and when asked for, failing which I understand that my eligibility and admission stands automatically cancelled and that the NIELIT Centre, Aurangabad is not responsible for the same.

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information.

Yours Faithfully,

(Name & Signature of Foreign National)

Date:

Place:

ANNEXURE-IV (C)

(To be typed on Rs.100/- Stamp Paper) Declaration and Undertaking

1. I hereby declare that I have carefully read the information brochure for the academic year 2022-2023.
2. I hereby declare that I have carefully read this application form for eligibility and admission and have noted the instructions / requirements thereby given in information brochure.
3. I have also carefully noted the rules of eligibility & conduct and discipline, laid down by the NIELIT Centre, Aurangabad and I agree to abide by them.
4. I understand and declare that I shall be responsible for any discrepancies, error, wrong or incorrect information, supplied by me in this application form and for cancellation of admission thereby or otherwise found ineligible.
5. I undertake to furnish the necessary certificate(s) / document(s) / paper(s) in original along with a true copy of each of them as and when asked for, failing which I understand that my eligibility and admission stands automatically cancelled and that the. NIELIT Centre, Aurangabad is not responsible for the same.

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information.

Yours Faithfully,

(Name & Signature of Candidate)

(Name & Signature of Guardian/Parent)

Date:
Place:

ANNEXURE V

NIELIT Aurangabad, Maharashtra (India)				
Application form				
Lateral Entry Admission (Direct 2nd Year) of 4 Year's B.TECH (Electronics Engineering (Specialization in System Engineering))				
To, The Dean Academics, NIELIT Aurangabad, Dr. BAM University Campus, Aurangabad, 4310004 (MS)				Passport Size Recent Photograph Attested (Size: 4.5x5.5 cm)
Sir/Madam, I have passed 3 year diploma course in Electronics & allied streams with minimum 45% marks (40 percent for SC/ST candidates) approved by AICTE. I hereby register for admission to 3rd Semester B. Tech (Electronics Engineering (Specialization in System Engineering)) Course through Lateral Entry during the Academic year 2022-23 and request you to kindly grant me admission to the said course at NIELIT Aurangabad. I submit my particular as under:				
Name of Candidate:				
Mother's Name				
Father's Name:				
Date of Birth:				
Category [General/SC/ST/OBC /PWD(General)/PWD(OBC)]				
Name of the Institute/Beneficiary		National Institute of Electronics and Information Technology(NIELIT)		
Name of the Bank		State Bank of India		
Branch		Samarth Nagar Aurangabad Maharashtra		
Saving Bank Account Number		32078399585		
IFSC/RTGS NO		SBIN 0007919		
Application Fee		The application fee is Rs.500/-. However, the candidates belonging to SC/ST/PWD are exempted from application fees.		
Mode of Electronic Transfer		NEFT, SBICollect, Website: www.onlinesbi.com		
Address for Correspondence:				
		Pin:		
E-mail ID:		Landline No.:	Mobile No.:	
Total % marks in Diploma Course in Engineering and Technology				
Branch/Discipline	1st Year %	2nd Year %	3rd Year %	Avg. %

Candidate Signature with date

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. Mail scanned copy of filled form & fee receipt to btech-abad@nielit.gov.in
4. Please attach the scan copy of fee deposit counter foil along with application form.

ANNEXURE VI

NIELIT Aurangabad, Maharashtra (India)		
Application form		
2 year/ 3 Year's M. Tech (Electronics Design Technology) Full time/ Part Time Course Admission		
<p>To,</p> <p>The Dean Academics, NIELIT Aurangabad, Dr. BAM University Campus, Aurangabad, 4310004(MS)</p>		<div style="border: 1px solid black; padding: 5px;"> Passport Size Recent Photograph Attested (Size: 4.5x5.5 cm) </div>
<p>Sir/Madam,</p> <p>I have passed B.E./ B. Tech Degree or equivalent in Electronics/Electrical/Telecommunication / Instrumentation engineering etc. from a recognized University with a minimum of 55 percent marks (50 percent for SC/ST candidates). I am hereby applying for the admission to M. Tech (Electronics Design Technology) Part time course during the Academic year 2022-2023. I submit my particulars as under:</p>		
Name of Candidate:		
Mother's Name		
Father's Name:		
Date of Birth:		
Category [General/SC/ST/OBC /PWD(General)/PWD(OBC)]		
Sponsorship by Academic Institute/Industry		
Working Experience		
Approximate Distance of Working place from the Institute(Kms)		
Name of the Institute/Beneficiary	National Institute of Electronics and Information Technology(NIELIT)	
Name of the Bank	State Bank of India	
Branch	Samarth Nagar Aurangabad Maharashtra	
Saving Bank Account Number	32078399585	
IFSC/RTGS NO	SBIN 0007919	
Mode of Electronic Transfer	NEFT, SBI Collect, Website: www.onlinesbi.com	
Application Fee	The application fee is Rs.500/-. However, the candidates belonging to SC/ST/PWD are exempted from application fees.	
Address for Correspondence:		
	Pin:	
E-mail ID:	Landline No.:	Mobile No.:

Total % marks in B. Tech/B.E Degree in Electronics Engineering or allied streams					
Stream/Discipline	1st Year %	2nd Year %	3rd Year %	4th Year %	Avg. %

Tick Appropriate cell based on category and hostel accommodation		
Amount to be paid on counseling for admission(1 st Semester M.Tech Fee & Deposits)		
Category	Institute Fee	Institute Hostel fee (in case hostel required)
All other than SC/ST	Rs: 49,250/-	Rs: 12000/-
SC/ST	Rs: 1250/-	Rs:12000/-

Following documents with one self-attested copy of each document to be handed over to NIELIT Aurangabad academic section at the time of admission depending upon the category to which the candidate belongs.

Documents (Original with Self attested copy)	Certificate No. & Date of Issue	Yes/No	Checked (By Office)
Two Passport Size Photograph (attested)			
Date of Birth proof(X Mark-sheet / Certificate)			
All Mark Lists of the Qualifying Examination			
Degree Certificate of the Qualifying Examination.			
Conduct Certificate from the College, where the student has last studied			
Transfer Certificate from the college last studied.			
Migration Certificate (In case students are from other university)			
Physical Fitness Certificate (as per given format)			
Non creamy layer OBC certificate valid up to 31st March 2022, as per given format (OBC candidate only)			
SC/ST Certificate as per given format (SC/ST candidate only)			
Physical With Disability Certificate as per given format (PWD candidate only)			
Sponsorship Certificate and Experience Certificate (Sponsored Candidates Only)			

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information. I am ready to remit Rs...../- to bank as per the details given above today itself to secure the admission.

Candidate Signature with date

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. Mail scanned copy of filled form & fee receipt to mtech-abad@nielit.gov.in
4. Please attach the scan copy of fee deposit counter foil along with application form

ANNEXURE VII

Prescribed Performa for SC/ST Certificate (for B.Tech/M.Tech)

This is to certify that Shri/ Shrimati/ Kumari* _____ son/daughter of _____ of _____ village/town/* in _____ District/Division* of the _____ State/Union Territory* belongs to the Caste/Tribe* which is recognized as a Scheduled Castes [SC]* / Scheduled Tribes [ST]* under:

The Constitution (Scheduled Castes) Order, 1950

The Constitution (Scheduled Tribes) Order, 1950

The Constitution (Scheduled Castes) Union Territories Order, 1951

The Constitution (Scheduled Tribes) Union Territories Order, 1951

As amended by the Scheduled Castes and Scheduled Tribes Lists (Modification) Order, 1956, the Bombay Reorganization Act, 1960 & the Punjab Reorganization Act, 1966, the State of Himachal Pradesh Act 1970, the North-Eastern Area (Reorganization) Act, 1971 and the Scheduled Castes and Scheduled Tribes Order (Amendment) Act, 1976. [%]

The Constitution (Jammu & Kashmir) Scheduled Castes Order, 1956. The Constitution (Andaman and Nicobar Islands) Scheduled Tribes Order, 1959 as amended by the Scheduled Castes and Scheduled Tribes Order (Amendment Act), 1976. The Constitution (Dadra and Nagar Haveli) Scheduled Castes Order, 1962. The Constitution (Dadra and Nagar Haveli) Scheduled Tribes Order 1962**. The Constitution (Pondicherry) Scheduled Castes Order, 1964**. The Constitution (Scheduled Tribes) (Uttar Pradesh) Order, 1967**. The Constitution (Goa, Daman & Diu) Scheduled Castes Order, 1968**. The Constitution (Goa, Daman & Diu) Scheduled Tribes Order, 1968**. The Constitution (Nagaland) Scheduled Tribes Order, 1970**. The Constitution (Sikkim) Scheduled Castes Order, 1978**. [%]

The Constitution (Sikkim) Scheduled Tribes Order, 1978**. The Constitution (Jammu & Kashmir) Scheduled Tribes Order 1989**. The Constitution (SC) Orders (Amendment) Act, 1990**. The Constitution (ST) Orders (Amendment) Ordinance, 1991**. The Constitution (ST) Orders (Second Amendment) Act, 1991**. The Constitution (ST) orders (Amendment) Ordinance, 1996. The Scheduled Caste and Scheduled Tribe Orders (Amendment) Act. 2002. The Constitution (Scheduled Caste) Orders (Amendment) Act, 2002. The Constitution (Scheduled Caste and Scheduled Tribe) Orders (Amendment) Act, 2002. The Constitution (Scheduled Caste) Order (Amendment) Act, 2007. [%]

2. Applicable in the case of Scheduled Castes, Scheduled Tribes persons who have migrated from one State/Union Territory Administration.

This certificate is issued on the basis of the Scheduled Castes / Scheduled Tribes certificate issued to Shri/Shrimati _____, Father/Mother of Shri/Srimati/Kumari*

of village/town* in the District/Division* of the State/Union Territory* , who belong to the Caste/Tribe* which is recognized as a Scheduled Caste* / Scheduled Tribe* in the State/Union Territory* issued by the dated . %

3. Shri/Shrimati/Kumari* _____ and/or* his/her* family ordinarily reside(s) in the village/town* _____ of _____ District/Division* of the State/Union Territory of _____

Place

Signature

Date

Designation

(with seal of office)

*** Please delete the words which are not applicable**

**** Please quote specific presidential order**

% please delete the paragraph which is not applicable.

^ List of authorities empowered to issue Schedule Caste / Schedule Tribe Certificates:

1) District Magistrate / Additional District Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / 1st Class Stipendiary Magistrate / Sub-Divisional Magistrate / Extra-Assistant Commissioner / Taluka Magistrate / Executive Magistrate.

2) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.

3) Revenue Officers not below the rank of Tehsildar.

4) Sub-Divisional Officers of the area where the candidate and/or his family normally resides.

5) Administrator/Secretary to Administrator/Development Officer (Lakshadweep).

NOTES:

1) The term ordinarily reside(s) used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.

ANNEXURE VIII
OBC Caste Certificate Format (for B.Tech/M.Tech)

[This certificate MUST have been issued on or after 1st April 2022]

This is to certify that Shri/Smt./Kum. _____ Son/Daughter of Shri/Smt. _____
of Village/Town District/Division in the State/UT belongs to the Community which is recognized as a
backward class under:

- (i) Resolution No. 12011/68/93-BCC(C), dated 10/09/93 published in the Gazette of India Extraordinary Part I Section I No. 186, dated 13/09/93.
- (ii) Resolution No. 12011/9/94-BCC, dated 19/10/94 published in the Gazette of India Extraordinary Part I Section I No. 163, dated 20/10/94.
- (iii) Resolution No. 12011/7/95-BCC, dated 24/05/95 published in the Gazette of India Extraordinary Part I Section I No. 88, dated 25/05/95.
- (iv) Resolution No. 12011/96/94-BCC, dated 9/03/96.
- (v) Resolution No. 12011/44/96-BCC, dated 6/12/96 published in the Gazette of India Extraordinary Part I Section I No. 210, dated 11/12/96.
- (vi) Resolution No. 12011/13/97-BCC, dated 03/12/97.
- (vii) Resolution No. 12011/99/94-BCC, dated 11/12/97.
- (viii) Resolution No. 12011/68/98-BCC, dated 27/10/99.
- (ix) Resolution No. 12011/88/98-BCC, dated 6/12/99 published in the Gazette of India Extraordinary Part I Section I No. 270, dated 06/12/99.
- (x) Resolution No. 12011/36/99-BCC, dated 04/04/2000 published in the Gazette of India Extraordinary Part I Section I No. 71, dated 04/04/2000.
- (xi) Resolution No. 12011/44/99-BCC, dated 21/09/2000 published in the Gazette of India Extraordinary Part I Section I No. 210, dated 21/09/2000.
- (xii) Resolution No. 12016/9/2000-BCC, dated 06/09/2001.
- (xiii) Resolution No. 12011/1/2001-BCC, dated 19/06/2003.
- (xiv) Resolution No. 12011/4/2002-BCC, dated 13/01/2004.
- (xv) Resolution No. 12011/9/2004-BCC, dated 16/01/2006 published in the Gazette of India Extraordinary Part I Section I No. 210, dated 16/01/2006.
- (xvi) Resolution No. 12011/14/2004-BCC dated 12th March, 2007 published in the Gazette of India Extraordinary, Part-I, Section-1, No. 67 dated 12th March, 2007.
- (xvii) Resolution No. 12015/2/2007-BCC, dated 18/08/2010.
- (xviii) Resolution No. 12015/2/2007-BCC, dated 11/10/2010.
- (xix) Resolution No. 12015/13/2010-BC-II, dated 08/12/2011.
- (xx) Resolution No. 12015/05/2011-BC-II, dated 17/02/2014.
- (xxi) Resolution No. 12011/6/2014-BC-II, dated 07/12/2016.
- (xxii) Resolution No. 12011/13/2016-BC-II, dated 22/12/2016
- (xxiii) Resolution No. 20012/1/2017-BC-II, dated 19/01/2017
- (xxiv) Resolution No. 12011/7/2017-BC-II, dated 31/07/2017

Shri/Smt./Kum. _____ and/or his family ordinarily reside(s) in the _____

District/Division of _____ State/UT. This is also to certify that he/she **does not belong to the persons/sections (Creamy Layer)** mentioned in Column 3 of the Schedule to the Government of India, Department of Personnel & Training O.M. No. 36 012/22/93-Estt.(SCT), dated 08/09/93 which is modified vide OM No. 36033/3/2004 Estt.(Res.), dated 09/03/2004, further modified vide OM No. 36033/3/2004-Estt. (Res) dated 14/10/2008, again further modified vide OM No. 36036/2/2013-Estt (Res) dated 30/05/2014.

Place

Signature

Date

Designation

(with seal of office)

NOTE:

(a) The term 'Ordinarily' used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.

(b) ^The authorities competent to issue Caste Certificates are indicated below:

(i) District Magistrate / Additional Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / First Class Stipendiary Magistrate/Sub-Divisional magistrate / Taluka Magistrate / Executive Magistrate / Extra Assistant Commissioner (not below the rank of 1st Class Stipendiary Magistrate).

(ii) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.

(iii) Revenue Officer not below the rank of Tehsildar.

(iv) Sub-Divisional Officer of the area where the candidate and / or his family resides

ANNEXURE IX

Physical Disability Certificate (format)

Certificate No.

This is to certify that I have examined
Mr./Ms.....Son/Daughter/Wife of
Mr.....Age.....
Gender.....on.....

Photograph of the Candidate Showing the Physical Disability

- i. He/ She is suffering from.....which comes under the sub category Blindness /Low vision/Speech & Hearing impairment/Orthopedic disorder/Learning disabilities. Dyslexia, Dyscalcula, Dysgraphica, Spastic.
- ii. The percentage of disability is.....%.
- iii. The disability is permanent innature.
- iv. This condition is progressive/non-progressive/likely to improve/not likely to improve. Reassessmentofthiscaseisnotrecommended/isrecommendedafteraperiodofyears.....months.
- v. The candidate is capable of carrying out all activities related to theory and practical work as applicable to DEPM/ B.Tech/ M.Tech course of NIELIT Aurangabad without any special concession and exemptions.
- vi. This certificate is issued as per the provisions given in the Persons with Disability Act, 1995 and its amendment.
- vii. This Certificate is issued for the purpose of his/her admission to of DEPM/ B.Tech (ESE)/ M.Tech (EDT) course of NIELIT Aurangabad in the Academic Year 2022-23 at NIELIT Centre, Aurangabad (MS).

Date:

Place:

Director OR Dean
/Civil Surgeon

Seal of Institution/Hospital

ANNEXURE X

Physical Fitness Certificate (format)

(To be issued by a Registered Medical Practitioner)

GENERAL EXPECTATIONS				
Candidates should have good general physique. In particular,				
<ul style="list-style-type: none"> a Chest measurements should not be less than 70cm, with satisfactory limits of expansion and contraction. b Vision should be normal. In case of defective vision, it should be corrected to 6/9 in both eyes or 6/6 in the better eye. Colour blind and unocular persons are restricted from admission. c Hearing should be normal. Defective hearing should be corrected. d Heart and lungs should not have any abnormality and there should be no history of mental illness and epileptic fits. 				
Name of the candidate:				
Identification Mark (a mole, scar or birthmark), if any				
Major illness/operation, if any (specify nature of illness/operation)				
Height in cm:		Weight in kg:		Blood Group:
Past History	(a) Mental illness (b) Epileptic Fit			
Chest		(a) Inspiration in cm		(b) Expiration in cm
Hearing				
Vision with or without glasses:	Right Eye	Left Eye	Colour Blindness	Unocular vision
Respiratory System				
Nervous System				
Heart		a) Sounds		b) Murmur
Abdomen Liver Spleen		Hernia		Hydrocele
Any other defects:				
Certificate of Medical Fitness (tick appropriate box below)				
<input type="checkbox"/>	The candidate fulfils the prescribed standard physical fitness, medical fitness and is fit for admission to DEPM/B.Tech/M.Tech course of NIELIT Aurangabad.			
<input type="checkbox"/>	The candidate does not fulfill the prescribed standard of physical fitness/medical fitness and is unfit/temporarily unfit for admission due to following defects:			
<input type="checkbox"/>				
Name of the Doctor	Signature with date	Registration number	Seal	

ANNEXURE XI

Sponsorship Certificate

(On letterhead of the Institute /Organization)

Outward No.:

Date: _

To,

The Dean Academics,
NIELIT Aurangabad
Dr. B A M University Campus,
Aurangabad 431 004

This is to certify that Mr. / Ms.is serving in
our Organization / Institution as.....since.....

The Organization / Institution has no objection for the mentioned candidate to join the M.Tech (EDT)
at NIELIT, Aurangabad and will permit the candidate to attend lectures as per the institute timetable.

The Organization / Institution will render all possible help to him / her in persuasion of studies.

He / She will be relieved for a requisite period, if selected for the course.

Signature of Competent Authority

Name: Designation:

Seal of Sponsoring Organization

ANNEXURE XII

**No Objection Certificate For M.Tech (EDT) Part Time Candidate (On letterhead
of the Institute /Organization)**

Outward No.:

Date: _

To,

The Dean Academics,
NIELIT Aurangabad
Dr. B A M University Campus,
Aurangabad 431 004

This is to certify that Mr. / Ms.is serving
in
our Organization / Institution as.....since.....

The Organization / Institution has no objection for the mentioned candidate to join the
M.Tech (EDT) Part time Course at NIELIT, Aurangabad.

The Organization / Institution will render all possible help to him / her in persuasion of
studies.

He/ She will be relieved for a requisite period, if selected for the course.

His/ Her working place is within 60 km distance from the institute. If candidate resigns or
transferred beyond 60 km distance, the intimation in this regard will be given to your office
in writing within a week.

Signature of Competent
Authority

Name:

Designation:

Seal

ANNEXURE-XIII CAD/CAM LAB

Objectives

The lab is aimed at giving exposure to and enhancing the knowledge and skills of engineers involved in the operation use of CNC machines, CAD/CAM packages and for those who want to provide training to others in this area. It gives exposure and on hand experience in the field of CAD/CAM and CNC machines, Computer Integrated Manufacturing and Industrial Robots.

CAD/CAM Lab is equipped with the latest Machines of for CNC machining, also has flagship CAD/CAM software packages and high end CAD/CAM workstations to meet the present industrial requirements. This Lab equipped with latest professional A0 width colour Plotter with paper roll feeder that makes realistic printout of colour CAD models and drawings. Also equipped with an 85 inch smart interactive digital display with various connectivity for Lab cum class room delivery. Some of the facilities available as follows:

Main Equipment's Available

CNC Lathe Machine



Lathes are machines that cut work pieces while they are rotated. CNC lathes are able to make fast, precision cuts, generally using indexable tools and drills with Automatic Tool Changer. It has Graphic simulation for product proving.

CNC Milling Centre



CNC mills use computer controls that are able to translate programs consisting of alpha-numeric codes to move the spindle (or workpiece) to various locations and depths to cut materials. It has Automatics tool turret with 6 tools and Graphic simulation for product

3. Colour Plotter



It prints professional-quality full colour real scaled drawings, models as per CAD at your desk. This plotter is equipped with roll feeder to allow print larger lengths drawings easily with the width A0 size.

4. Vacuum Formation



Vacuum forming is a simplified version of thermoforming, where a sheet of plastic is heated to a forming temperature, stretched onto a single-surface mold, and forced against the mold by a vacuum. Vacuum-formed components can be used in place of complex fabricated sheet metal, fiberglass, or plastic injection molding.

5. CAD/CAM Software



Major CAD/CAM Software available for this Lab

- Catia V5
- Creo (Pro/Engineer)
- MasterCAM
- AutoCAD

6. Some of the Practical Project Experiments

1. Creating 2D and 3D Models using CAD
 2. CNC Part programming through CAD/CAM
 3. Machining of complex parts using CNC Machines
 4. 3D printing the products of created models
 5. Scanning the parts to obtain 3DCAD models
- Surface Machining and Product proving.

**ANNEXURE-XIV
CONSUMER ELECTRONICS LAB**

Objective:

Consumer Electronics Lab has been established to provide hands on skills to the students that employers are seeking in Electronics Hardware & Productions as per Industrial requirements and standards. In various verticals of Consumer Electronics aspiring Maintenance Technician, Supervisor & Design Assistants are being skilled.

Main Equipment's available:

1. Rigol DS1104Z-S 100 MHz Digital Oscilloscope:



The DS1000Z 4 channel oscilloscopes come in 70 or 100 MHz versions with a 7 inch display and Rigol's Ultra Vision technology as well as a host of options.

2. EasyScope - Scientech 801C:



EasyScope - Scientech 801C is a New Trend. The Vertical Bandwidth is more than adequate for all our needs and we can easily view signals upto 40MHz.

3. NIELVIS Engineering Lab Workstation:



The NI Educational Laboratory Virtual Instrumentation Suite (NI ELVIS) II is a modular engineering educational laboratory solution developed specifically for academia.

4. Strain Gauge Trainer



Use of *strain gauge* for such a purpose can be studied using this *trainer*. This *Load cell trainer* is designed to measure the pressure of the cylinder by using a diaphragm as a primary transducer and *strain gauge* as a secondary transducer.

5. LCD Digital TV Trainer



This trainer has been designed with a view to provide theoretical and practical knowledge of a general LCD Digital TV (DTV) on SINGLE P.C.B.

6. DTH Trainer Kit



DTH trainer has been designed with a view to provide theoretical and practical knowledge of a Direct to Home Trainer (DTH) on Single P.C.B. Signals can be monitored and demonstrated at various testing point.

7. High-End Digital storage Oscilloscope (DSO)



ANNEXURE XV
CONSUMER ELECTRONICS LAB

High-End Digital storage Oscilloscope (DSO) can ensure the proper functioning of the device or design flaws allowing for a more intuitive visual diagnosis of the source of unexpected voltage.

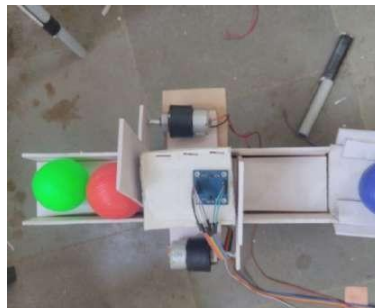
It allows probing of individual components and connections within electronic devices, acting as a simple signal tracer to determine the specific malfunctioning part besides providing alert regarding replacement need or fine tuning of electronic component.

Some of the Practical/Project Experiments:

1. Testing of Passive and Active components-
2. Characteristics diode: Transistor
3. Rectifying circuits
4. Filter circuits
5. Oscillator: Design of different type of biasing and their comparison
6. Study of Amplifiers
7. Verification of Thevenins, theorem for a two port network
8. Verification of Norton's theorem for a two port network
9. Maximum Power Transfer theorem
10. Series resonance - BW and Q factor
11. Parallel resonance –B.W. and Q-factor
12. To learn LCD Digital TV working and repairing using Trainer kit
13. To learn DTH working and repairing using Trainer Kit
14. To learn Dvd player working and repairing using Trainer Kit

PTH Mechanical assembly

Electro less plating is “plating without the use of electrical energy” a chemical reduction process which depends upon the catalytic reduction process of metal ions in an aqueous solution (containing a chemical reducing agent) and the subsequent deposition of the metal. Typical choice for irregularly shaped, highly detailed part shapes because of completely uniform deposit thickness and high precision. By using this process and principle, PTH process has been completed shapes because of completely uniform deposit thickness and high precision. By using this process and principle, PTH process has been completed.



ANNEXURE-XVI

INDUSTRIAL AUTOMATION LAB

Recent trend of merging control systems associated with both factory and process automation demands knowledge from diverse fields. The purpose of the lab work is to study automation of time critical systems that demand precise real-time readings and control.

Main Equipment's Available

1. NI 9217 4-Ch PT 100 RTD 24-bit, 100S/s/ch



The NI-9217 is compatible with 3- and 4-wire RTD measurements, and it automatically detects the type of RTD (3- or 4-wire) connected to the channel and configures each channel for the appropriate mode.

NI PCIe-6321, Xseries multifunction DAQ (16 AI, 24 DIO, and 2 AO), 250kS/s single channel



sampling rate:

The PCIe-6321 offers analog I/O, digital I/O, and four 32-bit counters/timers for PWM, encoder, frequency, event counting, and more.

3. NI USB-6211 Bus-powered M series Multifunction



DAQ device:

It offers analog I/O, digital input, digital output, and two 32-bit counters. The device provides an onboard amplifier designed for fast settling times at high scanning rates.

4. NIUSB-9211A, 4Ch., 24-Bit Thermocouple input module:



Plug-and-play connectivity via USB. Compatibility with J, K, R, S, T, N, E, and B thermocouple types

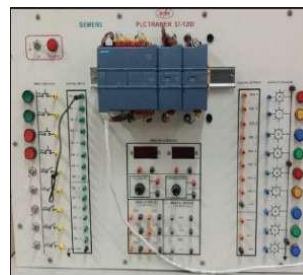
Small, portable device (12.1 x 8.6 x 2.5 cm)

5. Compact RIO:



Compact RIO (or cRIO) is a real-time embedded industrial controller made by National Instruments for industrial control systems. The **Compact RIO** is a combination of a real-time controller, reconfigurable IO Modules (**RIO**), FPGA module and an Ethernet expansion chassis.

6. Programmable Logic Controller

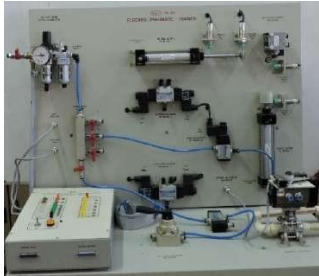


ANNEXURE-XVII

INDUSTRIAL AUTOMATION LAB

PLC is used for control applications as in special purpose machines for milling, drilling, packaging etc. PLC senses inputs from field (using its input cards), for example from a level sensor, a proximity switch, pushbutton etc. PLC's have been programmed in a language called as ladder language.

7. Electro-Pneumatic Trainer Kit



Electro-pneumatic control consists of electrical control systems and operating pneumatic power systems. In this solenoid valves are used as interface between the electrical and pneumatic systems. Devices like limit switches and proximity sensors are used as feedback elements.

8. Level Measurement



Capacitive Level Sensors also referred as Radio Frequency (RF) level sensors, are used for measuring process level at a specific point, multiple points or continuously over the entire vessel height. Level change results in a variation of capacitance value around the probe, depending upon the degree of immersion.



9. Siemens S7 300 & 1200 PLC, SCADA, HMI

Siemens S7 300 & 1200 PLC, SCADA, HMI is used for PLC programming on Siemens PLC software Simatic Manager & TIA portal V13 with Digital I/O as well as Analog I/O. SCADA designing with WinCC basic & advance Siemens SCADA software, HMI TP700 comfort programming & its applications.



10. Electro Hydraulic Trainer Kit

Electro Hydraulic Trainer outlines the basic Principle of Hydraulic Control System, Hydraulic Control System Components & its applications using electronic proximity position sensor & electro-mechanical actuators (solenoid valves).



Some of the practical project experiment:

1. Optimized Operation of Induction Generator for Small-scale Wind Power.
2. High Precision Stepper Motor Controller Implementation on FPGA with GUI on LabVIEW.
3. Real Time Data Monitoring of PV Solar Cell Using LabVIEW and DAQ.
4. Forest Fire Detection Using Optimized Solar-Power Zigbee Network
5. Optical Character Recognition Based Speech Synthesis System Using Lab VIEW
6. LabVIEW and Web-Server Based Human Body Monitoring System

ANNEXURE XVIII

Internet of Things

Objectives

The lab is equipped with popular boards such as ARM Cortex Processors with IDE of 10 users license, Raspberry Pi, Arduino and Node MCU ESP 8266 in addition to wi-fi, BLE and other connectivity modules. The campus is equipped with 24x7 wired and wireless internet connectivity.

Main Equipment's Available

1. Raspberry Pi3 B Board



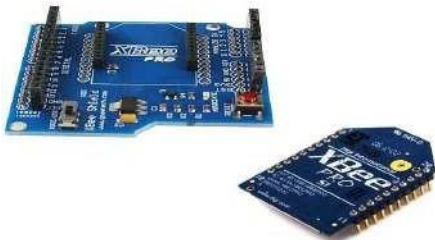
Smart card size PC board with CPU of 4× ARM Cortex-A53, 1.2GHz, 10/100 Ethernet 802.11n wireless, Bluetooth, 40-pin GPIO header, 4× USB 2.0, Ethernet, Camera Serial Interface (CSI), Display Serial Interface(DSI)

2. Arduino Boards



Very cheap IoT platform with ATmega328P cpu, 6 analog inputs, 14 digital I/O pins include 6 PWM outputs.

3. Xbee modules with explorer and shield



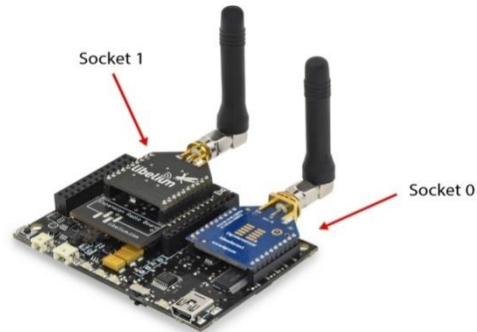
Low power, low cost modules for wireless mess networks suitable for home automation, environment monitoring etc.

4. SIM900 GPRS/GSM module



Very powerful single-chip board integrating AMR926EJ-S core, Quad - band GSM/GPRS module suitable for M2M solutions.

5. LoRaWAN module



Module provides Long range - wireless technology solution, low power and high capacity nodes.

6. Ethernet Shield



The Arduino Ethernet Shield allows an Arduino Board to connect to the internet. It is based on the (WiznetW5500 Ethernet chip).

Practical Project Experiments

1. Setting up of Raspberry Pi and connect to a network
2. Familiarization with GPIO pins and control hardware through GPIO pins.
3. Speed Control of motors using PWM with python programming.
4. Use sensors to measure temperature, humidity, light and distance.
5. Web based hardware control
6. Connect IOT devices through cloud using IoT protocol such as MQTT.
7. Controlling IoT devices using Arduino.
8. Create Wireless network of sensors using Zigbee.

ANNEXURE XIX
NETWORK and SERVER FACILITY

Objective

All the classrooms and lab are well connected with central switching center and have 100Mbps NKN Link. For academic and research purpose there is also a facility of a mini Data center having CISCO UCS5108Chassiswith4NumbersofB220blade servers. For faulty tolerance and to decrease down time there are redundant Fabric Interconnect in clustered fashion.

Server is CISCO 5108 with 4 B220 blade server each having 2 Xeon processor and 192 GB RAM. Chassis is connected using Fabric Inter connect to 40 GB storage. The hardware is controlled is CISCO UCS and VMware software.

Main Equipment's Available

1. CISCO Layer 3 Central Switch4507.



This CISCO L3 main switch deployed with two sup Engines, 2 Line card for 10G fiber optic ports and 2 Line card for 48 PoE Gigabit Ethernet copper ports with dual 6000w power supply. This form main 10G backbone on fiber optic connectivity for NIELIT Aurangabad.

2. CISCO Catalyst2960X-48TDL.



Every lab is deployed with CISCO Layer2 switch 48 Gigabit Ethernet port and 10G fiber optic port for uplink to main central L3 Switch.

3. UCS 5108 Chassis with 4 blade servers



4. Storage IBMv3700



IBM storage is used to provide space to the VMs created on servers. Storage is configured and working on iSCSI connectivity with server Chassis.

5. CISCO router ISR 2911 andISR2821



NIELIT Aurangabad is acting as CISCO network Academy for CCNA routing and switching course. The lab is equipped with 3 Nos. of 2911 CISCO routers to understand working and configure various routing protocols.

6. CISCO Catalyst switches2960:



There are 3 Nos. of CISCO Catalyst 2960 switches for CCNA practical. This is useful hands on practice to configure and troubleshoot various VLAN, VTP, STP, trunking protocols

ANNEXURE XX
NETWORK and SERVER FACILITY

1. High Speed WAN interface card

Cisco HWIC 2T serial port module is used to configure various WAN protocols like HDLC, PPP, Frame-relay, etc.



2. Connection to NKN link



Whole office is using 100Mbps NKN link for public connectivity using Juniper route MX-8 and for security using Cyberoam 300iNG as firewall.

3. System Administration using RHEL Linux and Windows Server 2008



There are 3 Nos. of HPProliant DL160 Rackmount servers. One of those is deployed as Public web service where other is used as hardware accreditation portal is hosted.

One server is being used as intranet server where various services are running like DNS, DHCP, Web, FTP, Telnet, etc. Authentication server is configured in Windows environment as Active Directory Server.

Some of Practical Project Experiments

Students can practice and build their own network scenario and configure various protocols using the lab. Some of the practical experiment covered are:

1. Providing seamless, strong backbone connectivity to all labs.
2. Usage of High end Hardware and Software tools to deploy the infrastructure.
3. Creating of network scenario and configuration of routing protocols like Staticroutes, Dynamic routing protocols like RIP, OSPF, EIGRP and BGP, etc.
4. Experiments in the domain of networking particularly in routing and switching.
5. Students learn, practice and prepare for Industry Certification.
6. Project Experiments to get certified as CCNA.



ANNEXURE XXI

OPTO ELECTRONICS LAB

Objectives:

The Objective of the lab is to conduct Master level courses in Optical Fiber Communication, Test & Measurement of Optical Fiber parameters, project and Industry Oriented Short Term Courses of the field

Main Equipment's available

1. EXFO MAKE AX-110 ALL FIBER OTDR:

The OTDR EXPO AX-110 is ideal for occasional or experienced users looking for ruggedness, ease of use, automation and top-of-the-line optical performance. It performs testing at wavelenths (nm) 850/1310/1490/1550/1625 .



2. Connectorisation Kit



The Kit contain all necessary tools, consumables, connectors and cable to demonstrate and practice the process of Fiber preparation, Inserting the fiber into the connector, applying epoxy, cutting the fiber, & polishing the surface etc.

3. Core Alignment Fusion Splicing Machine:

A core alignment splicer (Sumitomo Electric Make) is a fusion splicer that uses the core alignment method to observe the core from two directions with a built-in microscope and determine through image processing the

position in which the central axes of the cores are aligned.



4. Transmitter/ Receiver (Model 310 And Model 400):

BCP make Model 310 And Model 400 is high speed transmitter and receiver. This Model works at 850 and 1300nm and separate receivers are available with Si and Ge detectors.



5. 6 1/2 Digit Precision Multimeter

The 6 1/2 digit precision Multimeter deliver a comprehensive range of functions to meet our most demanding measurements on the bench or in a system.



The digital multimeters performs the functions we would expect to see in a multifunction DMM, including

measuring volts, ohms, amps, and frequency measurements

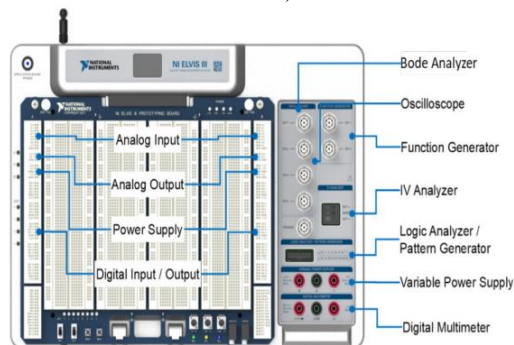
6. MIXED SIGNAL OSCILLOSCOPE (RHODE & SCHEWRTZ)

It has 4-Analog Channels & 16 Digital Channels with Bandwidth of 300 MHz, a sampling rate of 4GSa/s and high vertical sensitivity down to 1 mv/Div. The integrated three-digit digital voltmeter enables the users to simultaneously perform voltage measurements on all analog channels.



7. NI Educational Laboratory Virtual Instrumentation Suite (NI ELVIS)

NI ELVIS is a project-based learning solution that combines instrumentation, embedded



design, and web connectivity for engineering fundamentals and system design. It provides a comprehensive teaching solution for engaging students in hands-on labs involving analog circuits, mechatronics, power electronics, instrumentation, digital communications, digital electronics, controls, and more. Each laboratory solution includes lab material and complete experiments developed by experts in industry and education, so students can explore theory in the physical laboratory with a safe, in-depth experience.

8. Freescale MCU SLK

The Project Board consists of a large solderless breadboard area for building electronic circuits and has dedicated connectors for its Application Modules.



Some salient features of the Project Board are- on-board voltage regulators provide 4 different voltage levels (5V, 3.3V, +15V, -15V), push buttons, LEDs and LCD display for debugging, easy connectivity to the NI ELVIS/NI ELVIS II workstation via the NI ELVIS connector, power input from the included wall-plug transformer, integrated USB BDM, or from the NI ELVIS workstation.

Some of the Practical's/Experiments-

1. Handling and preparation of Optical Fibres.
2. Study and setting up an Optical fibre link
3. Study and Setting of an Digital Optical Link
4. Measurement of numerical aperture (NA) of an Optical fibre
5. Study of Losses in Optical fibres.
6. Voice and Data Transmission on Optical Fibres
7. Study of High speed Optical Links
8. Study of OTDR
9. Study of Fusion Splicing Machine
10. Preparation of Optical fibre connectors
11. Study of Intensity Based Sensors
12. Study of Network Analyser as Component Analyser.



ANNEXURE XXII POWER ELECTRONICS LAB

Objectives

The lab aims at imparting practical knowledge of Power Electronics to the students at various levels i.e. DEPM, B.Tech and M.Tech. It is accordingly well equipped with equipment's and trainer kits to teach practical from fundamentals to high level concepts to the students.

Main Equipment's Available

1. Triac AC Phase Control



All Components are terminated with a connector for the study of Students. One potentiometer is provided to vary the firing angle of SCR. Another potentiometer is provided to vary the firing angle of TRIAC.

2. Single Phase Converter



Power circuit with a DC shunt motor 2SCRs/4SCRs and 2 power diodes. A circuit breaker, a bridge rectifier for field supply. 1 phase converter firing circuit. SCR converter with open/close loop with motor rpm indication, mech. Loading load regulation = 1% with tacho F/B

3. SCR Lamp Flasher



Scientech PE40 SCR Lamp Flasher is compact, ready to use experiment board for lamp flasher using SCR circuit. This board is useful for students to study and understand operation of SCR controlled lamp flasher circuit and measurement of frequency, time, and voltage.

Some of the Practical project experiments.

1. Triac based speed control of small single Phase Induction Motor.

2. Electronic Heater Temperature control
3. Operation of SCR flasher.
4. Lamp brightness control using single Phase SCR Converter.

4. DC Chopper



DC Chopper Using SCR Trainer is very much useful for the students, to understand the principle of working and operation of the chopper. Output voltage can be controlled electronically by variation of Duty Cycle.

5. DC to AC Inverter



Power Electronic Training Board has been designed specifically for the study of working of inverter. A Battery 12V 80AH (Any car battery) is required to operate this apparatus. Different test points have been provided to check wave shape and amplitude of pulses how DC supply is changed to AC supply.

6. SCR Triggering Trainer



SCR trigger trainer system. UJT triggering circuit 24Volt 10W Lamp load 24 Volt ac supply for Circuit inputs LED indication for supply R-trigger circuit with phase angle control 5 degrees to 90 degrees R-trigger circuit half wave with phase angle control up to 180 degree maximum. UJT is an excellent triggering device which provides narrow gate pulses. Control is very accurate and from 0 Deg. to 180 Degree.

Practical project experiments.

1. DC Chopper based speed control of small DC Motor.
2. Lamp brightness control using inverter. Inverter driven small induction motor

ANNEXURE XXIII PRINTED CIRCUIT BOARD (PCB) LAB

Objectives

Printed Circuit Board laboratory caters to the need of Electronic Designer. It brings out the importance of quality and reliability to Electronic Manufacturing Industries. The Printed Circuit Board are designed by considering DFM (Design for Manufacturing)/DFA(Design for Assembly) and DFT (Design for Testing). Students are getting real world experience in PCB design and Manufacturing processes involved in Electronic Manufacturing and assembly Techniques.

Main Equipment's available

1. CNC Machine:



CCD/ATC/XL is a high quality Computer Controlled Drilling machine with Automatic Tool Change (ATC). The ATC/XL CCDs allows directly processing of Excellon / Sieb& Meyer drill data or HP/GL route data for producing PCBs (drilling, cut-out-routing, isolation milling) or routing/ engraving plastics, aluminum and other metals. CCD/ATCXL has a larger work space (500x600 mm). The ATC is ideal for very big and complex PCBs with many different drill sizes and for special applications.

2. Electroplating Machine:

HitecPlate 3040 are universally applicable electroplating machines for the deposition of metals and serve for the production of plated-through-hole printed circuit boards in vertical technology for prototype and small batch production.



The HitecPlate 3040 is designed for direct metallization and have baths for process steps cleaning, rinsing, pre-dipping, activating, rinsing, intensifying, rinsing, copper deposition.

3. Etching / developing Machines:(DL 500)

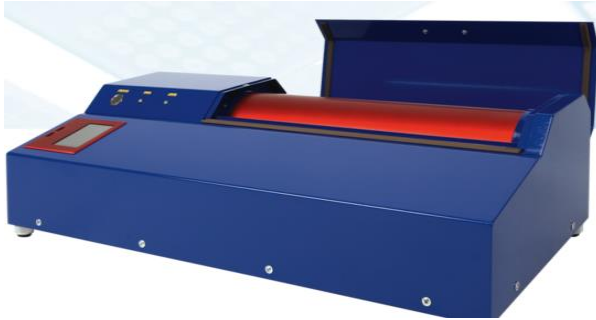


The DL 500 is a double sided conveyerised spray etching machine with integrated rinsing zone. This machine is easy to maintain and fits perfectly to a modern PCB laboratory. The maximum capacity within one hour is 10 m². Designed for being used for laboratory purposes,

there are lots of different applications (e.g. spray developing of tenting or solder mask).

4. PCB Photo Plotter:

PCB Photo plotter (Film Master Plus XL from Bungard) is used for generation of high end film



artwork layout with maximum plot size of 360 mm X 430 mm and can process Gerber files or bit map files.

5. U.V.Exposure Unit:



This Unit is used to expose photo tool /Film master /Image on photosensitive coated board by exposing with proper wavelength, exposing time, light intensity, Temperature, type of photo tool. The light source affects the degree of polymerization of photopolymer.

7. Reflow Oven :

Reflow soldering is a process in which a solder paste (a sticky mixture of powdered solder and flux) is used to temporarily attach one or several electrical components to their contact pads, after which the entire assembly is subjected to controlled heat, which melts the solder, permanently connecting the joint.

9. PCB Brushing Machine :



7. SMD Components Pick And Place:



SMT (surface mount technology) component placement systems, commonly called pick- and place machines or P&Ps, are manual assisted or robotic machines which are used to place surface-mount devices (SMDs) onto a printed circuit board (PCB).

8. Stencil Printer :



Stencil printer is used to deposit solder paste on the Printed Circuit Boards (PCB's). The laser etched screen allows to dispense a set amount of solder paste required for soldering the component.



The PCB Brushing machine (RBM 300 from Bungard) is used for deoxidizing, light deburring and surface finishing of single & double sided PCBs with working width of 33 mm.

10. Hot Air Oven:



The Hot Air Oven (Scientech SE-127) is used for PCB baking process and has the temperature range of upto 300 degree centigrade with digital display for temperature settings.

11. Soldering & De-soldering Unit:

DSS36 is an ESD soldering station used for SMD components mounting and rework, soldering common and directly-inserted electronics and lead-free soldering.

It is a new soldering station with a traditional heating system (ceramic heater), characterized by a 90 W power, temperature adjustment from 80°C to 480°C and temperature stability of $\pm 2^\circ\text{C}$.

The Quick 201B ESD Desoldering Tool allows removal of through hole soldered components with the minimum of effort, simply select the required temperature, place the nozzle over the lead and once the joint is molten press the trigger.

Some of the Practical Experiments-

1. Introduction to Printed Circuit Board
2. Introduction to various Electronics component Footprints.
3. Design & Develop Single sided PCB Documents for Manufacturing
4. Design & Develop Double sided PCB Documents for Manufacturing
5. Introduction to Computer aided Design(CAD)
6. Introduction to various artwork generation method
7. Introduction to Etching Techniques.
8. Manufacturing of SSB
9. Manufacturing of DSB
10. Introduction to Soldering, soldering methods & component Assembly.



ANNEXUREXXIV
VLSILAB

Objective

This Laboratory is well equipped for challenging IC design and validation. The Basys 3 FPGA development board design for Xilinx Vivado Design Suit featuring. The Artix-7 FPGA Architecture allows us put complex logic into device and verification logic. This supports for conducting the UG/PG labs and also research activities in M.S and PhD level.

Board has complete ready-to-use hardware, a large collection of on-board I/O devices, all required FPGA support circuits, and development tools.

3. Analog discovery 2 with parts kit

Main Equipment's Available

1. Altera DE2-115 Development and Education Board



DE2-115 offers an optimal balance of low cost, low power and a rich supply of logic, memory and DSP capabilities

2. Basys-3 FPGA Board



Digilent Analog Discovery 2 is a USB oscilloscope and multi-function instrument that allows users to measure, visualize, generate, record, and control mixed-signal circuits of all kinds.

Some of the Practical Project Experiments:

Digital circuits designing in Verilog and analog circuit design
Xilinx: VIVADO Simulation and Synthesis

Synopsys:

1. Design and Verify logic of all basic gates using Switch and Led.
2. Design all sequential circuits' logic.
3. Design all Combinations circuits Logic.
4. ALU Design
5. Decoder design UART design.

ANNEXURE XXV **EMBEDDED SYSTEM LAB**

Objectives

Studying a range of topics of immediate relevance to industry makes a student suitable for working in industries engaged in Embedded System and Electronic Product development. The purpose of this lab is to provide an excellent foundation for those wishing to engage in application research in this rapidly developing area.

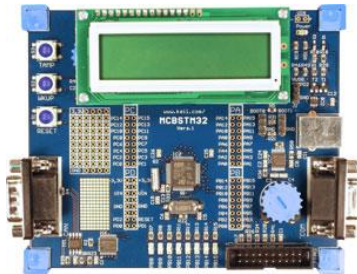
Main Equipment's available

1. High-end multipurpose embedded Development board



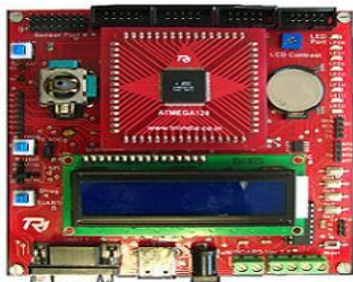
This board is very useful for students to learn ARM7, AVR, 8051 and its interfacing with Micro SD Card interface, Graphical LCD, 8 LEDs, On board, LDR on board

2. ARM processor and Controllers: -



An ARM processor is one of a family of CPUs based on the RISC architecture developed by Advanced RISC Machines (ARM). The Keil MCBSTM32 Evaluation Board enables you to create and test working programs based on the STMicroelectronics STM32 family of ARM Cortex™-M3 processor-based devices.

3. AVR



Learn how to interface any sensor or input output device with ATmega32 microcontroller. Here we teach the students all input output interfacing.

4. 8085Microprocessor



The PS-8085 board which demonstrates the capabilities of the 40-pin 8085(various families). All programs are provided to demonstrate the unique features of supported device.

5. 8051Microcontroller



The P89V51RD2 are 80C51 microcontrollers with 16/32/64 kB flash and 1024 B of data RAM. The flash program memory supports both parallel programming and in serial ISP. Parallel programming mode offers gang-programming at high speed, reducing programming costs and time to market.

6. Arduino Development Board:



Arduino Development Board (micro embedded) used for programming of Micro ATmega328 & having flash memory of 32kb. Availability of interfacing present on microcontroller kit LED interfacing, Keyboard interfacing, LCD interfacing and other interfacing devices.

7. ARM CORTEX M3:

ARM CORTEX M3 (micro embedded) used for programming of LPC1768 & having flash memory of 512kb. Availability of interfacing present on microcontroller kit LED interfacing, Keyboard interfacing, LCD interfacing and other interfacing devices.

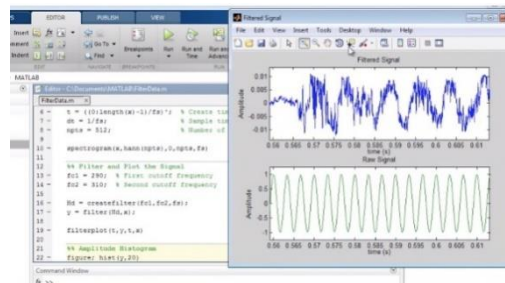


8. DSP Development Board:



The TMS320C6748 DSP development kit is a scalable platform that breaks down development barriers for applications that require embedded analytics and real-time signal processing, including biometric analytics, communications and audio. A wide variety of standard interfaces for connectivity and storage enable you to easily bring audio, video and other signals onto the board.

9. MATLAB-Math Works:



MATLAB combines a desktop environment tuned for iterative analysis and design processes with a programming language that expresses matrix and array mathematics directly. It includes the Live Editor for creating scripts that combine code, output, and formatted text in an executable notebook. MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages.

Some Practical Project Experiments

1. Familiarization with ARM board, RS- 232C interface with PC
2. Traffic Light Controller
3. SPI interface, ADC interfacing
4. Dining Philosophers Problem implementation in ARM processor
5. RMS Scheduler using Free RTOS
6. Program to demonstrate I2C interface on IDE environment
7. Study and observation of Position control of Servo Motor.

Some Innovative Project Works

- 1) An Android Controlled Mini Rover for real time surveillance using Raspberry Pi3
- 2) Smart Blind Stick



ANNEXURE XXVI

OPEN SOURCE COMPUTING LAB

Objective

- Course Available In Application Software's and Programming Languages :

Full-fledged three labs that are exclusively used for Open Source Software Development and Training purposes. All the labs are equipped with interconnected 10Gbps SFP+ port single mode fiber optics and has all PCs have at least i7 processor, 8 GB RAM, 100Mbps NKN Link etc.

Main Software's available

1. Operating System

The students have facilities to work and explore multiple Operating Systems viz **Ubuntu, Red Hat Linux, Fedora and Microsoft Windows -7,8,10.**

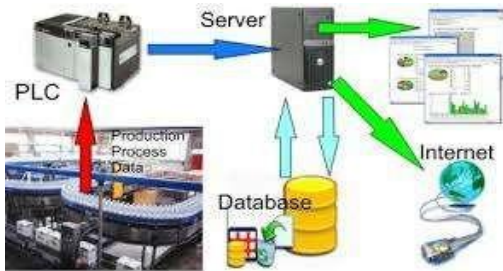
Course Available In Operating System:

- Certificate Course in System Administration using Unix
- Certificate Course in System Administration using Linux

2. RDBMS/ODBMS Software

Students can learn intricacies of Database Administration using both RDBMS and Object-Oriented DBMSs such as **MySQL, Oracle 11g and IBM DB2.**

Course Available In Database Management:



- Advanced Diploma in Big-Data Analytics
- Certified Data Scientist

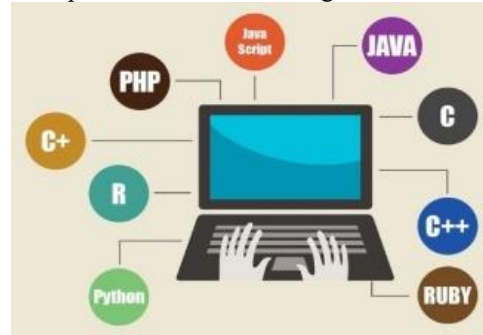


3. Application Software's

Students can master Web Application Development in all platforms of their choice such as Microsoft **DotNet, Java, LAMP.**

Android Apps Developer

- Advanced Diploma in Java Enterprise
- Certified Course in Web Designing Course
- Certified Multimedia Developer
- Advance Diploma in .Net Technologies



4. Programming Languages

The Centre has licensed software to learn latest programming languages for development purposes such as SQL, Assembly, Python, Perl, PHP, Java, C/C++, C# and VB.Net

5. Mobile Application Development

Students are taught intricacies of professional Mobile Application Development on Android and Phone Gap platforms. Students can use latest tools for this purpose like Android Studio, AVD Manager, and Android Debugging Bridge etc..

6. Software for Scientific Use

Students can do simulation using Mat Lab and will do hands-on practical's on Industrial Grade Software such as AutoCAD, PLC & SCADA, Lab View, Xilietc

7. Some Projects Developed by Students

1. 3-Tier Secured Web Application for Electricity Boards
2. Recruitment and Assessment Software
3. Digital marketing Mobile Application
4. Software for handling activities of Smart City Applications like Precision Agriculture, healthcare, Vehicular Traffic Management, Water Management, Home Automation etc.
5. Digital Image processing Software
6. Electrical Battery management System
7. Industrial Automation Software
8. Fault identification of Optical Fibre cable
9. Remote Control of Robots for Defense Application
10. Design of Industrial Grade device using CAD/CAM, 3D Printing and other Software's
11. Medicouse Mobile Application
12. Ultimate fruits Mobile Application

ANNEXURE XXVII

AR VR LAB

Objective:

The AR/VR Studio is a dedicated space for the exploration of cutting-edge immersive technologies physically and imaginatively. The AR/VR Studio is equipped with technologies that include various software and design platforms, multiple headset configurations, workstations, and filming resources to help students experiment and create innovations in a wide range of industries.

1. VR Headset: -



The HTC Vive is a virtual reality headset developed by HTC and Valve. The headset uses "room scale" tracking technology, allowing the user to move in 3D space and use motion-tracked handheld controllers to interact with the environment. The Vive headset has a refresh rate of 90 Hz and a 110 degree field of view. The device uses two OLED panels, one per eye, each having a display resolution of 1080×1200 (2160×1200 combined pixels). Safety features include a front-facing camera that allows the user to observe their surroundings without removing their headset. The software can also use the camera to identify any moving or static objects in a room; this functionality can be used as part of a "Chaperone" safety system, which will automatically display a virtual wall or a feed from the camera to safely guide users from obstacles or real-world walls.

2. Interactive Video Wall: -

QHR Series high brightness displays provide 4K UHD non-glare picture quality with up to a billion colors for crystal clear, lifelike color and exceptional detail even in bright ambient light. With an elegant, slim design and a clean cable management solution, these displays are easy to install into any type of environment. Plus, the embedded Magic INFO Player S6 software allows for easy content management and playback, without the need for an external PC. Slim, easy-to-install 4K UHD non-glare high brightness display delivering crystal clear, lifelike color plus intelligent UHD up scaling.



short, augmented reality has the potential to provide learners with contextualized information in real time to enhance learning experiences in the field. Augmented Reality has

i. Learning Tablets: -



Digital Workstation For Groups: -



Augmented reality is the experience of actual environments that is supplemented by digital information in the form of images, sounds, and texts. Setup of HI-Tech. laboratory with state of art facilities and equipments such VR head Sets, Tablet computers & Digital Workstations and Interactive display etc. For example, the Star Walk app for smart phones and tablets allows users to identify over 20 000 objects in the night sky. Sky Walk takes advantage of the built in GPS and Internet connections to provide an interactive 360-degree touch control star map that displays constellations, stars, planets, satellites, and galaxies that are currently overhead.

Engineering labs have used augmented reality to conduct lab practices remotely and in interaction with an actual lab. In

7. AR's connections with Virtual Reality

approached a technical maturity where we can now say it is present in our society and is now becoming a true presence in the modern world. AR will push innovation to new levels and create flexible and functional tools to help society how the public safety sector, military, and the legal field will be influenced by the use of Augmented Reality.

3. Artificial Intelligence

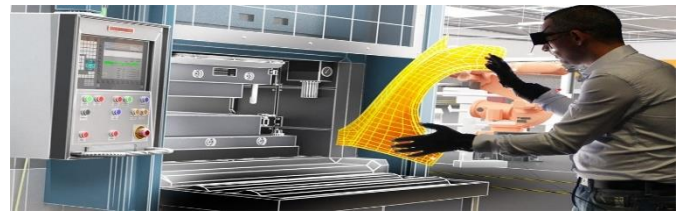


(AI)Artificial Intelligence is very much essential for the operations of augmented reality. AR allows objects to be labeled and identified in the viewer’s visual point of view. Many social media applications that we are using today are developed with a combination of augmented reality and artificial intelligence. For example, on Instagram and Snapchat there are various fun filters like the dog filter, bunny ears, pig filter etc. which are based on consumer-facing applications. These applications do not function unless and until both AI and AR are combined and operated. We are sure going to witness and get to use a lot of more social media applications which have functionalities like image enhancements.

4. NIELIT AR in terms of Teaching and Training

Both the training and teaching technologies belong to the field of education. By using augmented reality technology, any information or data can be passed to a learner in real-time. These systems give a better sense of vision and pull out the objects and hazards which ultimately result in best-practice. Augmented Reality has proved its worth by decreasing the amount of risk and the cost rates in association with training.

Facebook is one of those most used social media applications which beliefs in augmented reality and virtual reality environments. AR vs VR is also a viral topic nowadays. Augmented Reality results in connecting people, socializing them with the help of virtual reality. Both virtual and augmented realities together worked in developing the” conference calls” where users can see one and other and at the same time, they can interact with each other. These conference calls can carry more than 2 people also at the same time. These tools and equipment allow users to witness pin boards and whiteboards. Augmented Reality along with virtual reality together work on design-based documents lay on real-time objects.



8. AR in the Automobile Industry

Autonomous cars may take a few more years’ time to come into reality. Meanwhile, many automobile companies are making use of augmented reality and artificial intelligence technologies to mark their presence in the market. The automobile industry is an advent of augmented reality businesses. Automobile companies are currently working in dashboard-mounted display graphics from around a vehicle with the help of camera footage.

This technology is likely to reduce the occurrence of accidents, as it has the capability of pointing out towards hazards along with the identification of landmarks which are historical along the way. Seeing the results coming from AR technology-based companies, large-scale automobile companies like Tesla, Toyota, Mercedes-Benz, and Volvo have signed contracts to work on this platform. Augmented Reality technology also has the capability to take routes in protecting AR data onto a car’s windshield.

With the help of augmented reality technology, we can also experience the identification of frequently used lanes, navigational prompts, detection of hazards, information about the right direction etc.



ANNEXURE XXVIII

LIBRARY INFRASTRUCTURE

Objectives

Rich library caters to the information needs of the students, researchers and scientists with its well managed information resources housed in two floors spread over an area of 331Sq.Mtr.

The library has a huge collection of books, reference books, periodicals, and electronic resources. The mission to facilitate creation of new knowledge through acquisition, organization and dissemination of knowledge



resources.

Major Library Resources

1. MeitY Library Consortium

The Centre is part of MeitY Library Consortium and has access to an inventory of latest e-Books, Research Papers and e- Journals including **IEEEEXPLORE** among others.

2. National Knowledge Network



The purpose of NKN goes to the very core of the country's quest for building quality institutions.

The Centre is part of NKN and can seamlessly connect at gigabit speed and enables students, scientists, researchers to work together for accessing information to stimulate research and create next generation applications & services in critical and emerging areas.

3. National Digital Library

The Centre is also part of National Digital library of MHRD, India. The students and staff can access and download 6.5 million books in vernacular languages of multiple national and international digital libraries.



4. Rich Collection of Books

The library possesses rich collection of more than 1575 latest books covering subjects such as Electronics, Computer Science, Microcontrollers, Embedded systems, Internet of things, Bioinformatics, Information Security, Precision-Agriculture, Bio technology, Control Engineering, Instrumentation, Networking, Communication, Robotics.

5. Journals, Theses and Periodicals More than 20 National Journals/Magazines of repute are being subscribed by the library.

6. Magazines and News papers

Library is subscribing to all leading newspapers in English, Hindi and Marathi.

Facilities to Students

1. Book Bank

Six books are given to each student per Semester.

2. Book Request

Students can give recommendation for procurement of any Books, Journals and Magazines (Foreign as well as National).The requests are examined and procurement of the same is done.

3. Open Access

Students have unrestricted access to all shelf of books and Journals.

4. Facility for Downloading

There is adequate seating facility besides stack of computers for down loading research papers, e-Journals, e-Books and other reading material.

5. Miscellaneous Services

Reprographic service, Circulation, Curriculum Support for training programs and other User awareness services.



ANNEXURE XXIX Multimedia Lab

Objectives:

The lab is equipped with state-of-art infrastructure and latest/popular software's to build proficiency in students in Multimedia and Animation to cater to the needs of growing Animation and Multimedia Industry. Students are enabled to apply knowledge, techniques, skills of modern multimedia tools in different digital media disciplines like text, images, audio, video and animation (2D & 3D).

Main Software's used

1. CorelDraw



Using Coreldraw practical training is imparted for Large format print designs, Billboards, Complete Branding and Mock-up design presentations, Cards/Letterheads/Brochures/Logo design, Vector based designs, Vinyl designs, Art work for laser/wood/metal cutting & engraving to name a few.

2. Adobe Photoshop



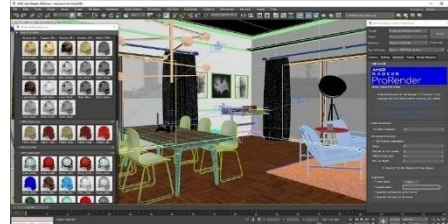
Using Adobe Photoshop image editing, manipulations and photo retouching are taught viz Integrities of creating, enhancing and editing of images, artwork, illustrations. Students are enabled to make Projects simulating a real-life painting or creating an alternative view of the universe. Tools are available for editing individual images as well as batches of photos & various video formats.

3. Adobe flash

Adobe flash for enabling professionals as well as beginners to create animations and interactive content for websites and applications.



4. 3D Max



3ds Max is a computer graphics program used for creating **3D** models, animations, and digital images. The **software** can handle several stages of the animation pipeline including pre-visualization, layout, cameras, modeling, texturing, rigging, animation, VFX, lighting, and rendering.

5. Sound Forge

Developed and optimized by MAGIX this legendary audio



editor stands for innovation and combines the spirit of pioneering ambition with the art of engineering precision. Powerful editing, ultra-fast processing, crystal-clear audio quality and an innovative workflow are many other tools are available.

6. Adobe Premier Pro

Adobe Premier Pro is used for editing videos, commercials and other film, television, and online video. It allows its users to transform raw footage into incredibly amazing video



products. Variety of tools to fine-tune the audio, adjust colors, and do more to create professional-looking results are available

ANNEXURE XXX

Additive Manufacturing / 3D Printing Lab

Objectives

Additive Manufacturing / 3D Printing Lab is equipped with various configurations of 3D Printers, Professional 3D Scanner, also has flagship CAD/CAM software packages and high end CAD/CAM workstations to meet the present industrial requirements.

The lab is aimed at giving exposure and enhancing the knowledge and skills of engineers involved in the operation use of 3D Scanners, 3D Printers, CAD packages and for those who want to provide training to others in this area. It gives exposure and on hand experience in the field of Additive Manufacturing / 3D Printing, 3D scanning and, reverse engineering, Some of the facilities available as follows:

Main Equipment's Available

1. 3D Printer - Mojo



Mojo prints professional-quality models at your desk. It's as simple to use as a document printer, yet powered by FDM Technology to build spot-on, functional concept models and rapid prototypes in ABS plus thermoplastic. Mojo 3D Print Pack equipped with everything designers, engineers or educators need to start 3Dprinting. This 3D printer has an accessory of WaveWash 55 Cleaning System that dissolves and removes the support/waste material from the 3D printed objects to make printed parts ready to use..

1. 3D Printer Assemblies

This Lab also equipped with number of 3D printer assemblies. With the help of these kits students / participants will understand the working and assembling of 3D printers.



Students' versions allow to see the actual processing in the 3D Printer when it prints so that students can gain the knowledge of working of 3D Printers.

3. 3D Scanner

A metrological 3D solution (reverse engineering), perfect for capturing 3D objects for CAD applications and captured images will be transformed to 3D Computer Aided Design (CAD) models that helps in improving the designs without having CAD drawings for the existing products.



4. CAD/CAM Software

Major software packages available for this lab are

- Catia V5
- Creo (Pro/Engineer)
- MasterCAM
- AutoCAD



5. Some of the Practical Project Experiments

1. 3D printing the products of created models from CAD drawings
2. Scanning the parts to obtain 3DCAD models
3. Reverse engineering with the help of 3D Scanner
4. Assembling of 3D Printer kit.

NIELIT AURANGABAD IN NEWS PAPERS

लोकमत समाचार

'नाईलीट' में आईआईटी स्तर प्राप्त करने की क्षमता : गुप्ता

संस्था का स्थापना दिवस मना उत्साह में
औरंगाबाद | 22 फिक्टोर | लोकमत

डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ, इंदूरमध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ (नाईलीट) संस्थाचे स्थापना दिवस मना करण्यात आला. डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ, इंदूरमध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ (नाईलीट) संस्थाचे स्थापना दिवस मना करण्यात आला.



आय.एन.डी.ए. मध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ, इंदूरमध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ (नाईलीट) संस्थाचे स्थापना दिवस मना करण्यात आला.

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Date 23, 2019
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Lokmat Times

Union minister Dhotre pays visit to NIELIT

LOKMAT NEWS NETWORK
AURANGABAD, AUG 31

Union minister of state for electronics and information technology Sanjay Dhotre recently visited the National Institute of Electronics and Information Technology (NIELIT). Dr. Sanjeev Kumar Gupta (executive director, NIELIT) apprised the minister about this unique centre and special training given to MP police, government officials and lecturers of polytechnics.



Union minister of state for electronics and information technology Sanjay Dhotre during his recent visit to NIELIT. Also seen are Dr. Sanjeev Kumar Gupta (executive director, NIELIT) and Marathwada Development Board chairman Dr. Bhagwat Karad.

In his speech, Dhotre said this is a matter of pride that such a regional centre of the western zone has been established in Aurangabad. He said the diligence of the staff working in the centre would definitely take it to new heights. He also promised that all possible help would be extended through the Marathwada Development Board (MDB).

लोकमत



एन.डी.ए. मध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ, इंदूरमध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ (नाईलीट) संस्थाचे स्थापना दिवस मना करण्यात आला.

'नाईलीट' मध्ये आयोजित शिबिरात ७० विद्यार्थ्यांचे रक्तदान

राज्याचे प्रमुख शासनाध्यक्ष अतिरिक्त मुख्यमंत्री अशोक चव्हाण यांच्या मार्फत आयोजित झालेल्या 'नाईलीट' मध्ये आयोजित झालेल्या शिबिरात ७० विद्यार्थ्यांचे रक्तदान करण्यात आले.

'नीलिट' चा औद्योगिक विकासात वाटा

म. टा. प्रतिनिधी, औरंगाबाद

'राष्ट्रीय इलेक्ट्रॉनिक्स आणि माहिती तंत्रज्ञान संस्थेच्या (नीलिट) विभागातील शैक्षणिक, औद्योगिक विकासात वाटा आहे. अशा प्रकारची राष्ट्रीय संस्था या क्षेत्राचा भाग असल्याचा अभिमान आहे.' असे आयोजनात मराठवाडा वैधानिक विकास मंडळाचे अध्यक्ष डॉ. भागवत कराड यांनी संस्थेच्या वर्षाभिनंदनानिमित्त आयोजित कार्यक्रमात केले.



डॉ. भागवत कराड यांनी संस्थेच्या वर्षाभिनंदनानिमित्त आयोजित कार्यक्रमात केले.

संस्थेच्या ३२ व्या वर्षाभिनंदनानिमित्त गुलबर्गा कार्यक्रम झाला. यावेळी व्यासपीठावर कार्यकारी संचालक डॉ. संजीवकुमार गुप्ता यांची प्रमुख उपस्थिती होती. यावेळी डॉ. कराड म्हणाले, 'संस्थेने मराठवाड्यातील विद्यार्थ्यांना उच्च शिक्षणासाठी प्रेरित केले. संस्थेनून विद्यार्थ्यांनी अधिकाधिक ज्ञान प्राप्त केले पाहिजे, त्यांचे

डॉ. कराड यांचे वर्षाभिनंदनानिमित्त प्रतिपादन

व्यवसायिक विकसित केले पाहिजे. माझी भूमिका या भागाच्या शैक्षणिक, आरोग्य, औद्योगिक विकासाकडे आहे. या संस्थेच्या सर्व आगामी

प्रकल्पामुळे संस्था अधिक प्रसिद्ध होऊन भारतीय एक सर्वोत्कृष्ट संस्था होईल. आयआयटीचा दर्जा प्राप्त करण्यासाठी या संस्थेची क्षमता आहे. संस्थेच्या विकासासाठी आपण खांद्याला खांदी लावून उभे राहू. संस्थेच्या विकासासाठी सर्वतोपरी सहकार्य करू' असे आयोजनात त्यांनी दिले. संचालक डॉ. गुप्ता म्हणाले, 'पोलिस, सरासरी सेना, राजकारणी, व्यावसायिक तसेच व्यावसायिकांच्या नियमित शैक्षणिक प्रशिक्षणा वितरित इलेक्ट्रॉनिक्स माहिती तंत्रज्ञानाच्या विविध क्षेत्रातील प्रशिक्षणांमध्ये नायलिटचा महत्त्व सहभाग आहे. पोलिस प्रायव्हेट पार्टनरशिप मॉडेलच्या माध्यमातून संस्था राष्ट्रीय स्तरावर कार्यरत आहे.' याप्रसंगी संस्थेच्या आवादा प्रमुख अतिथींच्या हस्ते वृत्तारोपण करण्यात आले. सशिकुमार मेरा यांनी आभार मानले.

Lokmat Times

Students exhibit talent at NIELIT

LOKMAT NEWS NETWORK
AURANGABAD, MARCH 14

Various projects and the innovative ideas behind them have been showcased at the National Institute of Electronics and Information Technology (NIELIT) Marathwada Development Board (MDB) premises of Dr. Babasaheb Ambedkar Marathwada University (Bamu).



View chancellor B A Chhapde inaugurating NIELIT Marathwada Development Board (MDB) premises of Dr. Babasaheb Ambedkar Marathwada University (Bamu).

Chancellor B A Chhapde inaugurating NIELIT Marathwada Development Board (MDB) premises of Dr. Babasaheb Ambedkar Marathwada University (Bamu).

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Date 01, 2019
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लोकमत समाचार

'नाईलीट' संस्था में विद्यार्थियों के आविष्कारों के दर्शन

औरंगाबाद | 14 मार्च | लोकमत

डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ, इंदूरमध्ये डॉ. वामनाथ अंबेडकर मराठवाड़ा परिषद विद्यापीठ (नाईलीट) संस्थाचे स्थापना दिवस मना करण्यात आला.

संस्थेच्या ३२ व्या वर्षाभिनंदनानिमित्त गुलबर्गा कार्यक्रम झाला. यावेळी व्यासपीठावर कार्यकारी संचालक डॉ. संजीवकुमार गुप्ता यांची प्रमुख उपस्थिती होती. यावेळी डॉ. कराड म्हणाले, 'संस्थेने मराठवाड्यातील विद्यार्थ्यांना उच्च शिक्षणासाठी प्रेरित केले. संस्थेनून विद्यार्थ्यांनी अधिकाधिक ज्ञान प्राप्त केले पाहिजे, त्यांचे

'इलेकोमा' विद्यार्थ्यांच्या कल्पनांना पंख

विविध स्पर्धांनी 'इलेकोमा-२०१८'चा पहिला दिवस गाजला, तीन दिवस महोत्सवाची धूम

म. टा. प्रतिनिधी, औरंगाबाद

सर्वांमध्ये सृजनशीलतेचे प्रेरणादायी वातावरण तयार करणे हे या महोत्सवाचे उद्देश्य आहे. विद्यार्थ्यांच्या कल्पनांना पंख देणे हे या महोत्सवाचे उद्देश्य आहे. विद्यार्थ्यांच्या कल्पनांना पंख देणे हे या महोत्सवाचे उद्देश्य आहे.



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