

OBJECTIVE OF THE CONFERENCE

The objective of this conference is to enable the researchers, academicians and students to present their latest findings and also to provide a forum for the transfer and dissemination of new ideas and technology among all those concerned in the field of VLSI, Computing, signal and image processing.

CALL FOR TECHNICAL PAPERS

VLSI

- Analog & Mixed Signal Circuit Design
- Digital VLSI
- NoC & SoC Design
- Testing of VLSI Circuits
- High Performance Arithmetic Circuits
- Reconfigurable Computing
- VLSI Architecture for High Speed and Low Power
- Software Defined and Cognitive Networks

Computing

- Data mining and Cloud computing
- Architectures for Hardware security, Cryptography and Watermarking
- Mobile Ad-hoc and Sensor Networks
- Cloud Computing
- IoT Applications and Services

Signal and Image Processing

- Adaptive/ Array Signal Processing
- Multimedia Processing
- Biomedical Signal and Image Processing
- Image/ Video Analytics and Multimedia Applications
- Remote Sensing
- Wireless & Mobile Communication

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National
Conference
On
VLSI,
Computing,
Signal and Image Processing
Applications

15th & 16th June 2017

National Institute of Electronics
and Information Technology
Ministry of Electronics & Information
Technology
PB No.5, NIT Campus post,
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ABOUT THE INSTITUTION

National Institute of Electronics and Information Technology (NIELIT) is an autonomous scientific establishment under the administrative control of Ministry of Electronics and Information Technology, (MeitY) Government of India. The institute is a premier organization for education, training, R&D and consultancy in Electronics Design & Information Technology. The basic objective of setting up the centre is to bridge the gap between the requirement of the industry and regular academic qualifiers. NIELIT is also engaged in R & D as well as product development and industrial consultancy services.

ACTIVITIES IN NIELIT

NIELIT Calicut is engaged in R & D Projects, high end training programs like PG Diploma programs in VLSI, ASIC Design and Verification, VLSI Physical Design etc. NIELIT Calicut is included as participating institute in Special Manpower Development Program-Chips to System Design Project along with IITs, NITs and other National institutes. As part of this project VLSI Design group is developing an Array beam former ASIC, a single chip solution for under water acoustic Imaging/SONAR Applications. As part of Make in India initiative by Govt. of India, NIELIT Calicut, VLSI Design Group is developing an Indigenous Ultrasound scanner.

How to reach?

NIELIT Calicut is located very close to NIT Calicut campus and is about 22Kms from the Calicut (Kozhikode) city. A number of buses (Buses to NIT via Kunnamangalam) are available from "Palayam Bus Stand and KSRTC Bus Stand". The bus stop at our Institute is called "Panthrand" and is one stop before NIT.

REGISTRATION FEE

Faculty/ResearchScholars/PGStudents: Rs 2000/-
Demand Draft for registration fee must be drawn in favor of "Executive Director, NIELIT Calicut" payable at Calicut.

PAPER GUIDELINES

Authors are requested to submit the full paper to the following mail "idnielit_nc@calicut.nielit.in". The paper submitted will be peer reviewed and evaluated based on the originality, research content and relevance to the conference. Accepted papers will be published in conference proceedings.

All the papers are accepted only in IEEE format. Extended version of selected papers will be invited for **ANNA University Annexure I/ Annexure II/Scopus indexed and International journals.**

Format: IEEE Standard, Double column format in A4
Sheets limited to 8 pages
only.

Font : Times New Roman, 12 (Size)

SUBMISSION DATES

Last date for submission of full paper	:15.05.2017
Confirmation of acceptance	:30.05.2017
Final submission of accepted paper	:05.06.2017
Last date for registration	:10.06.2017

Contact Address

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National Conference on VLSI, Computing, Signal and Image Processing Applications

15th&16th June 2017

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