

**NATIONAL INSTITUTE OF ELECTRONICS AND
INFORMATION TECHNOLOGY (NIELIT), CALICUT**

(An Autonomous Scientific Society of Ministry of Electronics & Information Technology, Government of India)
NIT Campus PO, CALICUT - 673 601

☎ **0495 2287266; Web: <http://nielit.gov.in/calicut>**

National Institute of Electronics and Information Technology (NIELIT) Calicut is a premier organization engaged in Education, Training, R&D and Consultancy in the areas of Electronics, IT and ITES. We conduct courses both in the formal (M.Techs) and Non-formal (PG Diplomas and customized courses etc.) sectors. We require the following personnel on contract basis.

SENIOR PROJECT ENGINEER – 1 POST

Project	Indigenous Color Doppler Ultrasound Scanner with PNDD Compliance (ICDU)
No. Of Posts	01
Qualification:	<u>Essential</u> : BE/B.Tech in Electronics and Communication
Desirable	PG Diploma in Embedded Systems from a recognized Institute / University
Preferred Skills	GUI Design using Qt/QML, C/C++, MATLAB, Embedded Linux, RTOS, PCI and USB Device Driver Development, ARM Microcontrollers
Experience	2-4 years in the relevant field
Age	Up to 35 Years
Selection Process	Candidates will be selected based on their performance in test/interview.
Duration of engagement	6 months or upto the duration of the project whichever is earlier
Remuneration	Upto ₹30000/- per month (consolidated) based on qualification, experience and performance in the Interview.

Interested candidates may appear for a Walk-in-Interview at our Institute on **14.08.2018** at **09.00 AM sharp**.

Candidates should bring with them all **original documents** in support of their age, qualifications, experience, Identity and address proof etc. along with a copy of **signed Bio-data**. The candidates will not be eligible for any TA for attending test/interview. Candidates who have the relevant qualification required for the above post **only** need to appear for the selection.

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