



### Short Term Courses – NIELIT

Books recommended for reference and reading:

1. A Verilog HDL Primer by J. Bhasker
2. Writing Testbenches using SystemVerilog by Janick Bergeron
3. Verification Methodology Manual by Janick Bergeron
4. SystemVerilog For Verification by Chris Spear

**Group Code:** VLSI

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**Course Code:** PG02

**Course Name:** PG Diploma in ASIC Design and Verification