

## Shortlisted for Interview

As informed by **Tachlog Pvt. Ltd.**, following students are shortlisted for Internship positions:

<b>Sl. No</b>	<b>Candidate Name</b>	<b>Degree</b>	<b>Branch</b>
1	Aishwarya Raju tatkase	M.Tech	VLSI and Embedded System
2	Ananda Padmanabhan	APGD	PD 900
3	Anuj T K	APGD	PD 900
4	Aparna R	M.Tech	Embedded System
5	Jesly K P	M.Tech	Embedded System
6	Jishu M	APGD	PD 900
7	Monisha N	APGD	PD 900
8	Poorva Tekam	M.Tech	VLSI and Embedded System
9	Prinyanka K	M.Tech	VLSI and Embedded System
10	Rahul Gandhi P	M.Tech	Electronic Design and Technology
11	Reema Elizabeth Mammen	M.Tech	VLSI and Embedded System
12	Rohit Kumar	APGD	PD 900
13	Sangeetha Jain	M.Tech	Electronic Design and Technology
14	Shatakshi	M.Tech	VLSI and Embedded System
15	Sofiya E	APGD	PD 900
16	Vikas Mishra	M.Tech	VLSI and Embedded System
17	Vivek Menon M	APGD	PD 900

Personal Interviews of the above students are scheduled on July 12, 2018, 2.00 pm onwards at NIELIT Calicut We wish all of them good luck for next stage of recruitment process.



Assistant Placement Officer  
NIELIT Calicut