<u>Course Name: A Level (1<sup>st</sup> Sem)</u> <u>Topic: ROM</u>

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**<u>ROM</u>**: Read-only memory (ROM) is a type of storage medium that permanently stores data on personal computers (PCs) and other electronic devices. It contains the programming needed to start a PC, which is essential for boot-up; it performs major input/output tasks and holds programs or software instructions.

Because ROM is read-only, it cannot be changed frequently; it is permanent and non-volatile.

## **Classification of ROMs:**

- <u>1.</u> P ROM
- <u>**2.</u> EP ROM</u>**
- 3. EEP ROM
- 4. Masked ROM



**Programmable Read-Only Memory (P ROM):** This form of ROM is initially blank. The user or manufacturer can write data/program on it by using special devices. However, once the program or data is written in PROM chip, it cannot be changed. If there is an error in writing instructions or data in PROM, the error cannot be erased. PROM chip becomes unusable

**Erasable Programmable Read-Only Memory (EP ROM):** This is programmed with the use of very high voltages and exposure to approximately 20 minutes of intense ultraviolet (UV) light.

<u>Electrically-Erasable Programmable Read-Only Memory (EEP ROM)</u>: This is used in many older computer BIOS chips, is non-volatile storage that can be erased and programmed several times and allows only one location at a time to be written or erased. An updated version of EEPROM is flash memory; this allows numerous memory locations to be altered simultaneously.

<u>Masked Read-Only Memory (Masked ROM)</u>: This kind of ROMs is permanent and hence can never be reprogrammed.

**<u>ROM Architecture</u>**: A ROM is a memory of size  $2^{k} \times n$  bits with  $2^{k}$  words each of <u>n-bit</u> size. For proper READ operation, a ROM is equipped with three important connections. These are:

- n-data output lines.
- k-address lines.
- One READ ENABLE.



**<u>Read Operation from ROM</u>**: Read operation from a RAM is accomplished by the following steps:

- 1. Put the address on ADDRESS LINES
- 2. Enable READ ENABLE LINE

## Assignments:

- **<u>1.</u>** Differentiate between RAM and ROM.
- **<u>2.</u>** Draw a clear-cut diagram of ROM architecture.