National Institute of Electronics & Information Technology
Gorakhpur – Extension Centre Lucknow
(Under Ministry of Electronics and Information Technology, Govt. of India)
MMMUT Campus, Deoria Road, Gorakhpur-273010
http://www.nielit.gov.in/gorakhpur/

VLSI Design

VLSI Design using Cadence Tool

2 Weeks Online Course

2 Weeks (2 Hrs. per day)
Timing: - 10:00 AM to 12:00 PM
Medium of Instruction: Bilingual (English & Hindi)

Objective

The course on VLSI Design using CADENCE Tool offered by NIELIT Gorakhpur will introduce the participants to the basics of VLSI Design flow. It will help the participants to understand the concepts through hands-on lab sessions, examples and assignments on CADENCE Tool (in Virtual Lab environment).

Eligibility

B.E. - B.Tech. / B.Sc. - M.Sc. / 3-Years Diploma pursuing or qualified in Electronics or Electrical or Instrumentation or Equivalent

Prerequisite

- ✓ Candidate must have latest computer/laptop with preferably 4 GB RAM or higher and Graphics Card (2 GB)
- ✓ Internet connection with good speed *(preferably 2 Mbps or higher)*

Course Fees

Rs. 1100/- incl. GST & all other charges.

Certificate

Certificate will be provided to the participants, based on minimum 75% attendance and on performance (minimum 50% marks) in the online test, conducted at the end of the course.

Methodology

- ✓ Instructor-led live classes.
- ✓ Instructor-led hands-on lab sessions through **VIRTUAL LABS.**
- ✓ Content Access through e-Learning portal.
- ✓ Assessment and Certification

How to Apply?

Step-1: Read the course structure & course requirements carefully.

Step-2: Visit the Registration portal and click on apply button.

Step-3: Create your login credentials and fill up all the details, see the preview and submit the form.

Step-4: Login with your credentials to verify the mobile number, email ID and then upload the documents, Lock the profile and Pay the Fees online, using ATM-Debit Card / Credit Card / Internet Banking / UPI etc.

Course Content

Day	Topic
Day #01	Introduction to VLSI Design, Historical Perspective, VLSI technology trends, System approach to VLSI Design.
Day #02	Getting started with Virtual Labs, Introduction to EDA tool.
Day #03	Basics of Analog Circuits, Design and Analysis of RC circuits, Integrators & Differentiators, Transient Analysis.
Day #04	Passive Filters, Operational Amplifiers Fundamentals, Design and Analysis.
Day #05	Design and Analysis of Op-Amps contd., Active Filters, Frequency Analysis.
Day #06	Introduction to MOSFETs, CMOS Inverter Fabrication, CMOS Inverter Basics, Voltage Transfer Characteristics.
Day #07	CMOS Inverter DC Analysis, Transistor Sizing, Symbol Generation
Day #08	Designing Combinational circuits, Functional Module Level Design.
Day #09	Timing Analysis and Power Calculation
Day #10	Assignment

Course Coordinator

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