

Annexure – I

NIELIT CHENNAI

Program Details

“Embedded System Design Using Zynq 7000”

Objective	: To provide training on application based Embedded System Design using Zedboard
Target Audience	: Students of Engineering Institutions
Batch Size	: Max 30 Nos
Topics Covered	: 1. Introduction to ZYNQ 7000 SoC 2. Embedded System Design using IP Integrator 3. GPIO Interfacing using AXI (EMIO & MIO) 4. Implementation of UART using IP Integrator 5. Hardware Debugging Using Logic Analyser 6. Communication through SPI Protocol 7. Working with OLED 6. BRAM Application
Duration	: Starting Date: 05-12-2016 Ending Date: 16-01-2017 (Training 100 hrs + Mini Project 20 hrs)
Timing	: 09:00AM to 01:00PM
Venue	: NIELIT CHENNAI
Course fee	: Rs.4,000/- (Inclusive of Service Tax)
Course in-charge	: R.AJAYNIRMAL, Project Associate Email ID : rajay@nielitchennai.edu.in Mobile : 044-24421445/47