

NATIONAL INSTITUTE OF ELECTRONICS & INFORMATION TECHNOLOGY- CHENNAI CENTRE

S&T Staff as on 30.09.2018

S.No	Name of the Employee	Designation	Category [S&T / Non S&T / Academic]	Date of Birth	Level as per 7th CPC /Grade Pay (Rs)	Group	Date of Joining in NIELIT as per Service Book	Date of Joining in the Present Centre	Date of Joining the present post	Category (Gen/SC/ST/OBC)	Whether PWD or Not	Whether Minority or Not	Whether Ex-Service man	Whether recruited on Compassionate Ground	Date of Superannuation	QUALIFICATION	Email Address
1	Sh. Martin K M	Scientist 'E' & Director Incharge	S&T	2/19/1965	(Level 13)	A	20-04-1992 (At Calicut)	1/1/2018	10/27/2009	GEN	NO	Minority (Christian)	NO	NO	2/28/2025	M.Tech & M.Sc Electronics	dir-chennai@nielit.gov.in martin@nielit.gov.in
2	Sh. Krishna Nand Chaturvedi	Scientist 'D'	S & T	2/1/1979	(Level 12)	A	2/15/2017	2/15/2017	2/15/2017	GEN	NO	NO	NO	NO	1/31/2039	M.E. (CSE), B.Tech (IT)	knchaturvedi@nielit.gov.in
3	Sh. Ripunjay Singh	Scientist 'D'	S&T	10/2/1980	(Level 12)	A	01-11-2011 (HQ New Delhi)	5/1/2013	27.12.2017	GEN	NO	NO	NO	NO	10/31/2040	M.Sc Electronics	ripunjay@nielit.gov.in
4	Sh. Sanjeev Kumar Jha	Joint Director (Tech.)	S&T	29.12.1973	(Level 12)	A	03.02.1997 (At Chandigarh)	28.08.2017	30.08.2018	GEN	NO	NO	NO	NO	31.12.2033	Phd (CS&E), M.A (Statistics), B.A (Hons)	skjha@nielit.gov.in
5	Sh. Munivel E	Scientist/Engineer "D"	S&T	18.07.1982	(Level 12)	A	31.12.2008	10.09.2018	30.08.2018	OBC	NO	Mo	No	No	31.07.2042	B.Sc (Computer Science) M.Sc (Computer Science), ME (Computer Science & Engg.)	munivel@nielit.gov.in
Non -S&T Staff																	
1	Sh. Ramachandran N.N	Assistant Director (Admn.)	NON-S&T	5/14/1964	(Level 9)	B	10-07-1992 (At Calicut)	6/19/2017	25/08/2016	GEN	NO	NO	NO	NO	5/31/2024	B.Com	ramachandran@nielit.gov.in
2	Sh. P. Dheenathayalan	Assistant Director (Admn.)	NON-S&T	5/17/1968	(Level 9)	B	01-01-1996 (At HQ New Delhi)	5/27/2015	27/9/2018	GEN	NO	NO	NO	NO	5/31/2028	HSC (12TH)	pdheen@nielit.gov.in