

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान (रा.इ.सू.प्रौ.सं.), कालिकट
**NATIONAL INSTITUTE OF ELECTRONICS AND
INFORMATION TECHNOLOGY (NIELIT), CALICUT**

(An Autonomous Scientific Society of Ministry of Electronics & Information Technology, Government of India)

NIT Campus PO, CALICUT - 673 601

☎ 0495 2287266; Web: <http://nielit.gov.in/calicut>

Requires the following personnel on contract basis.

FACULTY (VLSI/ASIC DESIGN)

Qualification:	PhD / M.E/ M.Tech in the field of Microelectronics/ VLSI/ Electronics Design Technology/ Embedded Systems/ Communication Systems/ Signal Processing/ Applied Electronics
Experience	Minimum one year experience in VLSI CAD Tools/ASIC Backend Design, Linux and any Programming Language. Domain knowledge in Digital Signal processing will be an added advantage.
Remuneration	₹40000/- per month (consolidated) based on the performance in Test/Interview, qualifications and experience.
Age	Up to 35 Years
Selection Process	Candidates will be selected based on their performance in test/interview.
Duration of engagement	One year

Interested candidates may appear for a Walk-in-Interview at our Institute on **19.08.2016** at **09.00 AM sharp.**

Candidates should bring with them all original documents in support of their age, qualifications, experience, Identity and address proof etc. along with a copy of signed Bio-data. The candidates will not be eligible for any TA for attending test/interview. Candidates who have the relevant qualification required for the above post **only** need to appear for the selection.