



राष्ट्रीयइलेक्ट्रॉनिकीएवंसूचनाप्रौद्योगिकीसंस्थान,कालीकट
National Institute of Electronics & Information Technology, Calicut



सत्यमेव जयते

Ministry of Electronics and
Information Technology
Government of India

On-line Courses Prospectus

ISO 9001-2015 Certified

VLSI Design -Timing Advanced

06th July 2020
(2 Hours)

Course Description

This is a live, online, instructor – led course which provides a thorough knowledge about the timing fundamentals of digital circuits. They can name the basic techniques used to improve timing performance of digital circuits. Participants can also relate the theoretical concepts of timing constraints with practical circuits and summarize the necessity of such constraints. By applying the discussed techniques to practical examples, participants will be able to examine their differences so that they can choose and integrate suitable methods to solve problems in real designs.

[Click Here to Register](#)

Who can attend?

Post Graduate Students, under Graduate students, Faculty members and working professionals who are interested in design of Digital circuits, its timing issues and solving those issues.

Target Audience

SPECIALLY DESIGNED FOR RESEARCH SCHOLARS, PHD/M.TECH STUDENTS, PROFESSIONALS FROM EDUCATION & STUDENTS.

Course Content

Session	Topic	Date
1	Introduction to Timing Path Analysis –Different timing Paths in Circuits How to calculate maximum frequency of a circuit?. Practical Examples of maximum frequency calculation of circuits. Solving Setup and Hold violations in circuits. Techniques to improve timing performance, including pipeline, retiming etc.	06 th July 2020 08:00 pm ~10pm (Approx. Timing)

Prerequisite

- Knowledge of Digital Circuits , it is appreciated if you have done our course VLSI Design -Timing Fundamentals - [Click here to register](#)

Fee and important dates

Fee	Rs. 500/- <i>(Including GST)</i>
Last date for registration & payment	02-July 2020
Sharing of Link / passwords & other details	05 th July 2020

Certificate

Certificate to all completed candidates

Minimum Requirements

- Attendees must have a Computer/Mobile phones with a good internet connection.

Faculty Profile

Qualification and experience of the faculty handling the sessions

Shri Jayaraj U Kidav, Scientist D,

He is an Ex IBM, Ex DRDO Employee.

He is having an experience of 18 years in the domain, he is principal investigator of many research and development projects funded by Ministry of Electronics and Information Technology, DRDO etc.

Course Coordinator

Sreejeesh SG

Senior Technical Officer

Mob:9447769756.

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Terms and Conditions

- 1. In case any registered candidate could not attend the online session due to technical issue at their side there will not be any refund of the course fee and the sessions will not be repeated.*
- 2. In case the online course is cancelled /postponed due to some technical issue at*

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Ministry of Electronics & Information Technology,
Govt. of India

